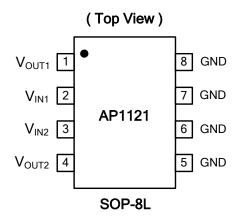


Pin Assignments

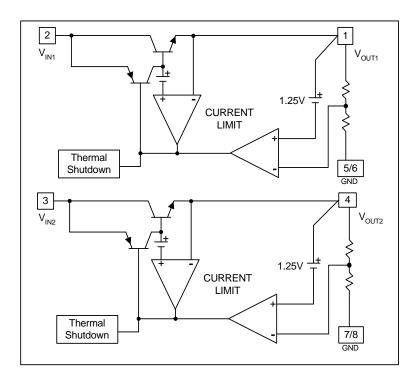


Pin Descriptions

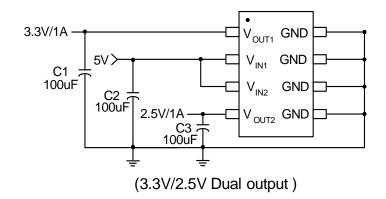
Pin Name	Descriptions
GND	Ground
3.3V(V _{OUT1})	The output of the regulator. A minimum of 10uF capacitor $(0.15\Omega \le ESR \le 20\Omega)$ must be
2.5V/1.8V (V _{OUT2})	connected from this pin to ground to insure stability.
V _{IN}	The input pin of regulator. Typically a large storage capacitor $(0.15\Omega \le \text{ESR} \le 20\Omega)$ is connected from this pin to ground.



Block Diagram



Typical Application Circuit





Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
V _{IN}	DC Supply Voltage	-0.3 to 18 V	V
T _{ST}	Storage Temperature	-65 to +150	°C
T _{OP}	Operating Junction Temperature Range	0 to +125	°C
T _M	Maximum Junction Temperature	150	oC

Electrical Characteristics (Under Operating Conditions)

Parameter Conditions		Min	Тур.	Max	Unit	
	AP1121 V _{OUT1}	$I_{OUT} = 10\text{mA}, T_A = 25^{\circ}\text{C},$ $4.8\text{V} \le \text{V}_{IN} \le 12\text{V}$	3.235	3.300	3.365	V
Output Voltage	AP1121A - V _{OUT2}	$I_{OUT} = 10 \text{mA}, T_A = 25^{\circ}\text{C},$ $4V \le V_{IN} \le 12V$	2.450	2.500	2.550	V
	AP1121B - V _{OUT2}	$I_{OUT} = 10 \text{mA}, T_A = 25^{\circ}\text{C},$ $4V \le V_{IN} \le 12V$	1.764	1.800	1.836	V
Line Regulation	$I_0 = 10 \text{mA}, V_{OUT} + 1.5$			0.2	%	
Load Degulation	AP1121 series V _{OUT1}	$V_{IN} = 5V, 0 \le I_{OUT} \le 1A,$ $T_A = 25^{\circ}C \text{ (Note 3, 4)}$		26	33	mV
Load Regulation	AP1121 series V _{OUT2}	$V_{IN} = 4V$, 0mA <lo<1a, $T_A = 25^{\circ}C$ (Note 4, 5)</lo<1a, 		20	25	mV
Dropout Voltage $(V_{IN}-V_{OUT})$ $I_{OUT} = 1A, \Delta V_{OUT} = 0.1\%V_{OUT}$				1.3	1.4	V
Current Limit			1. 1			Α
Minimum Load Current	0°C≤Tj≤125°C (Note 5)			5	10	mA
Thermal Regulation	T _A = 25 °C, 30ms pulse			0.008	0.04	%/W
Ripple Rejection	$F = 120Hz$, $C_{OUT} = 25uF$ Tantalum, $I_{OUT} = 1A$			60	70	dB
Temperature Stability	I _O = 10mA			0.5		%
$\theta_{\rm JA}$ Thermal Resistance Junction-to-Ambient (No heat sink; No air flow)	(Note 6)	uitry/Power Transistor = PD2		177 158		°C/W
$\begin{array}{ll} & \text{SOP-8L: Control Circuitry/Power Transistor} \\ \theta_{\text{JC}} \text{ Thermal Resistance} & \text{(Note 6)} \\ \text{Junction-to-Case} & \text{CH1 or CH2 only} \\ \text{CH1 & CH2 and PD1 = PD2} \end{array}$				29 19		°C/W

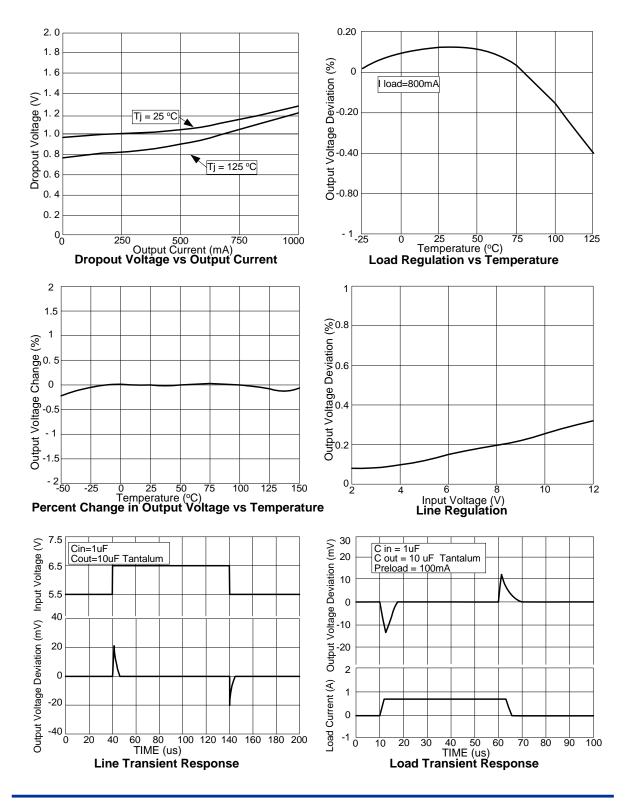
Notes:

- 3. See thermal regulation specifications for changes in output voltage due to heating effects. Line and load regulation are measured at a constant junction temperature by low duty cycle pulse testing. Load regulation is measured at the output lead = 1/18" from the package.
- 4. Line and load regulation are guaranteed up to the maximum power dissipation of 15W. Power dissipation is determined by the input/output differentially and the output current. Guaranteed maximum power dissipation will not be available over the full input/output range.
- 5. Quiescent current is defined as the minimum output current that requires maintaining regulation. At 12V input/output differential the device is
- guaranteed to regulate if the output current is greater than 10mA.

 6. Vout1 and Vout2 are connected to the PCB copper area 5.5mm*5.5mm separately. If you need large PD or lower Tc & Tj, please connect to the large copper area >> 5.5mm*5.5mm (like 10mm*10mm).



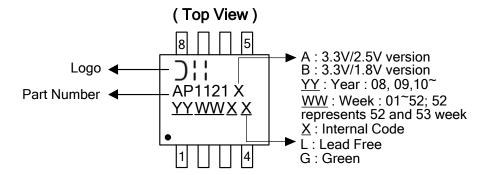
Typical Performance Characteristics





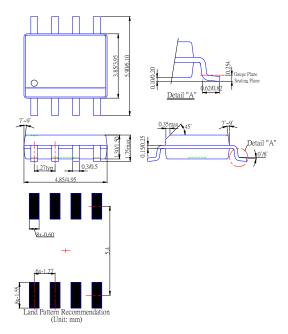
Marking Information

(1) SOP-8L



Package Information

(1) Package type: SOP-8L





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