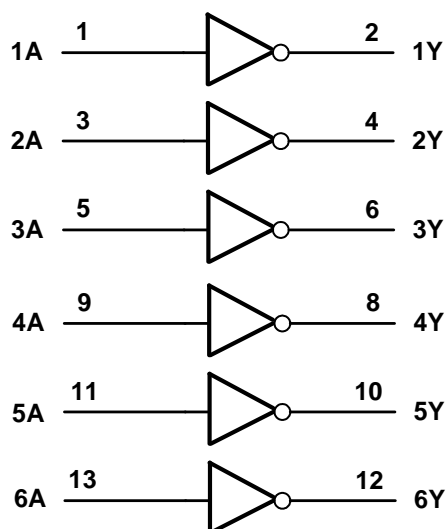


Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	V _{CC}	Supply Voltage

Logic Diagram



Function Table

Input	Output
A	Y
H	L
L	H

Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to +7.0	V
V _I	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current V _I < -0.5V or V _I > V _{CC} +0.5V	±20	mA
I _{OK}	Output Clamp Current V _O < -0.5V or V _O > V _{CC} +0.5V	±20	mA
I _O	Continuous Output Current -0.5V < V _O < V _{CC} +0.5V	±25	mA
I _{CC}	Continuous Current Through V _{CC}	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
T _J	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

- Notes:
- Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
 - Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded.

Recommended Operating Conditions (Note 6) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	Supply Voltage	—	2.0	6.0	V
V _I	Input Voltage	—	0	V _{CC}	V
V _O	Output Voltage	—	0	V _{CC}	V
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 2.0V	—	625	ns/V
		V _{CC} = 4.5V	—	140	
		V _{CC} = 6.0V	—	85	
T _A	Operating Free-Air Temperature	—	-40	+125	°C

- Note:
- Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V _{CC}	T _A = -40°C to +85°C		T _A = -40°C to +125°C		Unit
				Min	Max	Min	Max	
V _{IH}	High-Level Input Voltage	—	2.0V	1.7	—	1.7	—	V
		—	4.5V	3.6	—	3.6	—	
		—	6.0V	4.8	—	4.8	—	
V _{IL}	Low-Level Input Voltage	—	2.0V	—	0.3	—	0.3	V
		—	4.5V	—	0.9	—	0.9	
		—	6.0V	—	1.2	—	1.2	
V _{OH}	High-Level Output Voltage	I _{OH} = -20μA	2.0V	1.8	—	1.9	—	V
		I _{OH} = -20μA	4.5V	4.0	—	4.0	—	
		I _{OH} = -20μA	6.0V	5.5	—	5.5	—	
		I _{OH} = -4.0mA	4.5V	3.84	—	3.7	—	
		I _{OH} = -5.2mA	6.0V	5.34	—	5.2	—	
V _{OL}	Low-Level Output Voltage	I _{OL} = 20μA	2.0V	—	0.2	—	0.2	V
		I _{OL} = 20μA	4.5V	—	0.5	—	0.5	
		I _{OL} = 20μA	6.0V	—	0.5	—	0.5	
		I _{OL} = 4mA	4.5V	—	0.33	—	0.40	
		I _{OL} = 5.2mA	6.0V	—	0.33	—	0.40	
I _I	Input Current	V _I = GND to 5.5V	6.0V	—	±1	—	±1	μA
I _{CC}	Supply Current	V _I = GND or V _{CC} , I _O = 0	6.0V	—	20	—	40	μA

Switching Characteristics

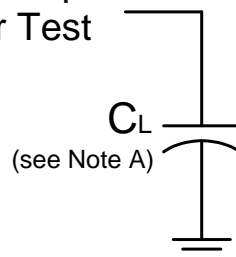
Symbol	Parameter	Test Conditions	V _{CC}	T _A = +25°C			-40°C to +85°C	-40°C to +125°C	Unit
				Min	Typ	Max	Max	Max	
t _{PD}	Propagation Delay A _N to Y _N	Figure 1 C _L = 50pF	2.0V	—	19	70	90	105	ns
			4.5V	—	7	14	18	21	
			6.0V	—	5	12	15	18	
t _t	Transition Time	Figure 1 C _L = 50pF	2.0V	—	19	75	95	110	ns
			4.5V	—	7	15	19	22	
			6.0V	—	6	13	16	19	

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

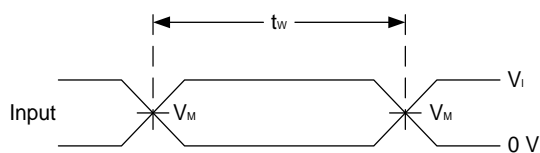
Parameter		Test Conditions	V _{CC} = 6V	Unit
			Typ	
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	10	pF
C _I	Input Capacitance	V _I = V _{CC} – or GND	4	pF

Parameter Measurement Information

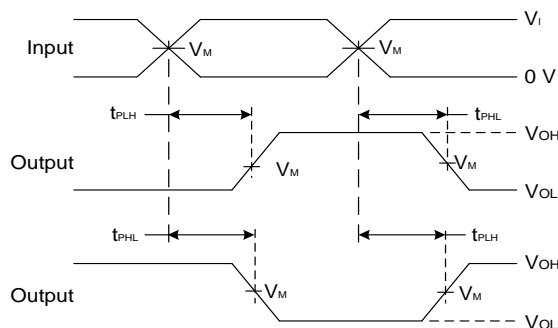
From Output
Under Test



V_{CC}	Inputs		V_M	C_L
	V_I	t_r/t_f		
2.0V to 6.0V	V_{CC}	6ns	$V_{CC}/2$	15pF, 50pF



**Voltage Waveform
Pulse Duration**

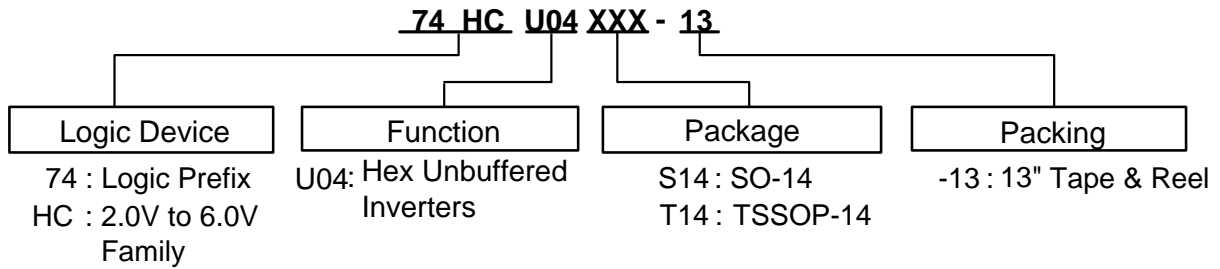


**Voltage Waveform
Propagation Delay Times
Inverting and Non Inverting Outputs**

- Notes:
- A. Includes test lead and test apparatus capacitance.
 - B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
 - C. Inputs are measured separately one transition per measurement.
 - D. t_{PLH} and t_{PHL} are the same as t_{PD} .

Figure 1 Load Circuit and Voltage Waveforms

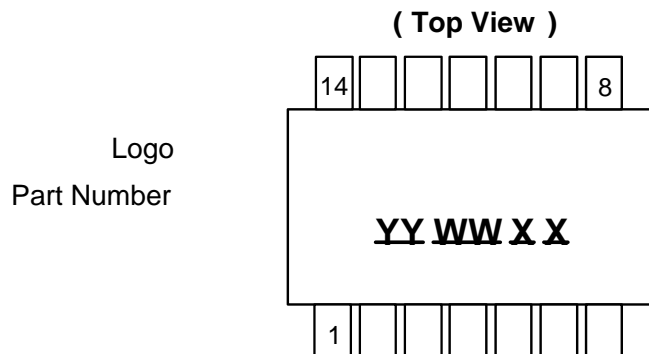
Ordering Information



Device	Package Code	Packaging (Note 7)	7" Tape and Reel	
			Quantity	Part Number Suffix
74HCU04S14-13	S14	SO-14	2500/Tape & Reel	-13
74HCU04T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14

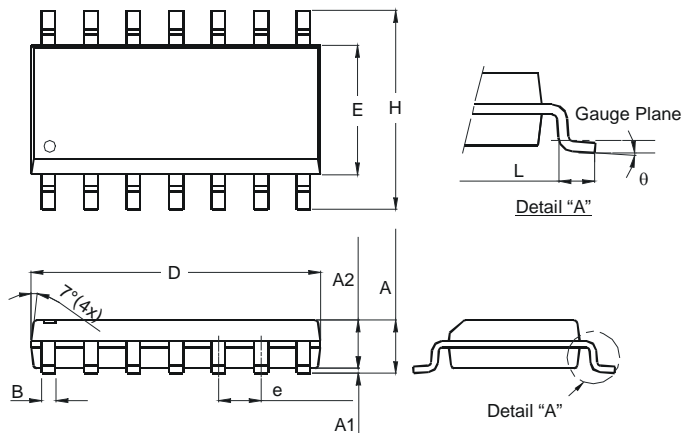


Part Number	Package
74HCU04S14	SO-14
74HCU04T14	TSSOP-14

Package Outline Dimensions (All dimensions in mm.)

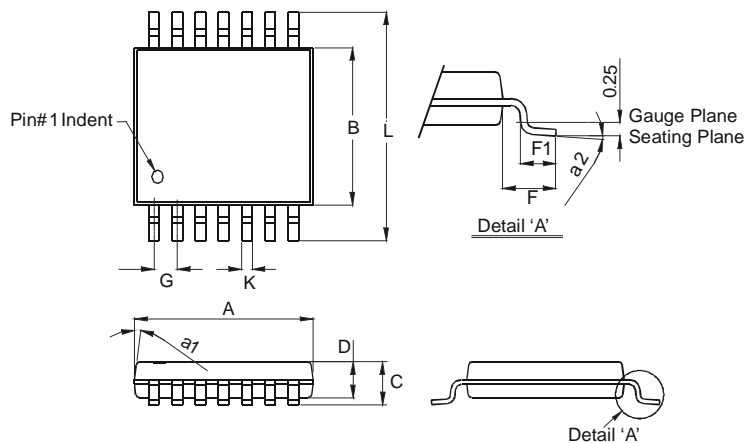
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-14



SO-14		
Dim	Min	Max
A	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
B	0.33	0.51
D	8.53	8.74
E	3.80	3.99
e	1.27 Typ	
H	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

TSSOP-14

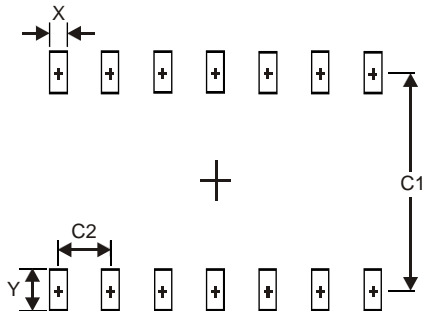


TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
A	4.9	5.10
B	4.30	4.50
C	-	1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		

Suggested Pad Layout

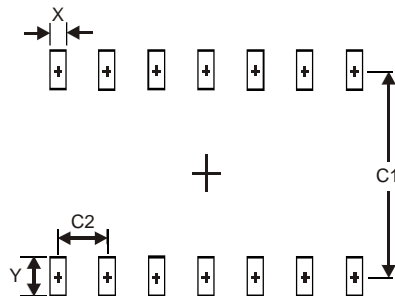
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SO-14



Dimensions	Value (in mm)
X	0.60
Y	1.50
C1	5.4
C2	1.27

TSSOP-14



Dimensions	Value (in mm)
X	0.45
Y	1.45
C1	5.9
C2	0.65

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