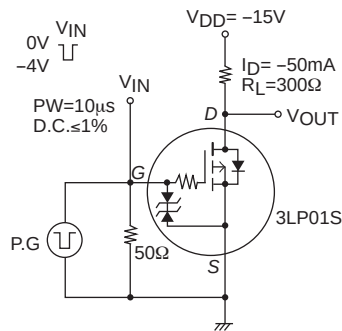


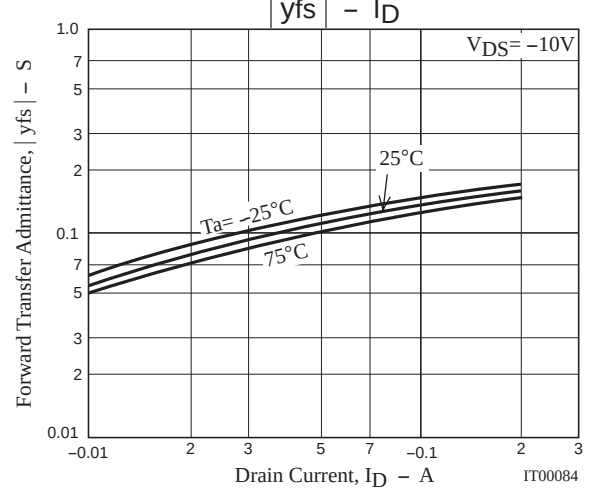
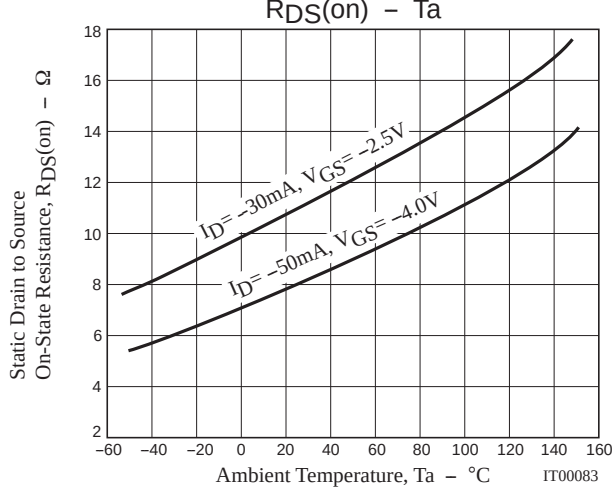
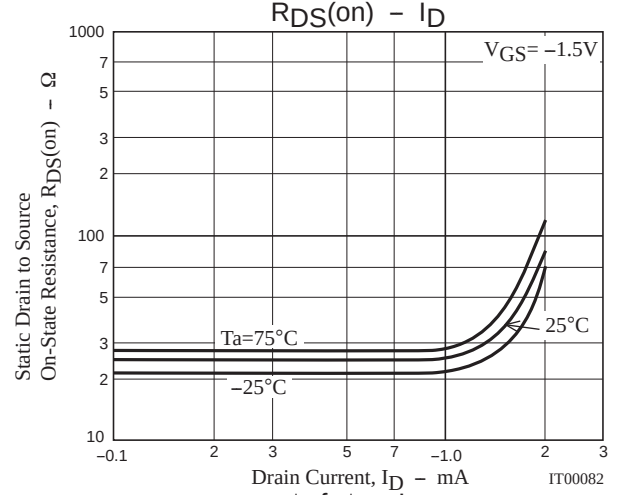
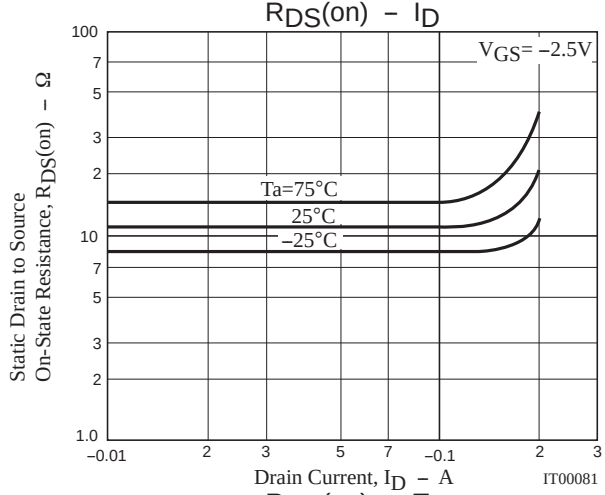
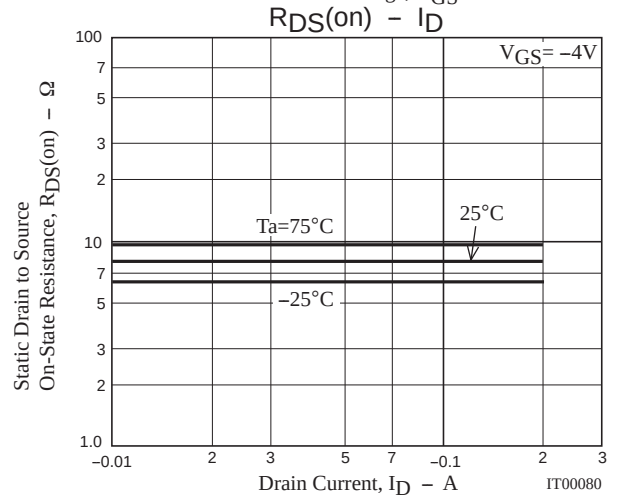
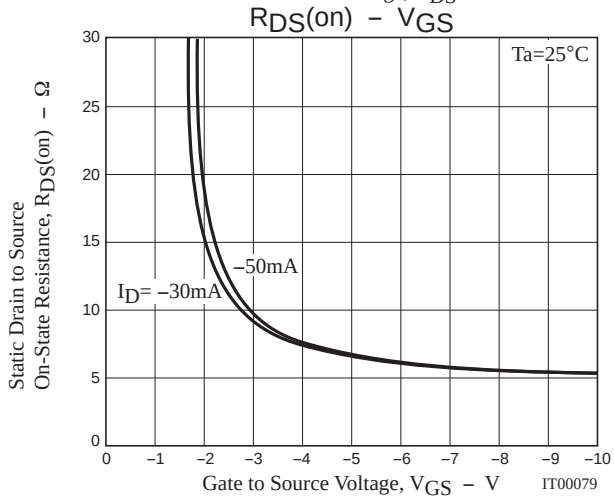
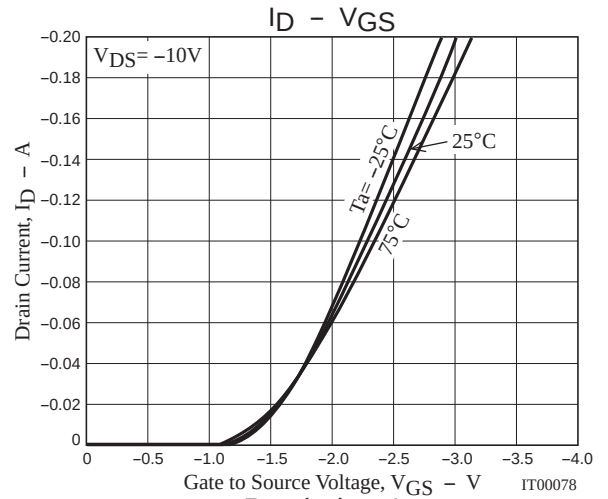
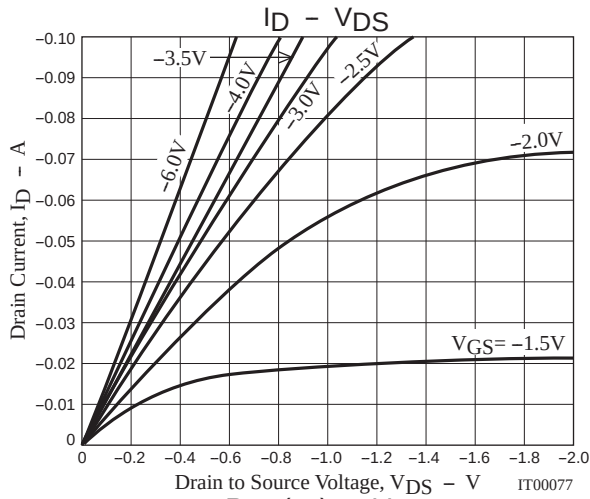
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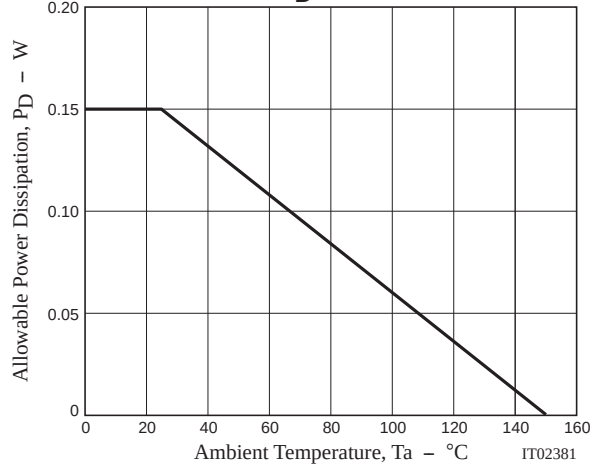
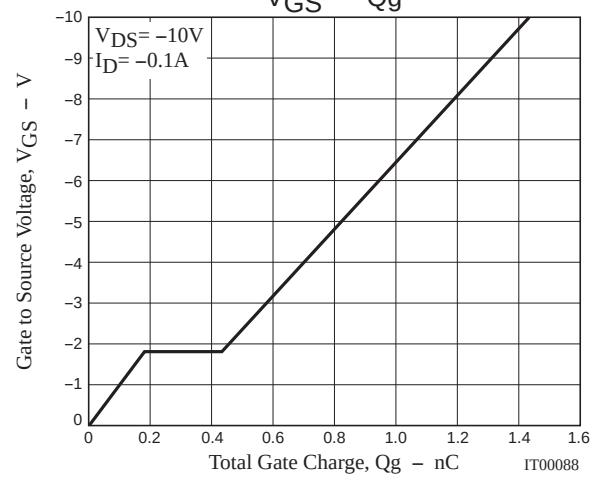
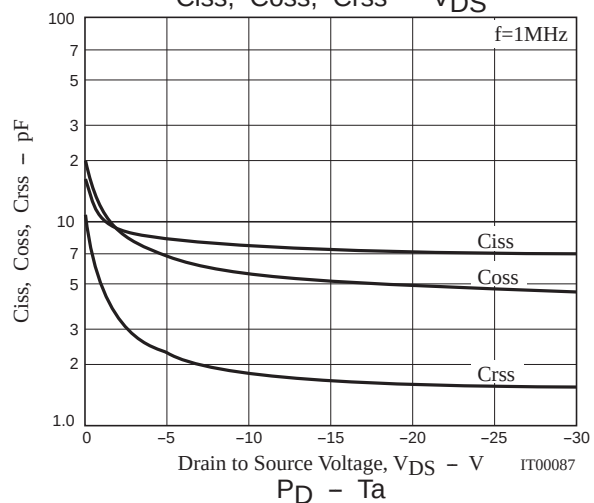
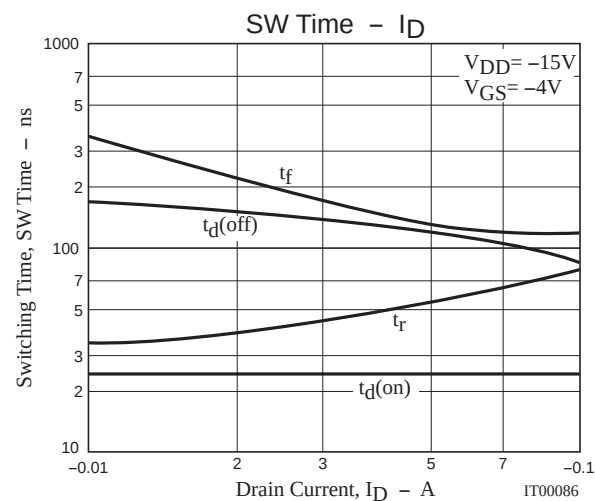
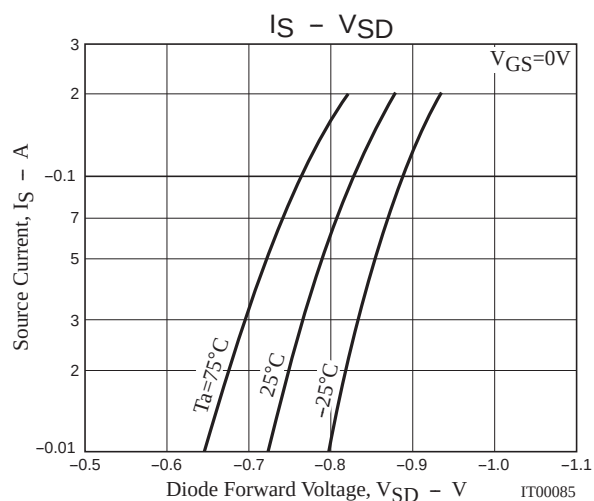
Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = -1mA, V_{GS} = 0V$	-30			V
Zero-Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$			-1	μA
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0V$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V, I_D = -100\mu A$	-0.4		-1.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = -10V, I_D = -50mA$	80	110		mS
Static Drain to Source On-State Resistance	$R_{DS(on)1}$	$I_D = -50mA, V_{GS} = -4V$		8	10.4	Ω
	$R_{DS(on)2}$	$I_D = -30mA, V_{GS} = -2.5V$		11	15.4	Ω
	$R_{DS(on)3}$	$I_D = -1mA, V_{GS} = -1.5V$		27	54	Ω
Input Capacitance	C_{iss}	$V_{DS} = -10V, f = 1MHz$		7.5		pF
Output Capacitance	C_{oss}			5.7		pF
Reverse Transfer Capacitance	C_{rss}			1.8		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		24		ns
Rise Time	t_r			55		ns
Turn-OFF Delay Time	$t_d(off)$			120		ns
Fall Time	t_f			130		ns
Total Gate Charge	Q_g	$V_{DS} = -10V, V_{GS} = -10V, I_D = -100mA$		1.43		nC
Gate to Source Charge	Q_{gs}			0.18		nC
Gate to Drain "Miller" Charge	Q_{gd}			0.25		nC
Diode Forward Voltage	V_{SD}	$I_S = -100mA, V_{GS} = 0V$		-0.83	-1.2	V

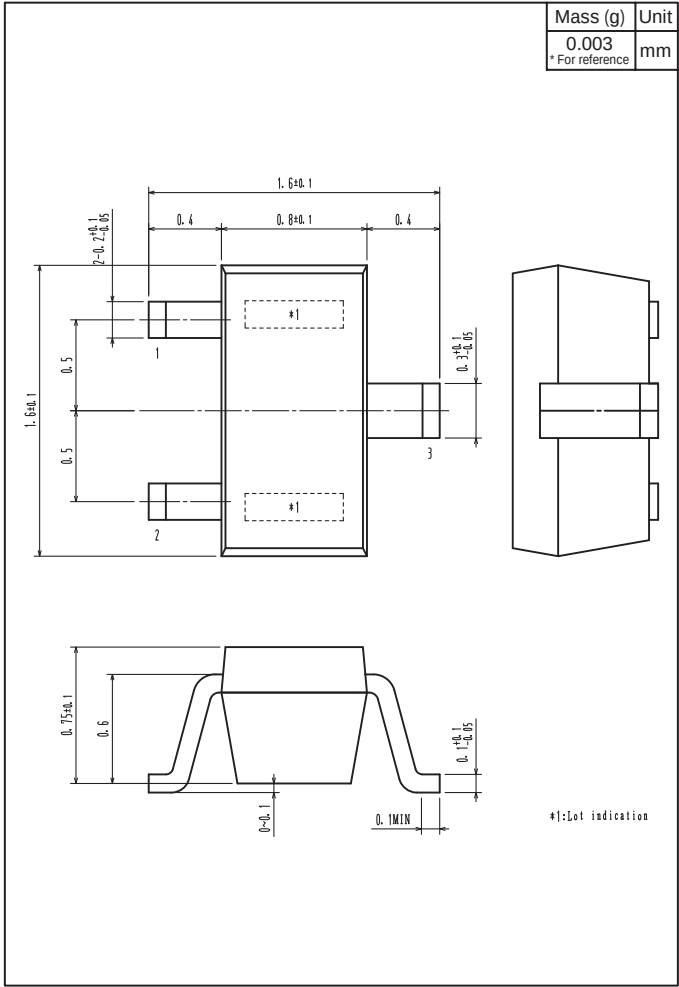
Switching Time Test Circuit



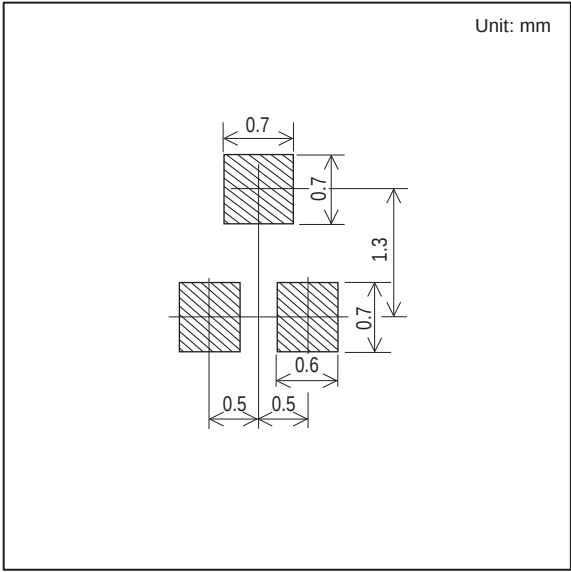




Outline Drawing
3LP01S-TL-E



Land Pattern Example



Note on usage : Since the 3LP01S is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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