

# 1 Characteristics

**Table 2. Absolute ratings (limiting values)**

Symbol	Parameter	Value			Unit
		TN805	TN815	TYNx08	
I <sub>T(RMS)</sub>	On-state rms current (180° conduction angle)	T <sub>c</sub> = 110 °C		8	A
I <sub>T(AV)</sub>	Average on-state current (180° conduction angle)	T <sub>c</sub> = 110 °C		5	A
I <sub>TSM</sub>	Non repetitive surge peak on-state current	t <sub>p</sub> = 8.3 ms	T <sub>j</sub> = 25 °C	73	A
		t <sub>p</sub> = 10 ms		70	
I <sup>2</sup> t	I <sup>2</sup> t value for fusing	t <sub>p</sub> = 10 ms	T <sub>j</sub> = 25 °C	24.5	45
dI/dt	Critical rate of rise of on-state current I <sub>G</sub> = 2 x I <sub>GT</sub> , t <sub>r</sub> ≤ 100 ns	F = 60 Hz	T <sub>j</sub> = 125 °C	50	A/μs
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 125 °C	4	A
P <sub>G(AV)</sub>	Average gate power dissipation		T <sub>j</sub> = 125 °C	1	W
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C
V <sub>RGM</sub>	Maximum peak reverse gate voltage (for TN8xx and TYNx08 only)			5	V

**Table 3. Sensitive electrical characteristics (T<sub>j</sub> = 25 °C, unless otherwise specified)**

Symbol	Test conditions		TS820	Unit
I <sub>GT</sub>	V <sub>D</sub> = 12 V, R <sub>L</sub> = 140 Ω	MAX.	200	μA
V <sub>GT</sub>		MAX.	0.8	V
V <sub>GD</sub>	V <sub>D</sub> = V <sub>DRM</sub> , R <sub>L</sub> = 3.3 kΩ, R <sub>GK</sub> = 220 Ω	T <sub>j</sub> = 125 °C	MIN.	0.1
V <sub>RG</sub>	I <sub>RG</sub> = 10 μA		MIN.	8
I <sub>H</sub>	I <sub>T</sub> = 50 mA, R <sub>GK</sub> = 1 kΩ		MAX.	5
I <sub>L</sub>	I <sub>G</sub> = 1 mA, R <sub>GK</sub> = 1 kΩ		MAX.	6
dV/dt	V <sub>D</sub> = 65% V <sub>DRM</sub> , R <sub>GK</sub> = 220 Ω	T <sub>j</sub> = 125 °C	MIN.	5
V <sub>TM</sub>	I <sub>TM</sub> = 16 A, t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C	MAX.	1.6
V <sub>t0</sub>	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.85
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX.	46
I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub> , R <sub>GK</sub> = 220 Ω	T <sub>j</sub> = 25 °C	MAX.	5
		T <sub>j</sub> = 125 °C		1

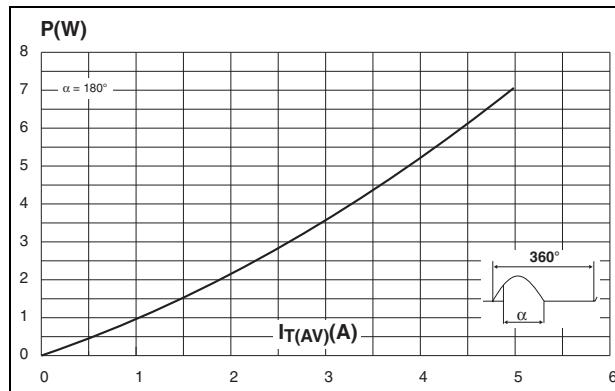
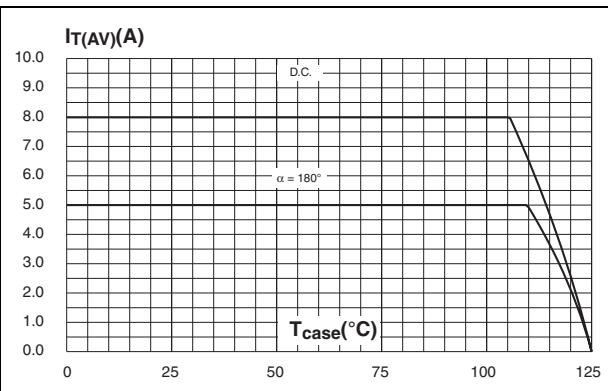
**Table 4. Standard electrical characteristics ( $T_j = 25^\circ\text{C}$ , unless otherwise specified)**

Symbol	Test conditions		TN805	TN815	TYNx08	Unit
$I_{GT}$	$V_D = 12\text{ V}$ , $R_L = 33\Omega$		MIN.	0.5	2	2
			MAX.	5	15	15
$V_{GT}$			MAX.	1.3		V
$V_{GD}$	$V_D = V_{DRM}$ , $R_L = 3.3\text{ k}\Omega$	$T_j = 125^\circ\text{C}$	MIN.	0.2		V
$I_H$	$I_T = 100\text{ mA}$ , gate open		MAX.	25	40	30
$I_L$	$I_G = 1.2 I_{GT}$		MAX.	30	50	70
$dV/dt$	$V_D = 67\% V_{DRM}$ , gate open	$T_j = 125^\circ\text{C}$	MIN.	50	150	150
$V_{TM}$	$I_{TM} = 16\text{ A}$ , $t_p = 380\mu\text{s}$	$T_j = 25^\circ\text{C}$	MAX.	1.6		V
$V_{t0}$	Threshold voltage	$T_j = 125^\circ\text{C}$	MAX.	0.85		V
$R_d$	Dynamic resistance	$T_j = 125^\circ\text{C}$	MAX.	46		$\text{m}\Omega$
$I_{DRM}$ $I_{RRM}$	$V_{DRM} = V_{RRM}$	$T_j = 25^\circ\text{C}$	MAX.	5		$\mu\text{A}$
		$T_j = 125^\circ\text{C}$		2		mA

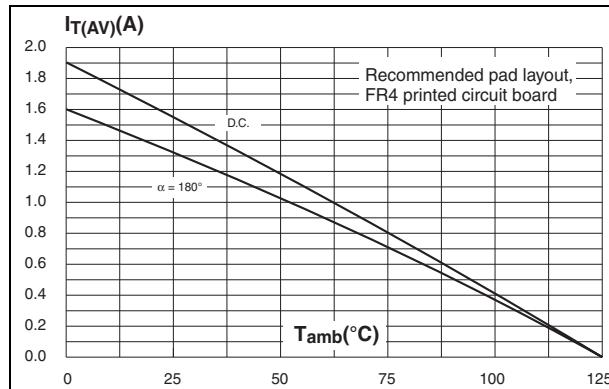
**Table 5. Thermal resistance**

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	Junction to case (DC)	1.3	$^\circ\text{C}/\text{W}$
$R_{th(j-a)}$	Junction to ambient (DC) S <sup>(1)</sup> = 0.5 cm <sup>2</sup>	70	$^\circ\text{C}/\text{W}$
		100	
		60	

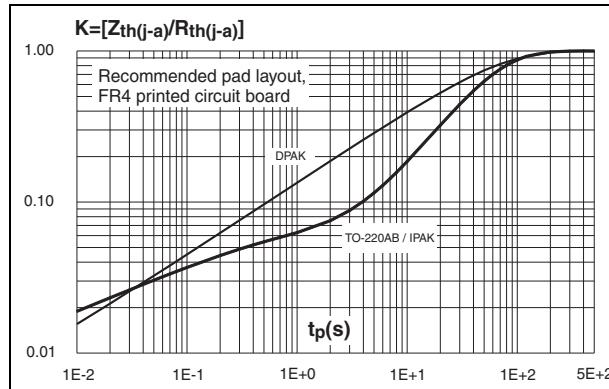
1. S = Copper surface under tab

**Figure 1. Maximum average power dissipation versus average on-state current****Figure 2. Average and DC on-state current versus case temperature**

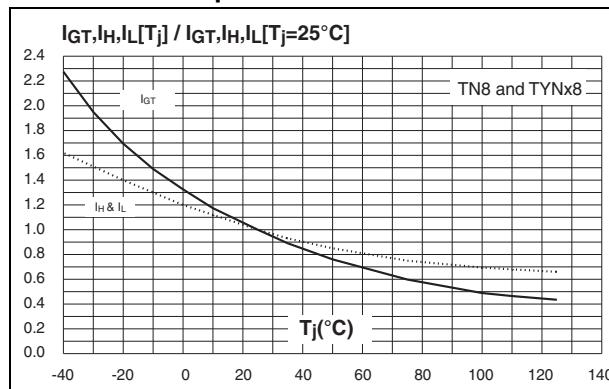
**Figure 3. Average and DC on-state current versus ambient temperature (DPAK)**



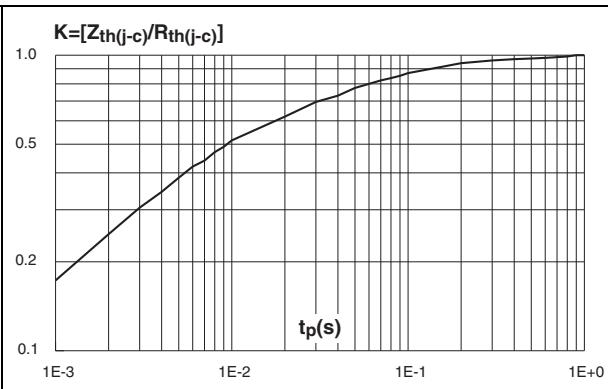
**Figure 5. Relative variation of thermal impedance junction to ambient versus pulse duration (DPAK)**



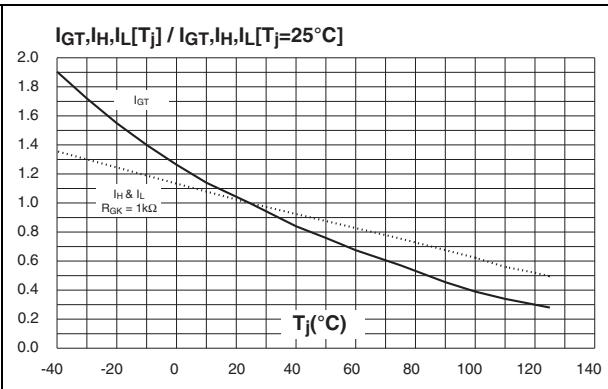
**Figure 7. Relative variation of gate trigger and holding current versus junction temperature**



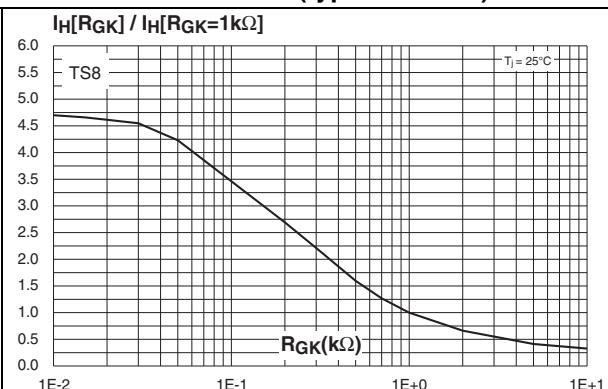
**Figure 4. Relative variation of thermal impedance junction to case versus pulse duration**



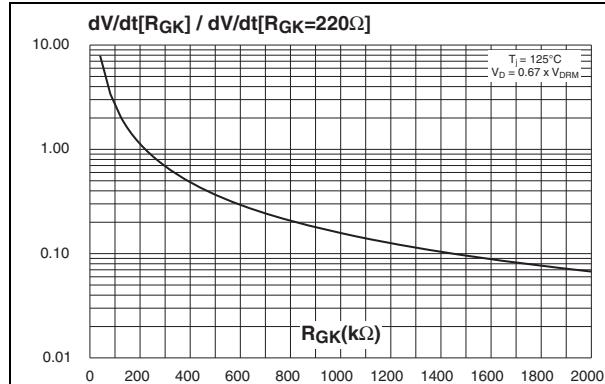
**Figure 6. Relative variation of gate trigger current and holding current versus junction temperature for TS820**



**Figure 8. Relative variation of holding current versus gate-cathode resistance (typical values)**

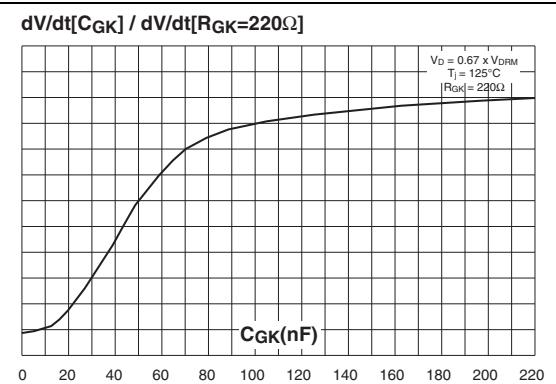


**Figure 9. Relative variation of dV/dt immunity versus gate-cathode resistance (typical values) for TS820**



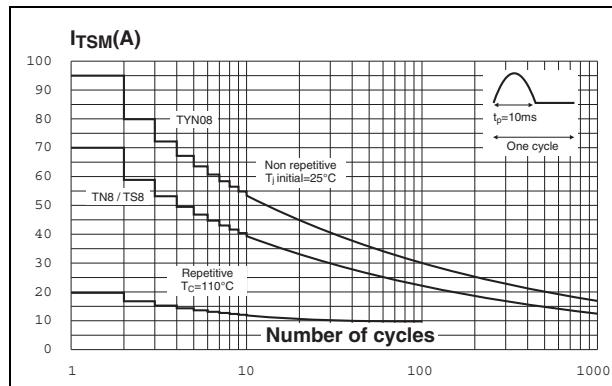
**Figure 11. Surge peak on-state current versus number of cycles**

**Figure 10. Relative variation of dV/dt immunity versus gate-cathode capacitance (typical values) for TS820**

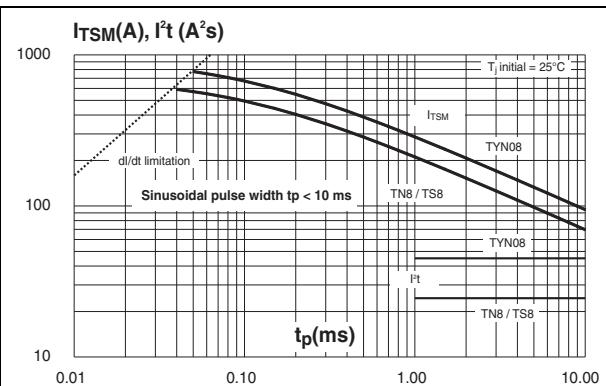


**Figure 11. Surge peak on-state current versus number of cycles**

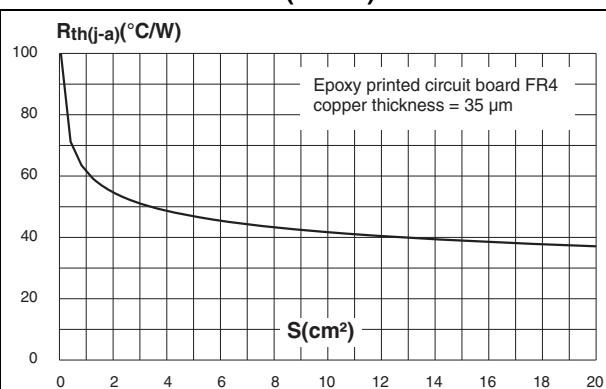
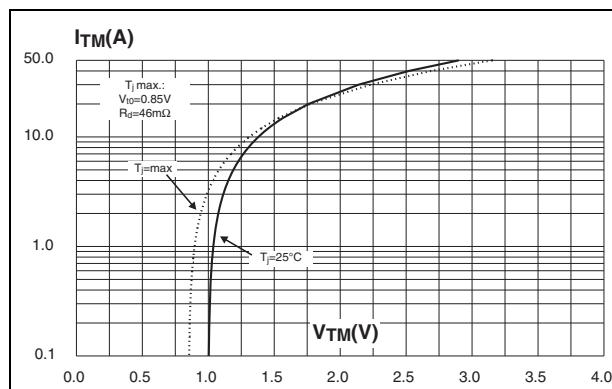
**Figure 12. Non-repetitive surge peak on-state current and corresponding values of  $I^2t$**



**Figure 13. On-state characteristics (maximum values)**



**Figure 14. Thermal resistance junction to ambient versus copper surface under tab (DPAK)**



## 2 Ordering information scheme

Figure 15. TN8 series

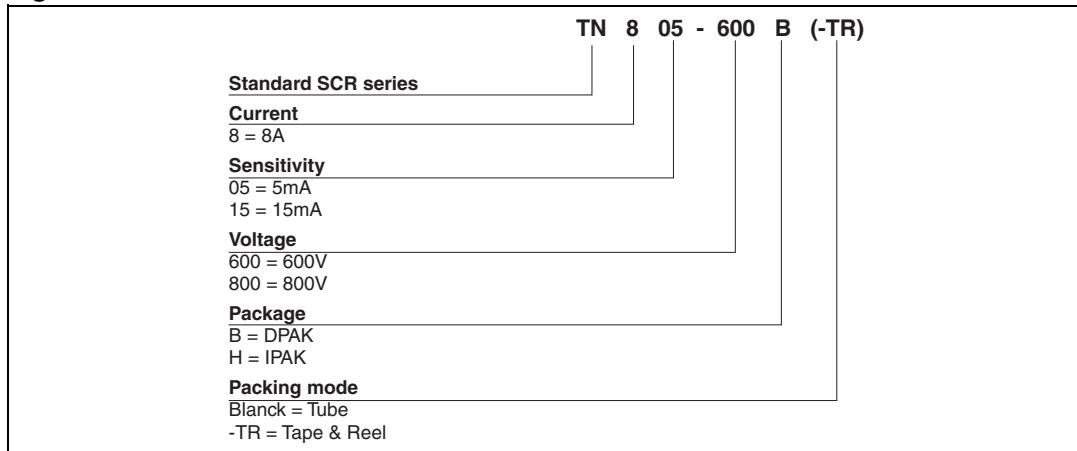


Figure 16. TS8 series

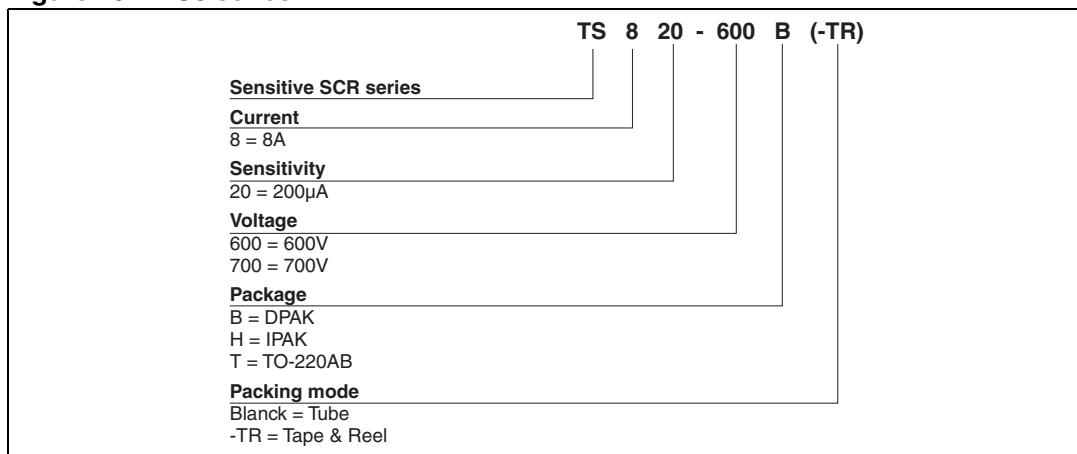
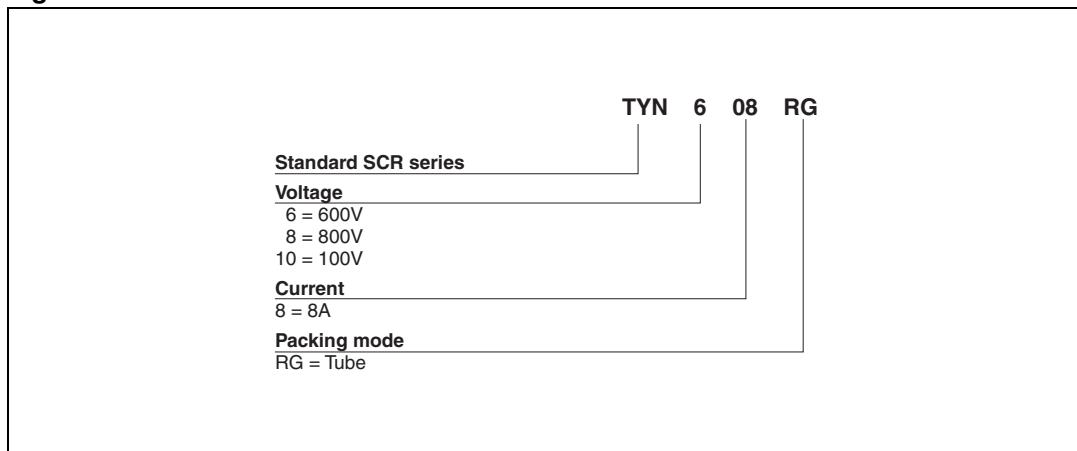


Figure 17. TYNx08 series



### 3 Package information

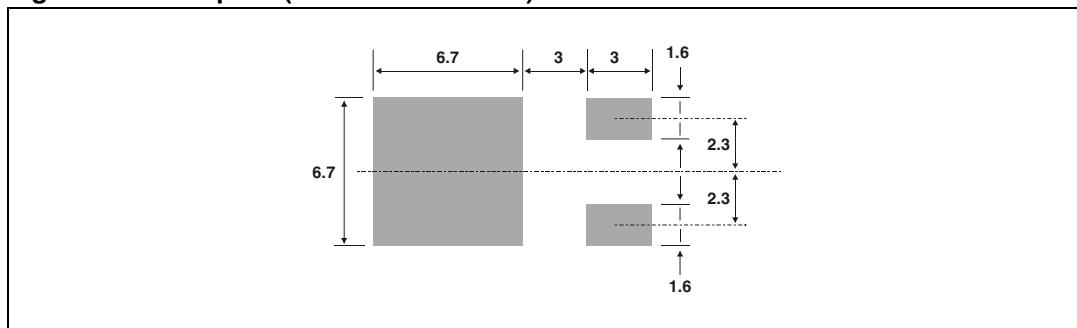
- Epoxy meets UL94, V0
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com).  
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**Table 6. DPAK dimensions**

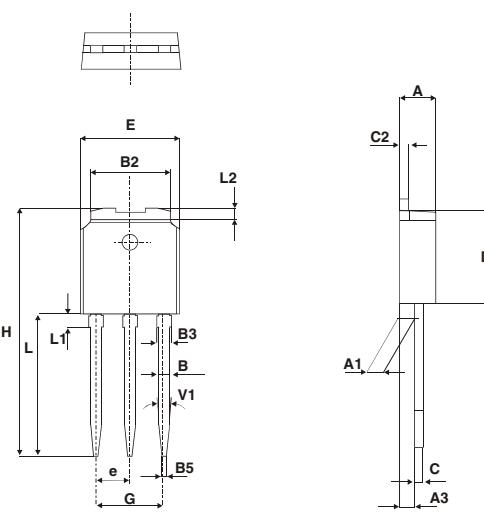
Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.40	0.086	0.094
A1	0.90	1.10	0.035	0.043
A2	0.03	0.23	0.001	0.009
B	0.64	0.90	0.025	0.035
B2	5.20	5.40	0.204	0.212
C	0.45	0.60	0.017	0.023
C2	0.48	0.60	0.018	0.023
D	6.00	6.20	0.236	0.244
E	6.40	6.60	0.251	0.259
G	4.40	4.60	0.173	0.181
H	9.35	10.10	0.368	0.397
L2	0.80 typ.		0.031 typ.	
L4	0.60	1.00	0.023	0.039
V2	0°	8°	0°	8°

**Figure 18. Footprint (dimensions in mm)**



**Table 7. IPAK dimensions**

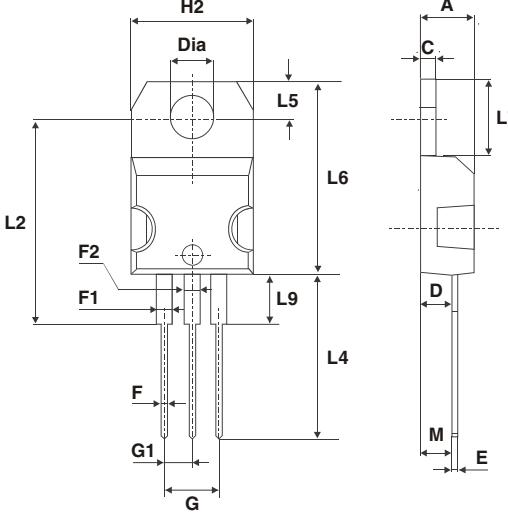
Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20		2.40	0.086		0.094
A1	0.90		1.10	0.035		0.043
A3	0.70		1.30	0.027		0.051
B	0.64		0.90	0.025		0.035
B2	5.20		5.40	0.204		0.212
B3			0.95			0.037
B5		0.30			0.035	
C	0.45		0.60	0.017		0.023
C2	0.48		0.60	0.019		0.023
D	6		6.20	0.236		0.244
E	6.40		6.60	0.252		0.260
e		2.28			0.090	
G	4.40		4.60	0.173		0.181
H		16.10			0.634	
L	9		9.40	0.354		0.370
L1	0.8		1.20	0.031		0.047
L2		0.80	1		0.031	0.039
V1		10°				10°



The technical drawing illustrates the physical dimensions of the IPAK package. The front view shows the overall height H, lead spacing L, lead length L1, lead pitch L2, and lead angle V1. The top view shows the width E, lead width B2, and lead thickness B3. The side view provides a detailed look at the lead profile, including lead height A, lead thickness A1, lead angle A2, lead width C2, lead thickness D, lead angle A3, and lead thickness C. The drawing also indicates the lead thickness B and lead width B5.

Table 8. TO-220AB dimensions (for TS820-xxxT)

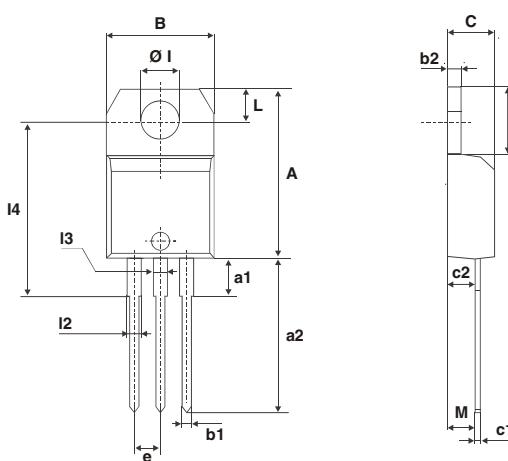
Ref.	Dimensions			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
C	1.23	1.32	0.048	0.051
D	2.40	2.72	0.094	0.107
E	0.49	0.70	0.019	0.027
F	0.61	0.88	0.024	0.034
F1	1.14	1.70	0.044	0.066
F2	1.14	1.70	0.044	0.066
G	4.95	5.15	0.194	0.202
G1	2.40	2.70	0.094	0.106
H2	10	10.40	0.393	0.409
L2	16.4 typ.		0.645 typ.	
L4	13	14	0.511	0.551
L5	2.65	2.95	0.104	0.116
L6	15.25	15.75	0.600	0.620
L7	6.20	6.60	0.244	0.259
L9	3.50	3.93	0.137	0.154
M	2.6 typ.		0.102 typ.	
Diam.	3.75	3.85	0.147	0.151



The technical drawing illustrates the physical dimensions of a TO-220AB package. The front view shows the top surface with lead spacing (L2), lead height (L4), lead thickness (L5), lead length (L6), lead gap (L9), lead width (H2), lead diameter (Dia), and lead pitch (F1, F2). The side view shows the overall height (A), shoulder width (C), shoulder height (D), shoulder thickness (E), and base thickness (M). The total lead length is indicated by L7.

Table 9. TO-220AB dimensions (for TYNx8 series)

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
B	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
C	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
e	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
Øl	3.75		3.85	0.147		0.151
I4	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
I2	1.14		1.70	0.044		0.066
I3	1.14		1.70	0.044		0.066
M		2.60			0.102	



The technical drawing illustrates the TO-220AB package in two views. The top view shows the package from above with dimensions A, B, C, D, E, F, G, H, I1, I2, I3, L, and M. The side cross-section shows the package height I4, lead thickness a1, lead spacing a2, lead width b1, lead pitch e, lead height c1, lead thickness c2, lead spacing b2, lead width C, lead height F, lead thickness M, and lead pitch Øl.

## 4 Ordering information

**Table 10. Ordering information**

Order code <sup>(1)</sup>	Marking <sup>(1)</sup>	Package	Weight	Base qty	Delivery mode
TN805-x00B	TN805x00	DPAK	0.3 g	75	Tube
TN805-x00B-TR	TN805x00	DPAK	0.3 g	2500	Tape and reel
TN805-x00H	TN805x00	IPAK	0.4 g	75	Tube
TN815-x00B	TN815x00	DPAK	0.3 g	75	Tube
TN815-x00B-TR	TN815x00	DPAK	0.3 g	2500	Tape and reel
TN815-x00H	TN815x00	IPAK	0.4 g	75	Tube
TS820-x00B	TS820x00	DPAK	0.3 g	75	Tube
TS820-x00B-TR	TS820x00	DPAK	0.3 g	2500	Tape and reel
TS820-x00H	TS820x00	IPAK	0.4 g	75	Tube
TS820-x00T	TS820x00T	TO-220AB	2.3 g	50	Tube
TYNx08RG	TYNx08	TO-220AB	2.3 g	50	Tube

1. x (6, 7, 8, or 10) depends on voltage

## 5 Revision history

**Table 11. Document revision history**

Date	Revision	Changes
Apr-2002	4A	Last update.
13-Feb-2006	5	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.
22-Jan-2010	6	Alpha definition updated in <a href="#">Figure 1</a> . Thermal resistance, junction to case, updated in <a href="#">Table 5</a> .

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