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Absolute Maximum Ratings, T_A = 25 °C (Unless Otherwise Noted)

Rating			Value	Unit	
	'4P015L1N		±8		
Ponetitive neek off state voltage	'4P020L1N	Vaav	±12		
nepetitive peak off-state voltage	'4P025L1N	■DRM	±16	V	
	'4P035L1N		±24		
Non-repetitive peak impulse current (see Notes 1, 2 and 3)					
8/20 μs (IEC 61000-4-5, 1.2/50 μs voltage, 8/20 current combination wave generator)		I _{PPSM}	±30	A	
10/1000 µs (GR-1089-CORE, 10/1000 µs voltage wave shape)			±18		
Junction temperature		Τ _J	-40 to +150	°C	
Storage temperature range		T _{stg}	-65 to +150	°C	

NOTES: 1. Initially the device must be in thermal equilibrium with $T_J = 25$ °C.

2. The surge may be repeated after the device returns to its initial conditions.

3. Rated currents only apply if pins 1 & 5 (Tip) are connected together and pins 3 & 4 (Ring) are connected together.

Electrical Characteristics, T_A = 25 °C (Unless Otherwise Noted)

Parameter Test Conditions			Min	Тур	Max	Unit	
I _{DRM}	Repetitive peak off-state current	$V_{\rm D} = V_{\rm DRM}$				±1	μA
V _(BO) Breakover		dv/dt = ±250 V/ms, R _{SOURCE} = 300 Ω	'4P015L1N			±15	
	Broakover veltage		'4P020L1N			±20	v
	Breakover voltage		'4P025L1N			±25	
			'4P035L1N			±35	
I _Н		$I_T = \pm 5 \text{ A, } \text{di/dt} = \pm 30 \text{ mA/ms}$	'4P015L1N		±30		
			'4P020L1N		±10		mA
	Holding current		'4P025L1N		±30		
			'4P035L1N		±30		
co	Off state samesitance		'4P015L1N		6.5		
			'4P020L1N		6		
	On-state capacitance	$I = I W \square Z, V_d = I V IIIS, V_D = Z V$	'4P025L1N		5.5	5	p⊢
			'4P035L1N		3.5		
ΔC	Delta-capacitance	$f = 1 \text{ MHz}, V_d = 1 \text{ V rms}, V_D = 1 \text{ V to } V_{DRM}$ $\begin{array}{c} & `4I \\ `4I \\ `4I \end{array}$	'4P015L1N		2		
			'4P020L1N		2.5		nE
			'4P025L1N		3		P
			'4P035L1N		2		

Typical Part Marking



Environmental Specifications

Moisture Sensitivity Leve	11
ESD Classification (HBM)3B

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Parameter Measurement Information



Figure 1. Voltage-Current Characteristic for Tip and Ring Terminals All Measurements are Referenced to the Ring Terminal

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Typical Characteristics



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VDSL Application Examples







Figure 4.



Figure 5.

Recommended PCB Layout



MD-SOT223-5-xxx

Figure 6.

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