

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
 For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	50	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>RM</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	35	V
Average Rectified Output Current	I <sub>O</sub>	20	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	100	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 6)	R <sub>θJA</sub>	30	°C/W
Typical Thermal Resistance Junction to Case (Note 6)	R <sub>θJC</sub>	5	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +125	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop (Note 8)	V <sub>F</sub>	—	—	0.48 0.52	V	I <sub>F</sub> = 10A, T <sub>J</sub> = +25°C I <sub>F</sub> = 20A, T <sub>J</sub> = +25°C
Leakage Current (Note 8)	I <sub>R</sub>	—	—	0.5 100	mA	V <sub>R</sub> = 50V, T <sub>J</sub> = +25°C V <sub>R</sub> = 50V, T <sub>J</sub> = +125°C
Reverse Recovery Time	t <sub>rr</sub>	—	57	—	ns	I <sub>F</sub> = 0.5A, I <sub>R</sub> = 1.0A, I <sub>RR</sub> = 0.25A
Total Capacitance	C <sub>T</sub>	—	400	—	pf	V <sub>R</sub> = 40V, f = 1MHz

- Notes:
- Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
  - Device mounted on FR4 substrate PC board with 10cm\*10cm copper pad.
  - Device mounted on Aluminum substrate PC board with 2-inch sq. copper pad + additional heatsink (Al 48mm\*35mm\*80mm).
  - Short duration pulse test used to minimize self-heating effect.

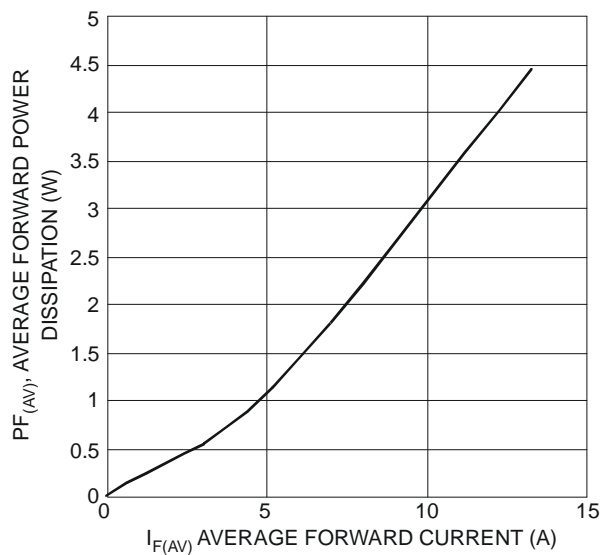


Figure 1 Forward Power Dissipation

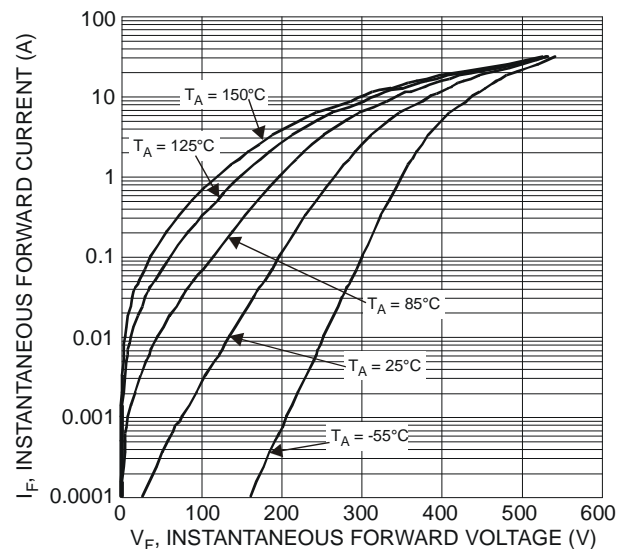


Figure 2 Typical Forward Characteristics

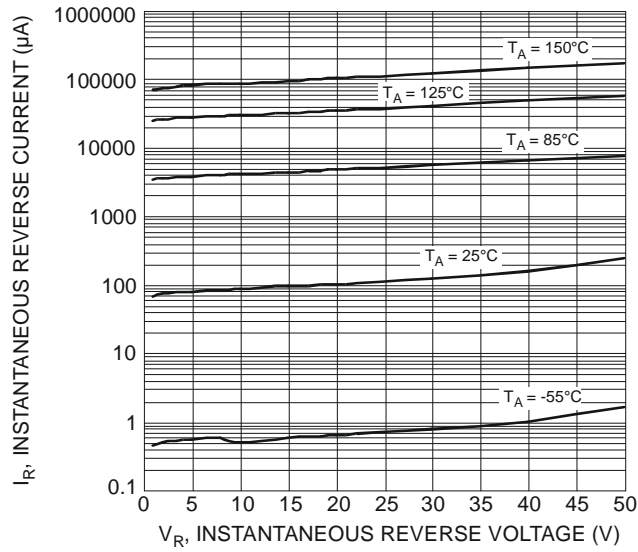


Figure 3 Typical Reverse Characteristics

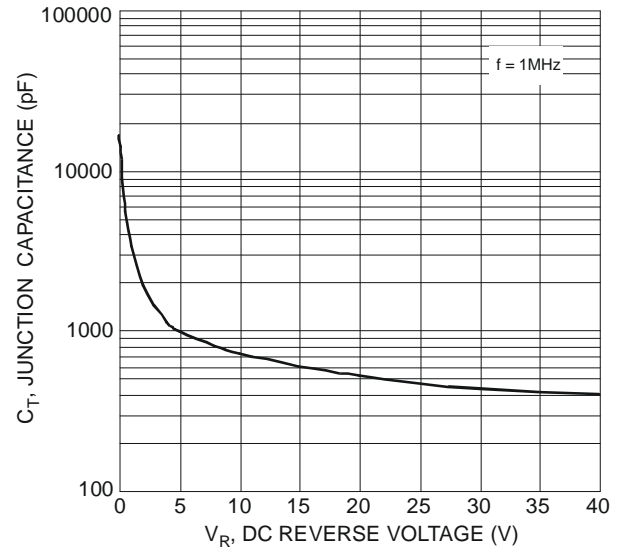


Figure 4 Typical Junction vs. Reverse Voltage

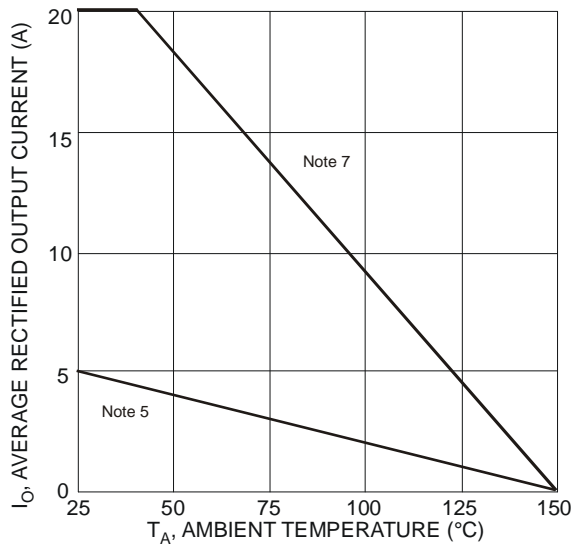


Figure 5 DC Forward Current Derating Curve

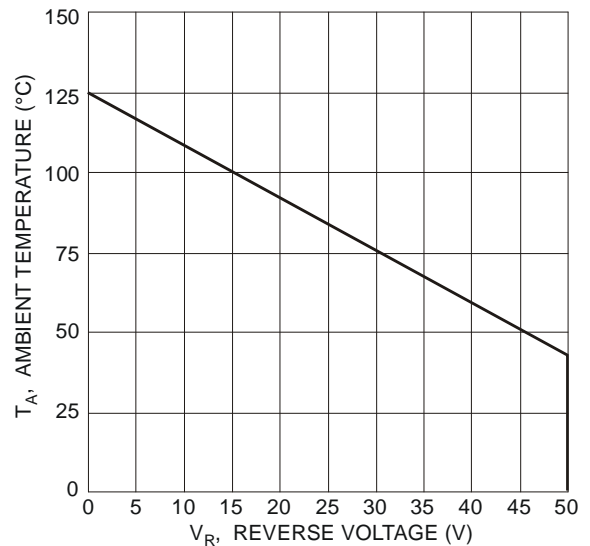
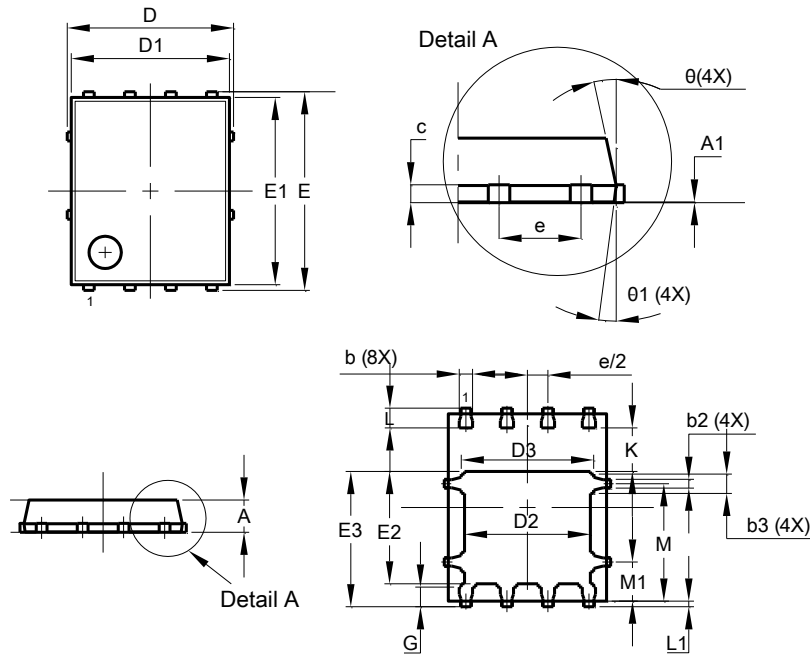


Figure 6 Operating Temperature Derating

## Package Outline Dimensions

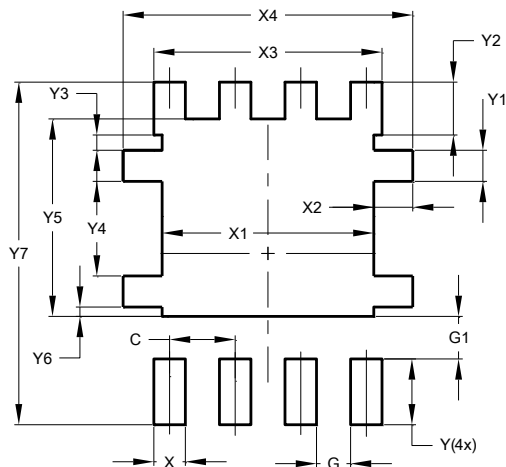
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



POWERDI® 5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	-
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	-	-
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
theta	10°	12°	11°
theta1	6°	8°	7°
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

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