

Absolute Maximum Ratings

| Parameter | | Max. | Units |
|--|------|------|-------|
| Storage Temperature | -65 | 150 | °C |
| Ambient Temperature with Power Applied | -40 | 85 | °C |
| Supply Voltage to Ground Potential | -0.5 | 4.6 | V |
| DC Input Voltage | -0.5 | 5.5 | V |
| DC Output Current | - | 120 | mA |
| Power Dissipation | - | 0.5 | W |

 $Stress\ beyond\ those\ listed\ under\ "Absolute\ Maximum\ Ratings"\ may\ cause\ permanent\ damage\ to\ the\ device.$

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

| Parameters | Description | Test Conditions ⁽¹⁾ | Min | Typ (2) | Max | Units |
|--------------------|-------------------------------------|--|------|---------|------|-------|
| V_{IH} | Input HIGH Voltage | Guaranteed Logic HIGH Level | 2.0 | | | V |
| V _{IL} | Input LOW Voltage | Guaranteed Logic LOW Level | -0.5 | | 0.8 | V |
| I_{IH} | Input HIGH Current | $V_{CC} = Max., V_{IN} = V_{CC}$ | | | ±1 | |
| I_{IL} | Input LOW Current | V _{CC} = Max., V _{IN} = GND | | | ±1 | μΑ |
| I _{OZH} | High Impedance Output Current | $0 \le A, B \le V_{CC}$ | | | ±1 | |
| V _{IK} | Clamp Diode Voltage | V_{CC} = Min., I_{IN} = -18 mA | | -0.73 | -1.2 | V |
| R _{ON} Sw | Switch On Resistance ⁽³⁾ | $V_{\rm CC} = Min., V_{\rm IN} = 0.0 V, I_{\rm ON} = 48 mA$ or $64 mA$ | | 5 | 7 | Ω |
| - | | $V_{CC} = Min$, $V_{IN} = 2.4V$, $I_{ON} = 15mA$ | | 8 | 15 | |

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

| Parameters ⁽⁴⁾ | Description | Test Conditions | Тур | Units |
|---------------------------|-----------------------------|------------------------|------|-------|
| C_{IN} | Input Capacitance | | 3.5 | pF |
| C_{OFF} | A/B Capacitance, Switch Off | $V_{IN} = 0V$ | 5.0 | pF |
| Con | A/B Capacitance, Switch On | | 10.0 | pF |

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.
- 3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
- 4. This parameter is determined by device characterization but is not production tested.

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Power Supply Characteristics

| Parameters | Description | Test Conditions(1) | Min | Typ (2) | Max | Units |
|-----------------|-----------------------------------|---|-----|---------|-----|-------|
| I_{CC} | Quiescent Power Supply Current | $V_{CC} = Max.$ $V_{IN} = GND \text{ or } V_{CC}$ | | 0.5 | 1.0 | A |
| ΔI_{CC} | Supply Current per Input HIGH | $\begin{aligned} V_{\rm CC} &= Max. \\ V_{\rm IN} &= 3.0 V^{(3)} \end{aligned}$ | | | 2.5 | mA |

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V$, +25°C ambient.
- 3. Per TTL driven input (control input only); A and B pins do not contribute to Icc.

Switching Characteristics over 3.3V Operating Range

| | | | Com. | | |
|-----------------------------------|---|--|------|------|-------|
| Parameters | Description | Test Conditions (1) | Min | Max | Units |
| t _{PLH} t _{PHL} | Propagation Delay ^(2,3) Ax to Bx, Bx to Ax | | | 0.25 | |
| t _{PZH} | Bus Enable Time BE to Ax or Bx | $ C_L = 50 \text{ pF} $ $R_L = 500\Omega $ | 1.5 | 6.5 | ns |
| t _{PHZ} | Bus Disable Time BE to Ax or Bx | | 1.5 | 5.5 | |

Switching Characteristics over 2.5V Operating Range

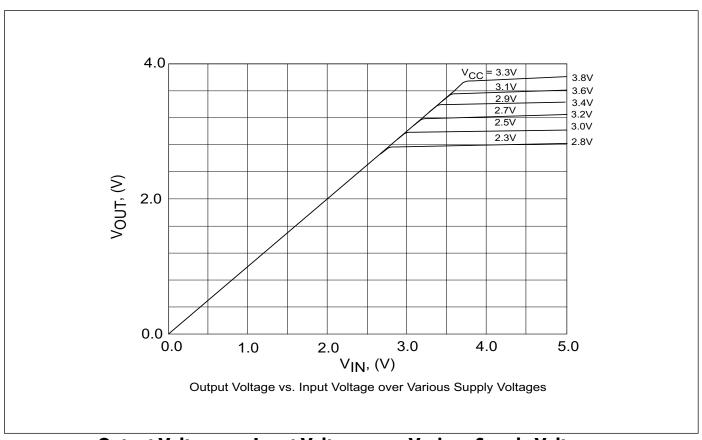
| | | | Com. | | |
|-----------------------------------|---|---|------|------|-------|
| Parameters | Description | Test Conditions (1) | Min | Max | Units |
| t _{PLH} t _{PHL} | Propagation Delay ^(2,3) Ax to Bx, Bx to Ax | | | 0.25 | |
| t _{PZH} t _{PZL} | $\frac{\text{Bus Enable Time}}{\text{BE}} \text{ to Ax or Bx}$ | $C_{L} = 50 \text{ pF}$ $R_{L} = 500\Omega$ | 1.5 | 9.8 | ns |
| t _{PHZ} | $\frac{\text{Bus Disable Time}}{\text{BE} \text{ to Ax or Bx}}$ | | 1.5 | 8.3 | |

Notes:

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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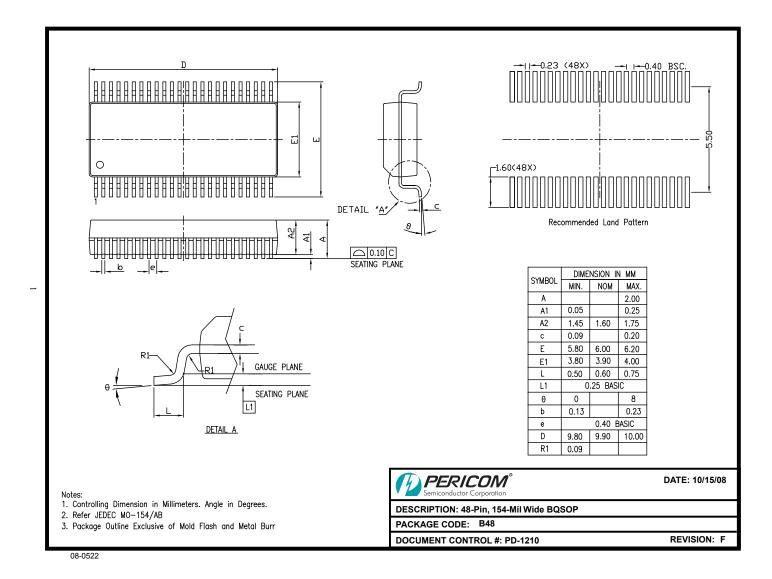




Output Voltage vs. Input Voltage over Various Supply Voltages



Packaging Mechanical: 48-pin BQSOP (B)



Ordering Information

| Ordering Code | Package Code | Package Type |
|---------------|--------------|-------------------------------|
| PI3C32X384BE | В | Pb-free & Green, 48-pin BQSOP |

^{1.} Thermal characteristics can be found on the company web site at www.pericom.com/packaging/

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