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1 Maximum ratings

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 2 Maximum ratings

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Continuous drain current ¹⁾	I_D	-	-	11 7.1	A	$T_C=25^\circ\text{C}$ $T_C=100^\circ\text{C}$
Pulsed drain current ²⁾	$I_{D,pulse}$	-	-	29	A	$T_C=25^\circ\text{C}$
Avalanche energy, single pulse	E_{AS}	-	-	29	mJ	$I_D=1.8\text{A}$; $V_{DD}=50\text{V}$
Avalanche energy, repetitive	E_{AR}	-	-	0.22	mJ	$I_D=1.8\text{A}$; $V_{DD}=50\text{V}$
Avalanche current, repetitive	I_{AR}	-	-	1.8	A	-
MOSFET dv/dt ruggedness	dv/dt	-	-	100	V/ns	$V_{DS}=0$ to 400V
Gate source voltage	V_{GS}	-20 -30	-	20 30	V	static; AC ($f>1\text{ Hz}$)
Power dissipation	P_{tot}	-	-	73	W	$T_C=25^\circ\text{C}$
Operating and storage temperature	T_j, T_{stg}	-55	-	150	$^\circ\text{C}$	-
Mounting torque	-	-	-	60	Ncm	M3 and M3.5 screws
Continuous diode forward current	I_S	-	-	8	A	$T_C=25^\circ\text{C}$
Diode pulse current ²⁾	$I_{S,pulse}$	-	-	29	A	$T_C=25^\circ\text{C}$
Reverse diode dv/dt ³⁾	dv/dt	-	-	1	V/ns	$V_{DS}=0$ to 400V, $I_{SD}\leq 2.2\text{A}$, $T_j=25^\circ\text{C}$
Maximum diode commutation speed ³⁾	di/dt	-	-	50	A/ μs	$V_{DS}=0$ to 400V, $I_{SD}\leq 2.2\text{A}$, $T_j=25^\circ\text{C}$

2 Thermal characteristics

Table 3 Thermal characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Thermal resistance, junction - case	R_{thJC}	-	-	1.7	$^\circ\text{C/W}$	-
Thermal resistance, junction - ambient	R_{thJA}	-	-	62	$^\circ\text{C/W}$	leaded
Thermal resistance, junction - ambient for SMD version	R_{thJA}	-	-	-	$^\circ\text{C/W}$	n.a.
Soldering temperature, wavesoldering only allowed at leads	T_{sold}	-	-	260	$^\circ\text{C}$	1.6 mm (0.063 in.) from case for 10s

¹⁾ Limited by $T_{j,max}$. Maximum duty cycle $D=0.5$

²⁾ Pulse width t_p limited by $T_{j,max}$

³⁾ $V_{DClk}=400\text{V}$; $V_{DS,peak}<V_{(BR)DSS}$; identical low side and high side switch with identical R_G ; $t_{cond}<2\mu\text{s}$

3 Electrical characteristics

at $T_j = 25^\circ\text{C}$, unless otherwise specified

Table 4 Static characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Drain-source breakdown voltage	$V_{(BR)DSS}$	800	-	-	V	$V_{GS}=0V$, $I_D=1mA$
Gate threshold voltage	$V_{GS(th)}$	2.5	3	3.5	V	$V_{DS}=V_{GS}$, $I_D=0.22mA$
Zero gate voltage drain current	I_{DSS}	-	-	1	μA	$V_{DS}=800V$, $V_{GS}=0V$, $T_j=25^\circ\text{C}$ $V_{DS}=800V$, $V_{GS}=0V$, $T_j=150^\circ\text{C}$
Gate-source leakage current incl. zener diode	I_{GSS}	-	-	1	μA	$V_{GS}=20V$, $V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	0.38 0.99	0.45 -	Ω	$V_{GS}=10V$, $I_D=4.5A$, $T_j=25^\circ\text{C}$ $V_{GS}=10V$, $I_D=4.5A$, $T_j=150^\circ\text{C}$
Gate resistance	R_G	-	1	-	Ω	$f=250kHz$, open drain

Table 5 Dynamic characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Input capacitance	C_{iss}	-	770	-	pF	$V_{GS}=0V$, $V_{DS}=500V$, $f=250kHz$
Output capacitance	C_{oss}	-	14	-	pF	$V_{GS}=0V$, $V_{DS}=500V$, $f=250kHz$
Effective output capacitance, energy related ¹⁾	$C_{o(er)}$	-	24	-	pF	$V_{GS}=0V$, $V_{DS}=0$ to $500V$
Effective output capacitance, time related ²⁾	$C_{o(tr)}$	-	305	-	pF	$I_D=\text{constant}$, $V_{GS}=0V$, $V_{DS}=0$ to $500V$
Turn-on delay time	$t_{d(on)}$	-	10	-	ns	$V_{DD}=400V$, $V_{GS}=13V$, $I_D=4.5A$, $R_G=7.5\Omega$
Rise time	t_r	-	6	-	ns	$V_{DD}=400V$, $V_{GS}=13V$, $I_D=4.5A$, $R_G=7.5\Omega$
Turn-off delay time	$t_{d(off)}$	-	40	-	ns	$V_{DD}=400V$, $V_{GS}=13V$, $I_D=4.5A$, $R_G=7.5\Omega$
Fall time	t_f	-	10	-	ns	$V_{DD}=400V$, $V_{GS}=13V$, $I_D=4.5A$, $R_G=7.5\Omega$

Table 6 Gate charge characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Gate to source charge	Q_{gs}	-	4	-	nC	$V_{DD}=640V$, $I_D=4.5A$, $V_{GS}=0$ to $10V$
Gate to drain charge	Q_{gd}	-	9	-	nC	$V_{DD}=640V$, $I_D=4.5A$, $V_{GS}=0$ to $10V$
Gate charge total	Q_g	-	24	-	nC	$V_{DD}=640V$, $I_D=4.5A$, $V_{GS}=0$ to $10V$
Gate plateau voltage	$V_{plateau}$	-	4.5	-	V	$V_{DD}=640V$, $I_D=4.5A$, $V_{GS}=0$ to $10V$

¹⁾ $C_{o(er)}$ is a fixed capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0 to 500V

²⁾ $C_{o(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 to 500V

Table 7 Reverse diode characteristics

Parameter	Symbol	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
Diode forward voltage	V_{SD}	-	0.9	-	V	$V_{GS}=0V, I_F=4.5A, T_f=25^{\circ}C$
Reverse recovery time	t_{rr}	-	1000	-	ns	$V_R=400V, I_F=2.2A, di_F/dt=50A/\mu s$
Reverse recovery charge	Q_{rr}	-	11	-	μC	$V_R=400V, I_F=2.2A, di_F/dt=50A/\mu s$
Peak reverse recovery current	I_{rrm}	-	17	-	A	$V_R=400V, I_F=2.2A, di_F/dt=50A/\mu s$

4 Electrical characteristics diagrams

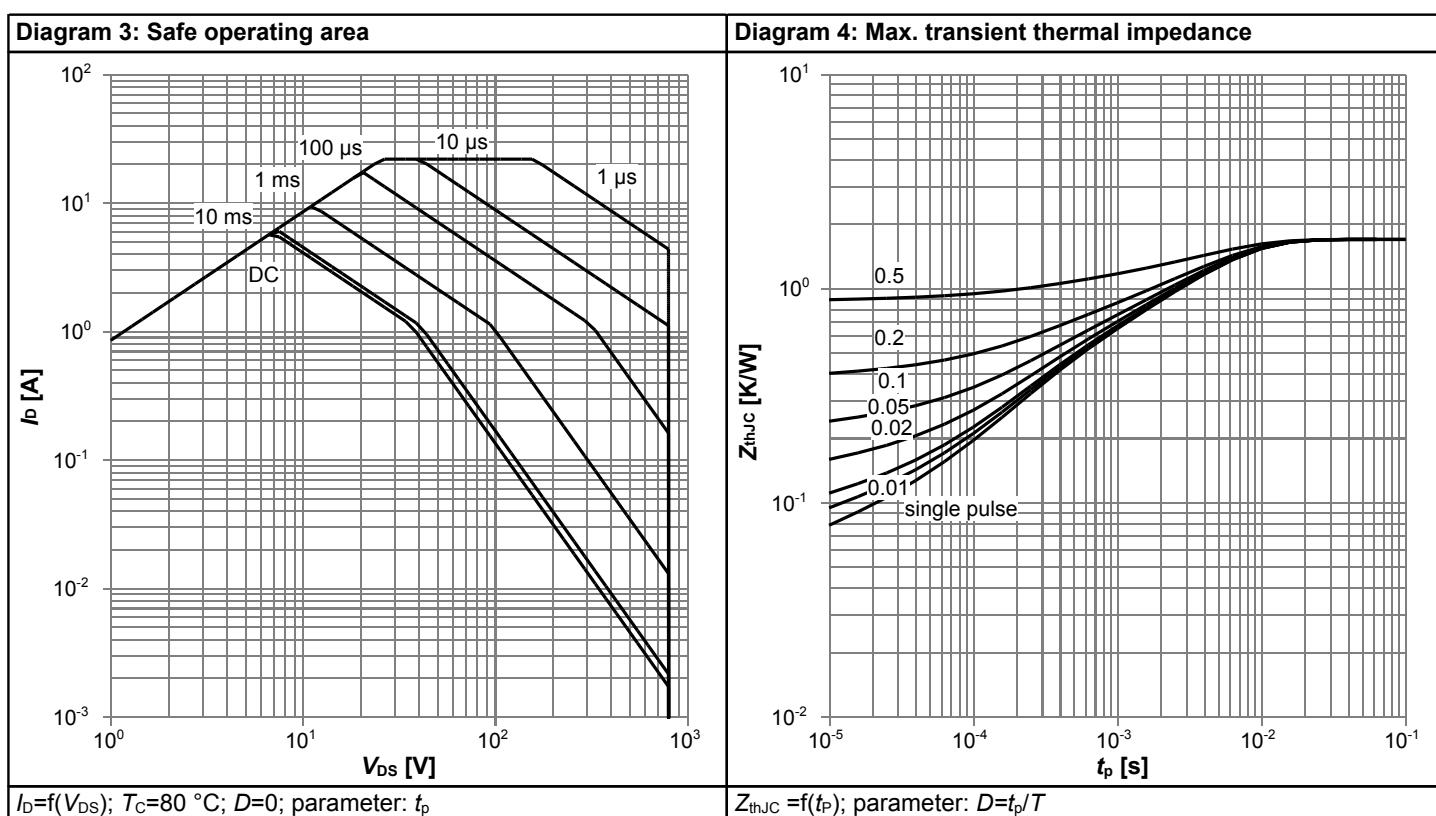
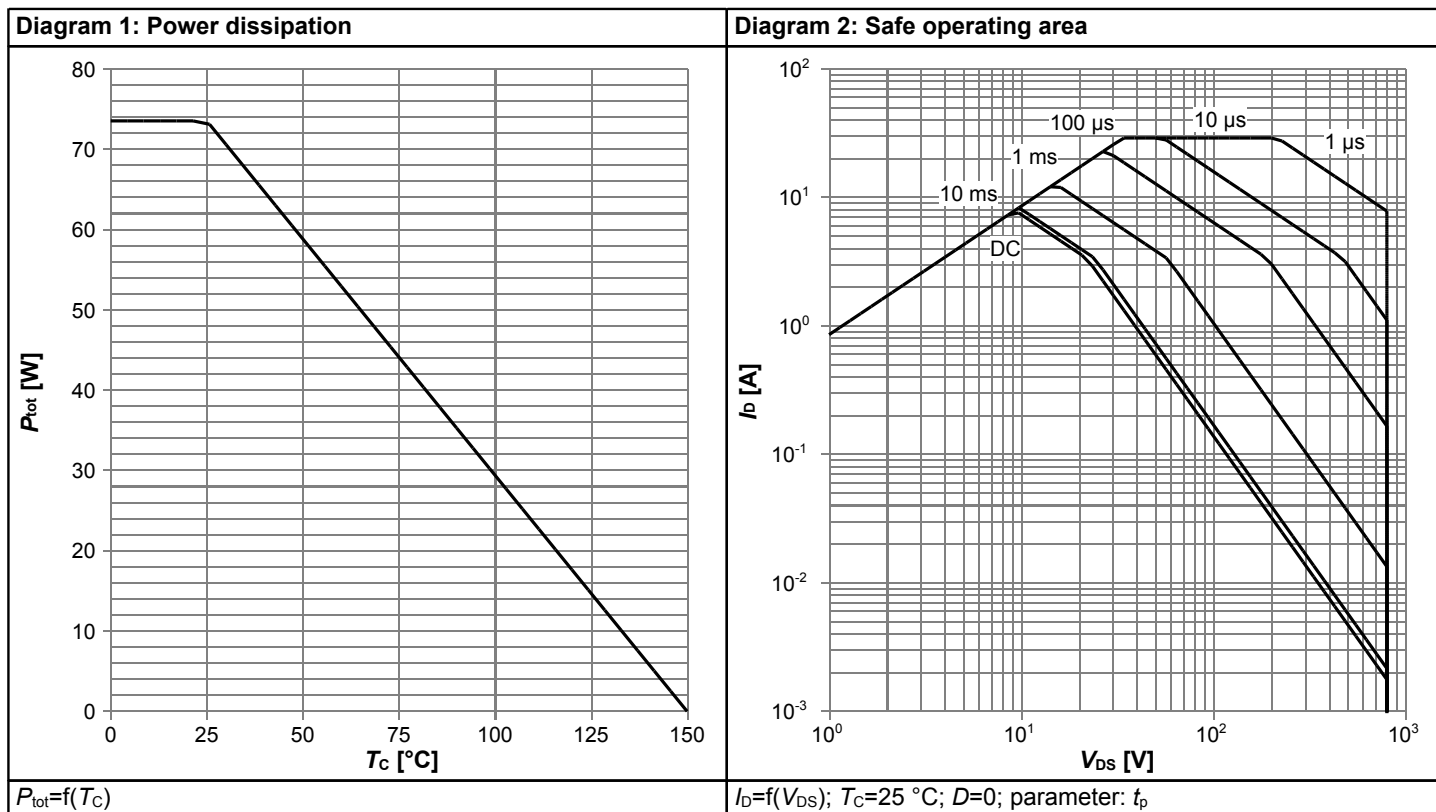
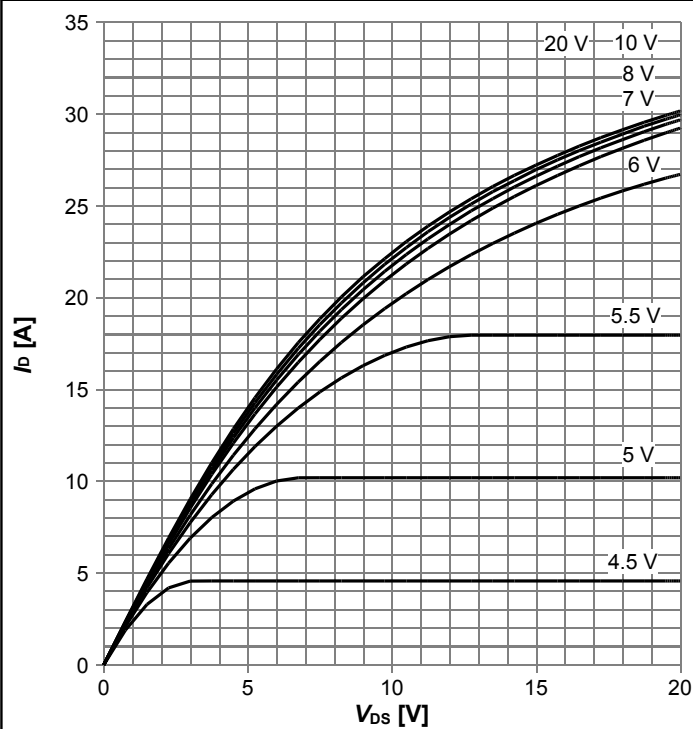
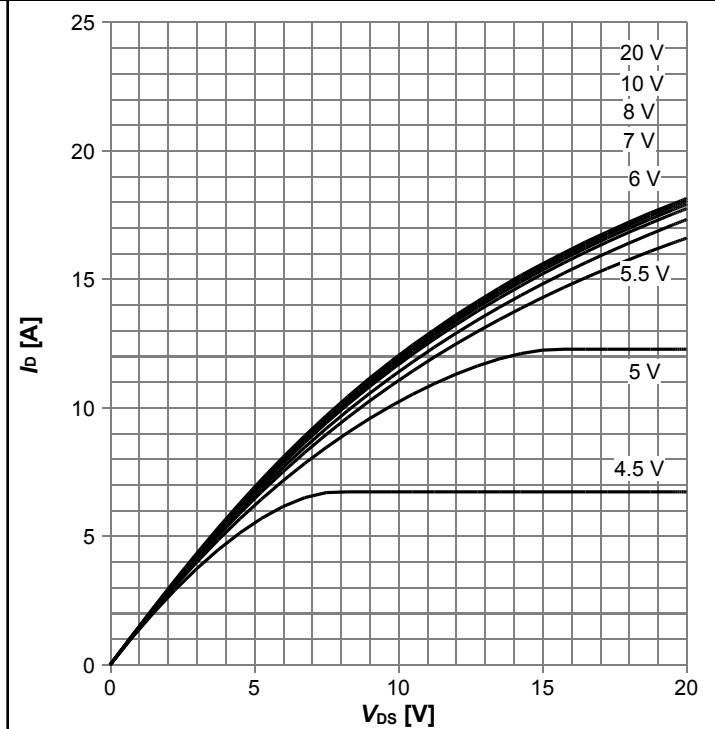


Diagram 5: Typ. output characteristics



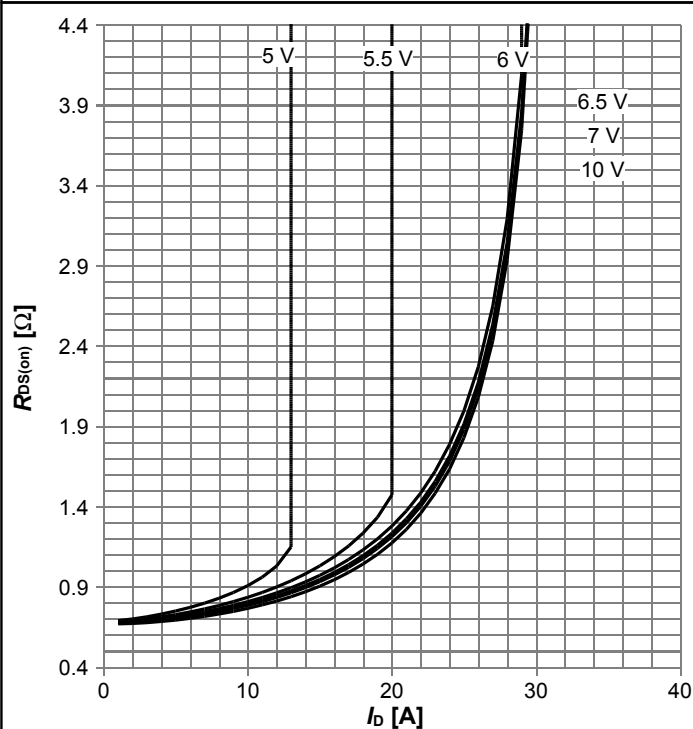
$I_D = f(V_{DS})$; $T_j = 25^\circ\text{C}$; parameter: V_{GS}

Diagram 6: Typ. output characteristics



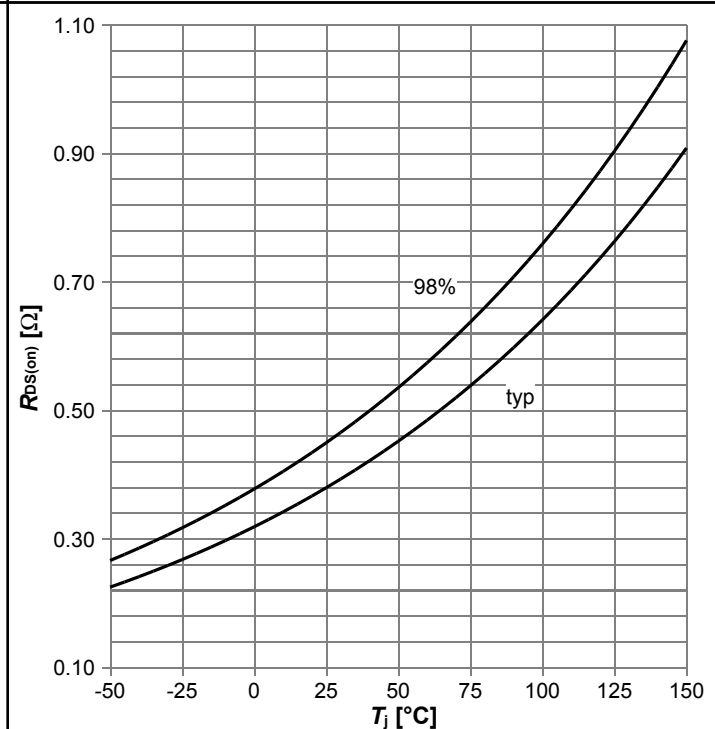
$I_D = f(V_{DS})$; $T_j = 125^\circ\text{C}$; parameter: V_{GS}

Diagram 7: Typ. drain-source on-state resistance



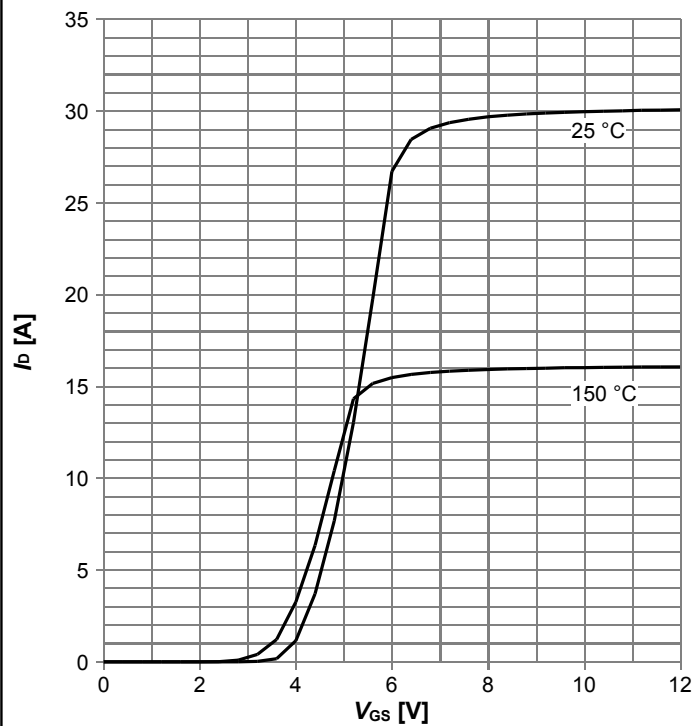
$R_{DS(on)} = f(I_D)$; $T_j = 125^\circ\text{C}$; parameter: V_{GS}

Diagram 8: Drain-source on-state resistance



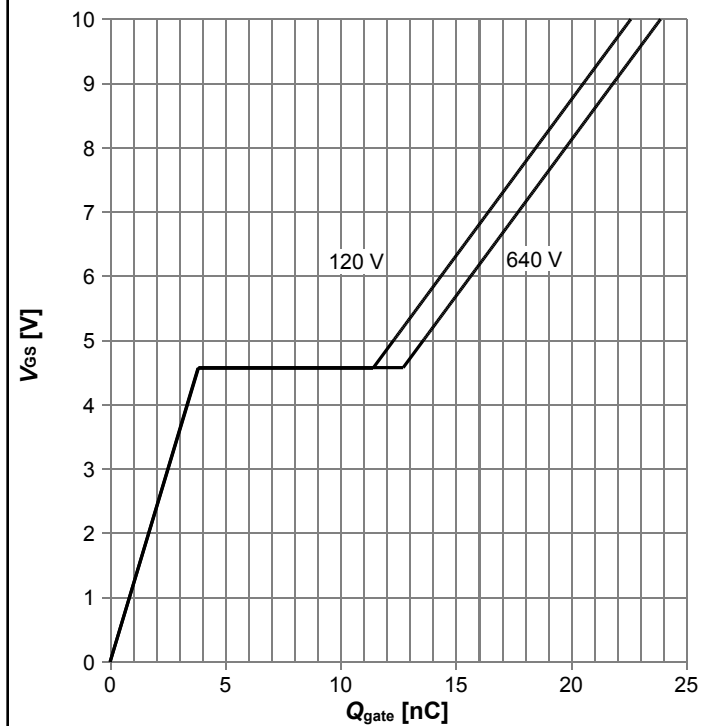
$R_{DS(on)} = f(T_j)$; $I_D = 4.5\text{ A}$; $V_{GS} = 10\text{ V}$

Diagram 9: Typ. transfer characteristics



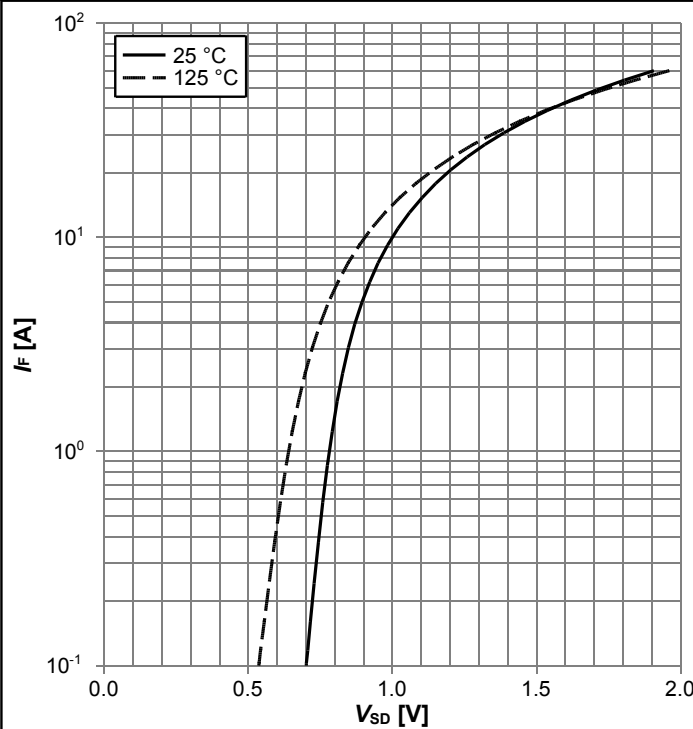
$I_D = f(V_{GS})$; $V_{DS} = 20V$; parameter: T_j

Diagram 10: Typ. gate charge



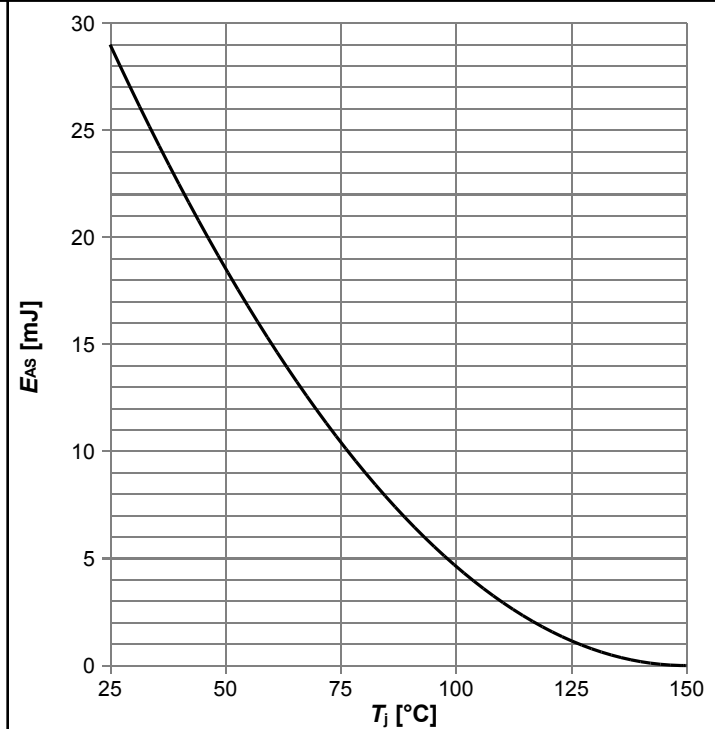
$V_{GS} = f(Q_{gate})$; $I_D = 4.5$ A pulsed; parameter: V_{DD}

Diagram 11: Forward characteristics of reverse diode



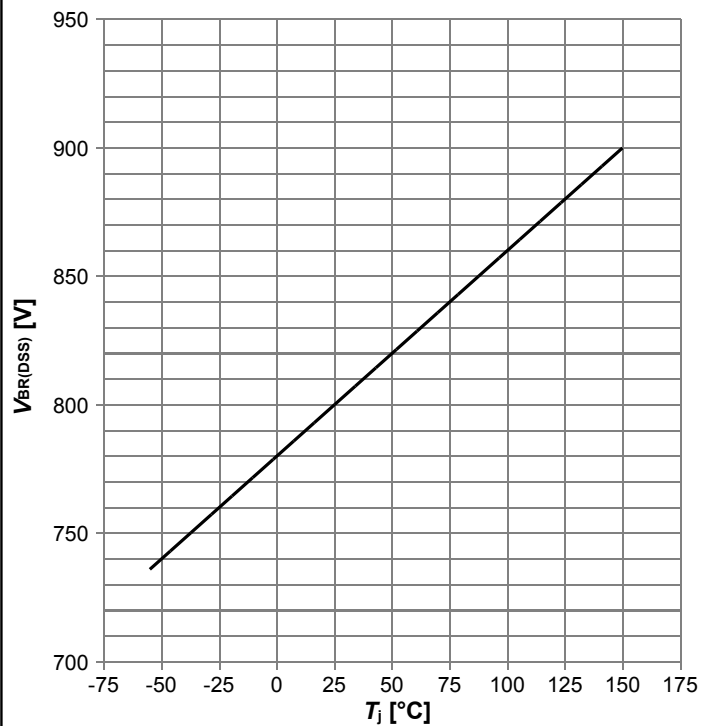
$I_F = f(V_{SD})$; parameter: T_j

Diagram 12: Avalanche energy



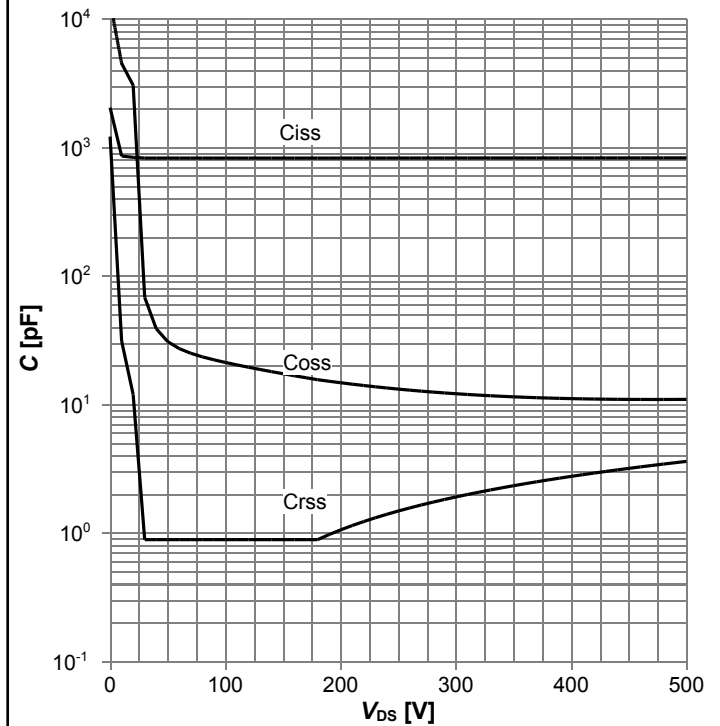
$E_{AS} = f(T_j)$; $I_D = 1.8$ A; $V_{DD} = 50$ V

Diagram 13: Drain-source breakdown voltage



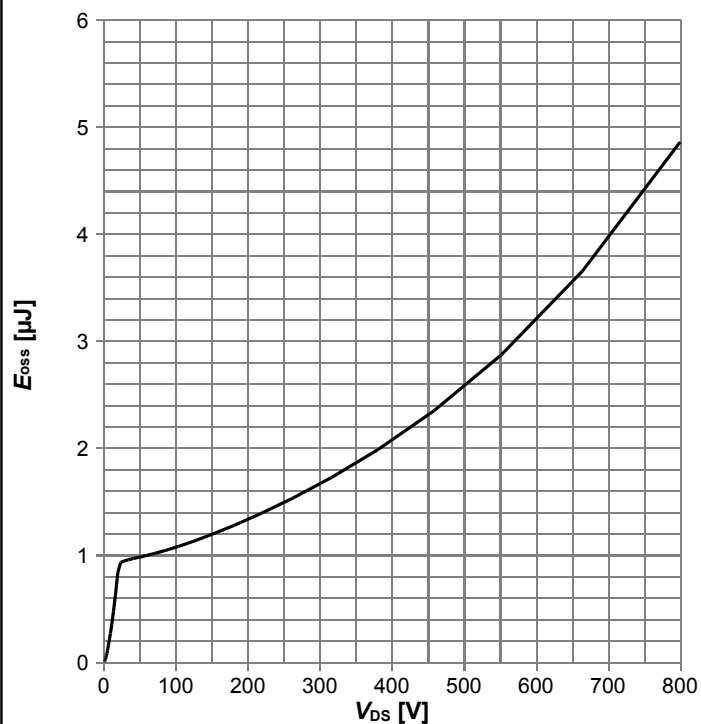
$V_{BR(DSS)} = f(T_j); I_D = 1 \text{ mA}$

Diagram 14: Typ. capacitances



$C = f(V_{DS}); V_{GS} = 0 \text{ V}; f = 250 \text{ kHz}$

Diagram 15: Typ. Coss stored energy



$E_{oss} = f(V_{DS})$

5 Test Circuits

Table 8 Diode characteristics

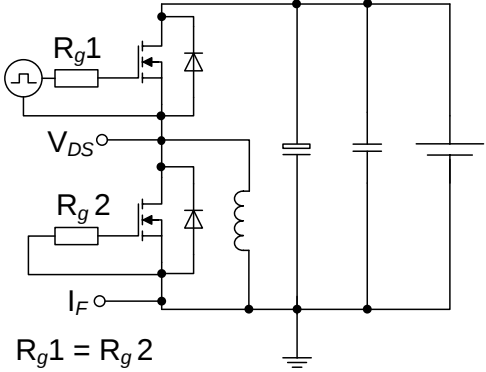
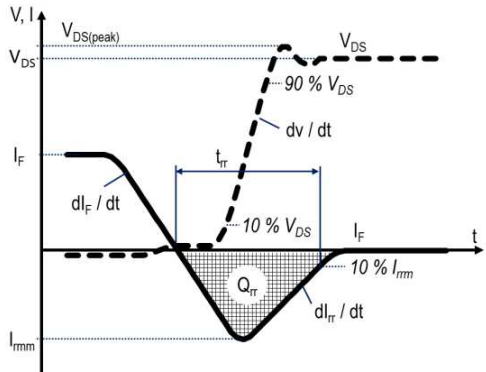
Test circuit for diode characteristics	Diode recovery waveform
 $R_{g1} = R_{g2}$	

Table 9 Switching times

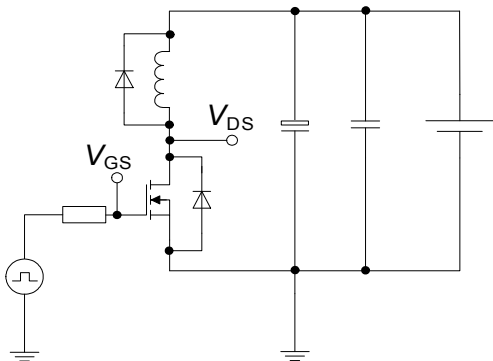
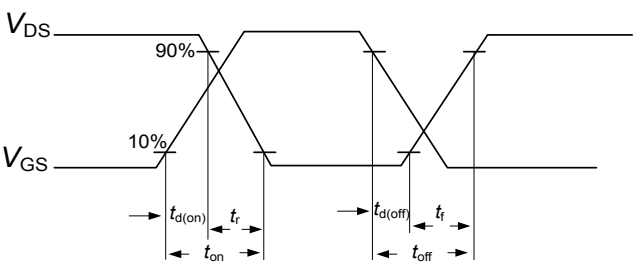
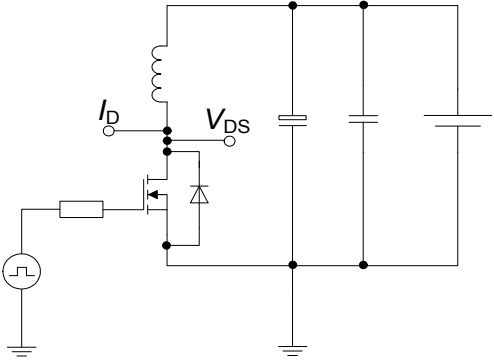
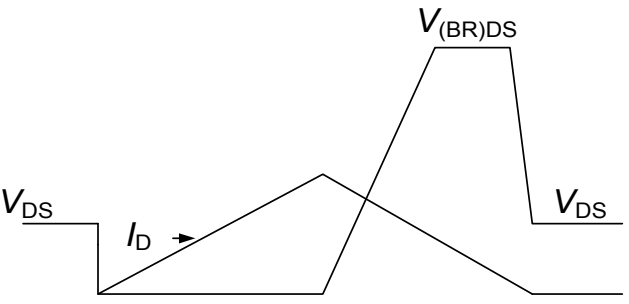
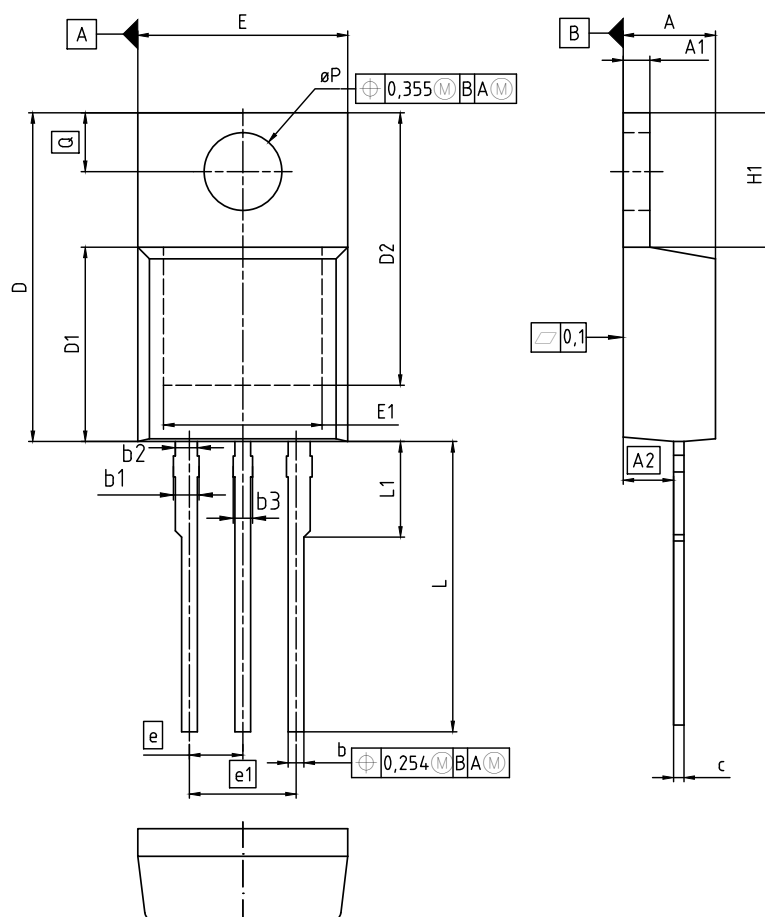
Switching times test circuit for inductive load	Switching times waveform
	

Table 10 Unclamped inductive load

Unclamped inductive load test circuit	Unclamped inductive waveform
	

6 Package Outlines



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	1.17	1.40	0.046	0.055
A2	2.15	2.72	0.085	0.107
b	0.65	0.86	0.026	0.034
b1	0.95	1.40	0.037	0.055
b2	0.95	1.15	0.037	0.045
b3	0.65	1.15	0.026	0.045
c	0.33	0.60	0.013	0.024
D	14.81	15.95	0.583	0.628
D1	8.51	9.45	0.335	0.372
D2	12.19	13.10	0.480	0.516
E	9.70	10.36	0.382	0.408
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	3		3	
H1	5.90	6.90	0.232	0.272
L	13.00	14.00	0.512	0.551
L1	-	4.80	-	0.189
øP	3.60	3.89	0.142	0.153
Q	2.60	3.00	0.102	0.118

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Figure 1 Outline PG-TO 220-3, dimensions in mm/inches

7 Appendix A

Table 11 Related Links

- **IFX CoolMOS Webpage:** www.infineon.com
- **IFX Design tools:** www.infineon.com

Revision History

IPP80R450P7

Revision: 2018-02-08, Rev. 2.1

Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.0	2016-07-05	Release of final version
2.1	2018-02-08	Corrected front page text

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