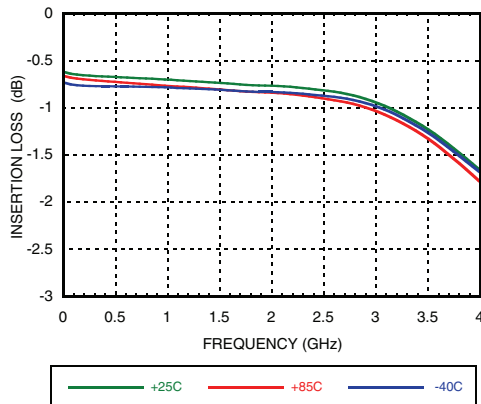
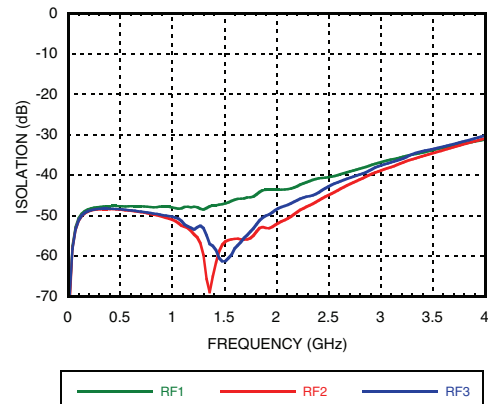
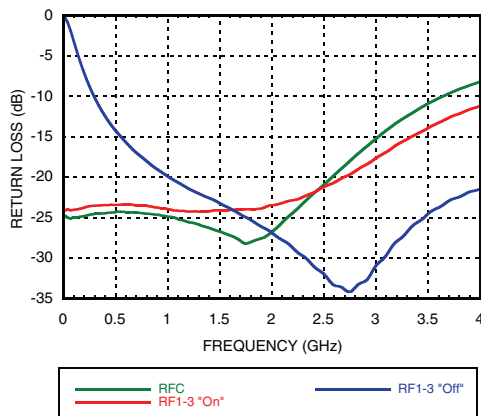
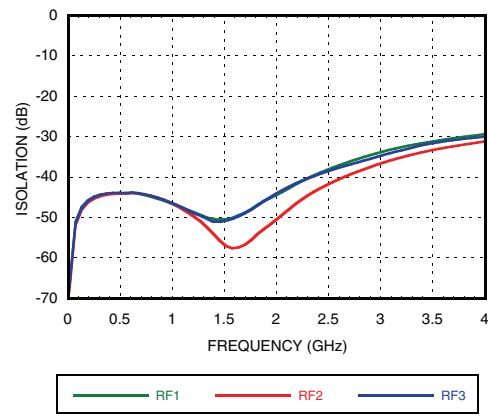
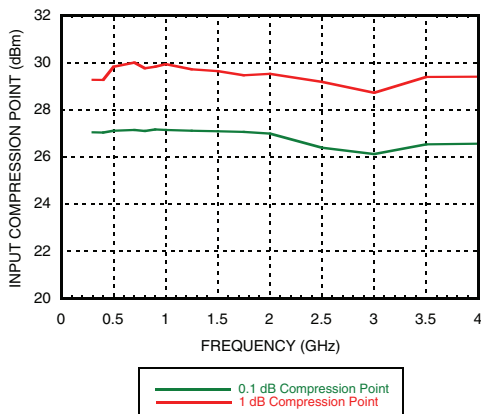
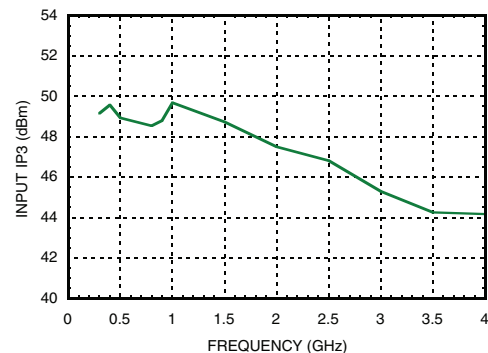


**GaAs MMIC SP3T Non-REFLECTIVE  
SWITCH, DC - 3.5 GHz**
**Insertion Loss vs. Temperature**

**Isolation**

**Return Loss**

**Off State Isolation**

**0.1 and 1 dB Input Compression Point**

**Input Third Order Intercept Point**



**GaAs MMIC SP3T Non-REFLECTIVE  
SWITCH, DC - 3.5 GHz**
**Bias Voltage & Current**

| Vdd Range= +5 Vdc $\pm$ 10% |                |                |
|-----------------------------|----------------|----------------|
| Vdd (Vdc)                   | Idd (Typ) (mA) | Idd (Max) (mA) |
| +5                          | 2.2            | 6.0            |

**TTL/CMOS Control Voltages**

| State | Bias Condition                   |
|-------|----------------------------------|
| Low   | 0 to +0.8 Vdc @ 0.2 $\mu$ A Typ. |
| High  | +2.0 to +5 Vdc @ 35 $\mu$ A Typ. |

**Truth Table**

| Control Input |      | Signal Path State |
|---------------|------|-------------------|
| A             | B    | RF COM to:        |
| Low           | Low  | RF1               |
| High          | Low  | RF2               |
| Low           | High | RF3               |
| High          | High | All Off           |



## GaAs MMIC SP3T Non-REFLECTIVE SWITCH, DC - 3.5 GHz

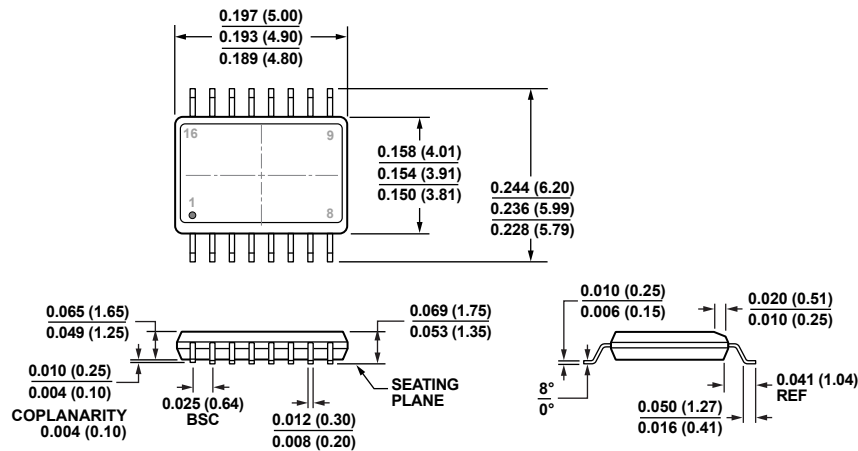
### Absolute Maximum Ratings

|                                     |                     |
|-------------------------------------|---------------------|
| Bias Voltage Range (Port Vdd)       | +7.0 Vdc            |
| Control Voltage Range (A & B)       | -0.5V to Vdd +1 Vdc |
| Channel Temperature                 | 150 °C              |
| Thermal Resistance                  |                     |
| Insertion Loss Path                 | 150 °C/W            |
| Terminated Path                     | 297 °C/W            |
| Storage Temperature                 | -65 to +150 °C      |
| Operating Temperature               | -40 to +85 °C       |
| Maximum Input Power<br>Vdd = +5 Vdc |                     |
| Insertion Loss Path                 | +28.5 dBm           |
| Terminated Path                     | +23.4 dBm           |
| ESD Sensitivity (HBM)               | Class 1A            |



ELECTROSTATIC SENSITIVE DEVICE  
OBSERVE HANDLING PRECAUTIONS

### Outline Drawing



COMPLIANT TO JEDEC STANDARDS MO-137-AB  
CONTROLLING DIMENSIONS ARE IN INCHES; MILLIMETER DIMENSIONS  
(IN PARENTHESES) ARE ROUNDED-OFF INCH EQUIVALENTS FOR  
REFERENCE ONLY AND ARE NOT APPROPRIATE FOR USE IN DESIGN.

16-Lead Shrink Small Outline Package [QSOP]  
(RQ-16)

Dimensions shown in inches and (millimeters)

08-122014-A

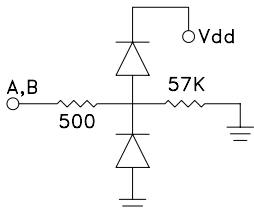
### Package Information

| Part Number  | Package Body Material                                                             | Leadframe Plating | MSL Rating | Package Marking [2] |
|--------------|-----------------------------------------------------------------------------------|-------------------|------------|---------------------|
| HMC245AQS16E | RoHS-compliant Low Stress Injection Molded Plastic Silica and Silicon Impregnated | 100% Matte Tin    | MSL1 [1]   | H245A<br>XXXX       |

[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX

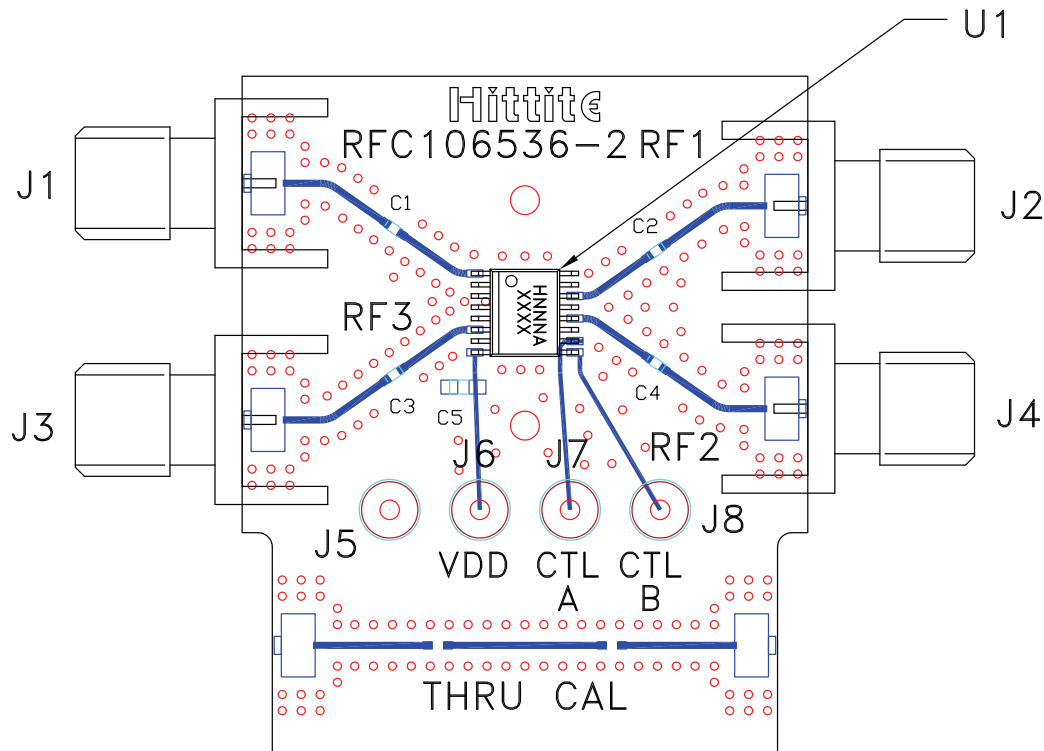

**GaAs MMIC SP3T Non-REFLECTIVE  
SWITCH, DC - 3.5 GHz**
**Pin Descriptions**

| Pin Number                  | Function              | Description                                                                         | Interface Schematic                                                                 |
|-----------------------------|-----------------------|-------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------|
| 1, 6, 12, 14                | RF3, RF2,<br>RF1, RFC | This pin is DC coupled and matched to 50 Ohms.<br>Blocking capacitors are required. |                                                                                     |
| 2 - 5, 7, 11, 13,<br>15, 16 | GND                   | This pin must be connected to PCB RF<br>ground to maximize isolation.               |  |
| 8                           | Vdd                   | Supply Voltage +5 Vdc $\pm$ 10%                                                     |                                                                                     |
| 9                           | B                     | See truth table and control voltage table.                                          |  |
| 10                          | A                     | See truth table and control voltage table.                                          |                                                                                     |



**GaAs MMIC SP3T Non-REFLECTIVE  
SWITCH, DC - 3.5 GHz**

**Evaluation PCB**



**List of Materials for Evaluation PCB EV1HMC245AQS16 [1]**

| Item    | Description                            |
|---------|----------------------------------------|
| J1 - J4 | PCB Mount SMA RF Connector             |
| J5 - J8 | DC Pin                                 |
| C1 - C4 | 100 pF Capacitor, 0402 Pkg.            |
| C5      | 10k pF Capacitor, 0603 Pkg.            |
| U1      | HMC245AQS16 / 245AQS16E<br>SP3T Switch |
| PCB [2] | 106536 Evaluation PCB                  |

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should be generated with proper RF circuit design techniques. Signal lines at the RF port should have 50 ohm impedance and the package ground leads should be connected directly to the ground plane similar to that shown above. The evaluation circuit board shown above is available from Analog Devices upon request.