VS-MURD620CTPbF

Vishay Semiconductors

Ultrafast Rectifier, 2 x 3 A FRED Pt®



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
Reverse recovery time	t _{rr}	$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	35	9	
		$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	-	25		
		T _J = 25 °C	$I_F = 3 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 160 \text{ V}$	-	19	-	ns	
		T _J = 125 °C		-	26	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	3.1	-	Α	
		T _J = 125 °C		-	4.6	-	_ ^	
Reverse recovery charge	Q _{rr}		T _J = 25 °C	••	=	30	-	nC
		T _J = 125 °C		-	60	-	IIC	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	9.0	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	80	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	-	-	
Weight			-	0.3	-	g
			-	0.01	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D-PAK		MURE	620CT	





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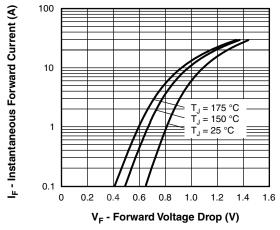


Fig. 1 - Typical Forward Voltage Drop Characteristics

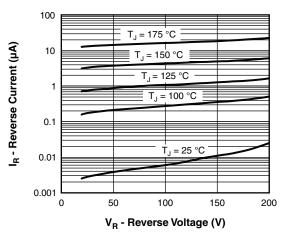


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

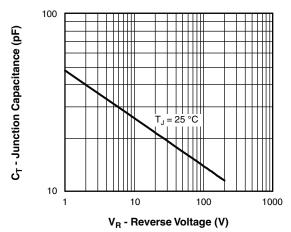


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

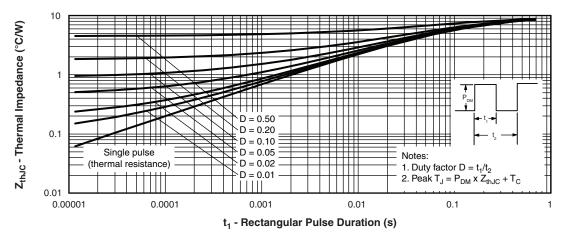


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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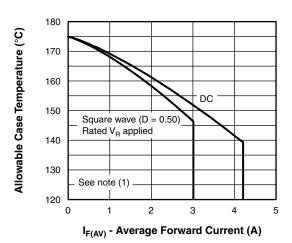


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

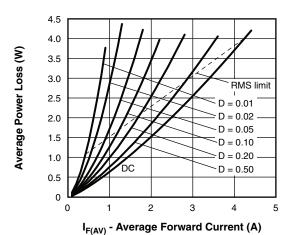


Fig. 6 - Forward Power Loss Characteristics

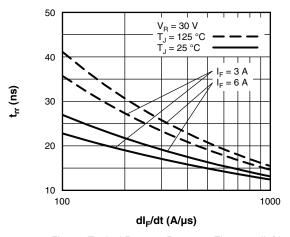


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

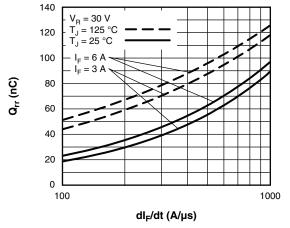


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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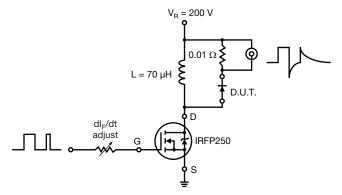
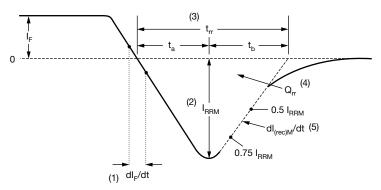


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) ${\rm I}_{\rm RRM}$ peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) Q_{rr} area under curve defined by t_{rr} and I_{BBM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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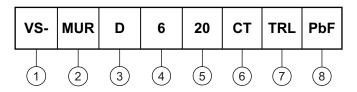
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ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

Ultrafast MUR series

D = D-PAK

Current rating (6 = 6 A)

Voltage rating (20 = 200 V)

TR = Tape and reel CT = Center tap (dual)

TRL = Tape and reel (left oriented) Tape and reel suffix TRR = Tape and reel (right oriented)

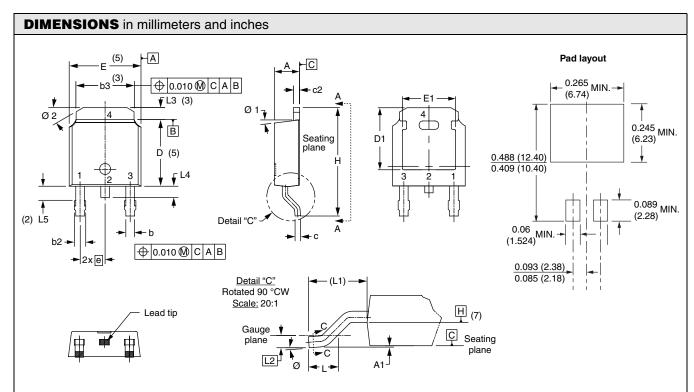
PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95059				
Packaging information	www.vishay.com/doc?95033				



Vishay High Power Products

D-PAK (TO-252AA)



SYMBOL	MILLIMETERS		INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	2.18	2.39	0.086	0.094	
A1	-	0.13	-	0.005	
b	0.64	0.89	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	3
С	0.46	0.61	0.018	0.024	
c2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	5
D1	5.21	-	0.205	1	3
Е	6.35	6.73	0.250	0.265	5
E1	4.32	-	0.170	-	3

SYMBOL	MILLIMETERS		INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
е	2.29	BSC	0.090		
Н	9.40	10.41	0.370	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74	BSC	0.108 REF.		
L2	0.51	BSC	0.020 BSC		
L3	0.89	1.27	0.035	0.050	3
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	2
Ø	0°	10°	0°	10°	
Ø1	0°	15°	0°	15°	
Ø2	25°	35°	25°	35°	

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension uncontrolled in L5
- (3) Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- (5) Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (6) Dimension b1 and c1 applied to base metal only
- $^{\left(7\right)}\,$ Datum A and B to be determined at datum plane H
- (8) Outline conforms to JEDEC outline TO-252AA

Document Number: 95016 Revision: 04-Nov-08

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