

VSC8486

10 Gbps XAUI or XGMII to XFI LAN/WAN Transceiver

Low Power

 Exceptionally low power (700 mW LAN mode or 750 mW WAN mode) allows for higher port densities

Wide Range of Support

- Fully compatible with IEEE 802.3ae and T11 10 GFC
- Extended-WIS (E-WIS) provides full Clause 50 support and enables transport over existing SONET networks
- 10 Gbps serial interface exceeds all SONET and 10 GbE requirements
- 4 x 3.125 Gbps and 3.182 Gbps XAUI I/O enable interconnection with a wide range of Layer 2 devices
- Seamless connectivity to XFP and SFP+ modules
- 10 Gbps receiver exceeds all 10 GbE stressed eye requirements

Tools For Rapid Design

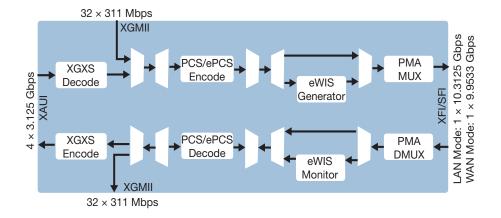
- Multiple loopback modes and built-in self test (BIST) capabilities reduce system development costs, enable manufacturing tests, and improve time to market
- JTAG access port facilitates boundary scan for in-circuit test to improve board yield
- Single 1.2 voltage for the whole chip with optional power supply range (1.2 V, 1.8 V, and 3.3 V) for the TTL interface

Flexibility

- XAUI I/O programmability for lane swap, invert, amplitude, preemphasis, and equalization
- KX4-compatible 3 Gbps I/O for long-reach copper interconnect provides additional margin to traverse connectors and the backplane up to 40 inches
- Extended-PCS (E-PCS) mode with forward error correction (FEC) auto-negotiation allows extended reach over single-mode and multimode fiber with 10–15 error floor
- Enhanced de-emphasis and equalization compensates for connector and channel losses and makes the device SFP+ compatible
- Accessibility of the recovered clock from high-speed input and separate clock paths for CMU and CRU enable Layer 1 support for synchronous Ethernet
- XGMII interface enables the device to be used in a roll-over redundant link application to provide fault tolerance

Related Products

Visit www.microsemi.com for information about other related products.





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