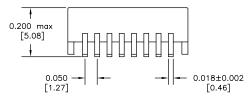
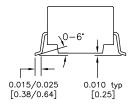
# **10BASE-T Isolation Modules**

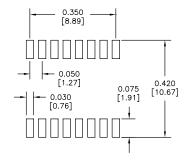
## Package N/N1

# 0.500 max [12.70] HHHHHHHHHH 0.350 [8.89]



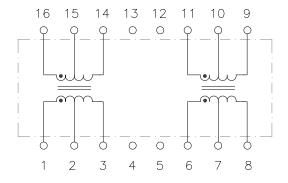


Dimensions: inch [mm] Co-Planarity: 0.004 [0.10]

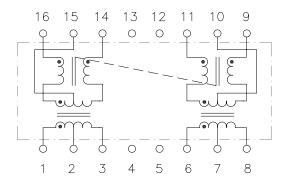


Recommended Solder Pad Layout

### Schematic A



## Schematic B



**US Patent Numbers** 

5,656,985 6,297,721 B1 6,297,720 B1 6,320,489 B1 6,344,785 B1 6,662,431 B1



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