## **Vishay Siliconix**

### **New Product**



Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
Static	•		•	-	-	-
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS}$ = 0 V, I <sub>D</sub> = 250 $\mu$ A	30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = 250 μA		25		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			- 6.3		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = 250 \ \mu A$	1.2		2.4	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS}$ = 0 V, $V_{GS}$ = ±20 V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub> -	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μΑ
		$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55°C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			А
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	$V_{GS} = 10$ V, $I_{D} = 20$ A		0.0046	0.0057	Ω
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 20 \text{ A}$		0.0062	0.0078	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 30 \text{ A}$		70		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 0 V, f = 1 MHz		3800		pF
Output Capacitance	C <sub>oss</sub>			615		
Reverse Transfer Capacitance	C <sub>rss</sub>			305		
		$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		62	95	nC
Total Gate Charge	Qg			30	45	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 15 V, $V_{GS}$ = 4.5 V, $I_{D}$ = 25 A		11		
Gate-Drain Charge	Q <sub>gd</sub>			9		
Gate Resistance	R <sub>g</sub>	f = 1 MHz		0.9	1.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	$\label{eq:VDD} \begin{array}{l} V_{DD} = 15 \ \text{V}, \ \text{R}_L = 0.5 \ \Omega \\ \text{I}_D \ \cong \ 30 \ \text{A}, \ \text{V}_{GEN} = 10 \ \text{V}, \ \text{R}_g = 1 \ \Omega \end{array}$		12	18	ns
Rise Time	t <sub>r</sub>			10	15	
Turn-Off Delay Time	t <sub>d(off)</sub>			30	45	
Fall Time	t <sub>f</sub>			8	12	
Turn-On Delay Time	t <sub>d(on)</sub>	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 15 \; V,  R_{\text{L}} = 0.6 \; \Omega \\ I_{\text{D}} \cong 25 \; A,  V_{\text{GEN}} = 4.5 \; V,  R_{\text{g}} = 1 \; \Omega \end{array}$		26	40	
Rise Time	t <sub>r</sub>			230	345	
Turn-Off Delay Time	t <sub>d(off)</sub>			25	40	
Fall Time	t <sub>f</sub>			9	14	
Drain-Source Body Diode Characte	eristics					
Continuous Source-Drain Diode Current	۱ <sub>S</sub>	$T_{C} = 25^{\circ}C$			55 <sup>c</sup>	•
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				100	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 6.7 A		0.9	1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	$I_F = 6.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, \text{ T}_J = 25^{\circ}\text{C}$	1	65	100	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			38	60	nC
Reverse Recovery Fall Time	ta			50		ns
Reverse Recovery Rise Time	t <sub>b</sub>			15		

Notes

a.

b.

Pulse test; pulse width  $\leq 300 \ \mu$ s, duty cycle  $\leq 2\%$ . Guaranteed by design, not subject to production testing. Calculated based on maximum junction temperature. Package limitation current is 50 A. c.

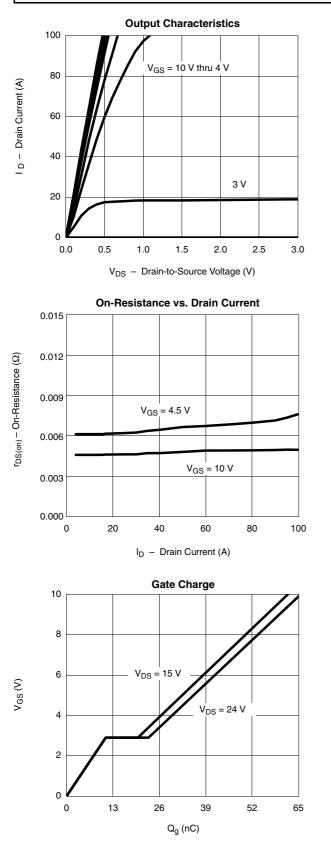
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

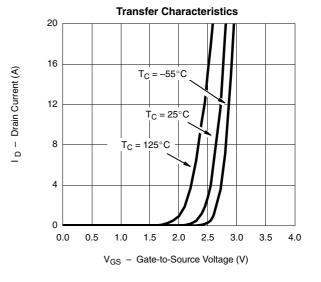


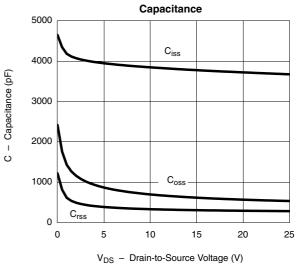
**New Product** 

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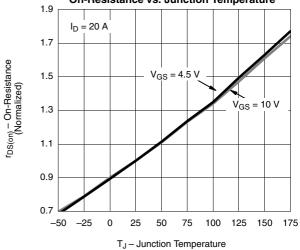
### **TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**







**On-Resistance vs. Junction Temperature** 



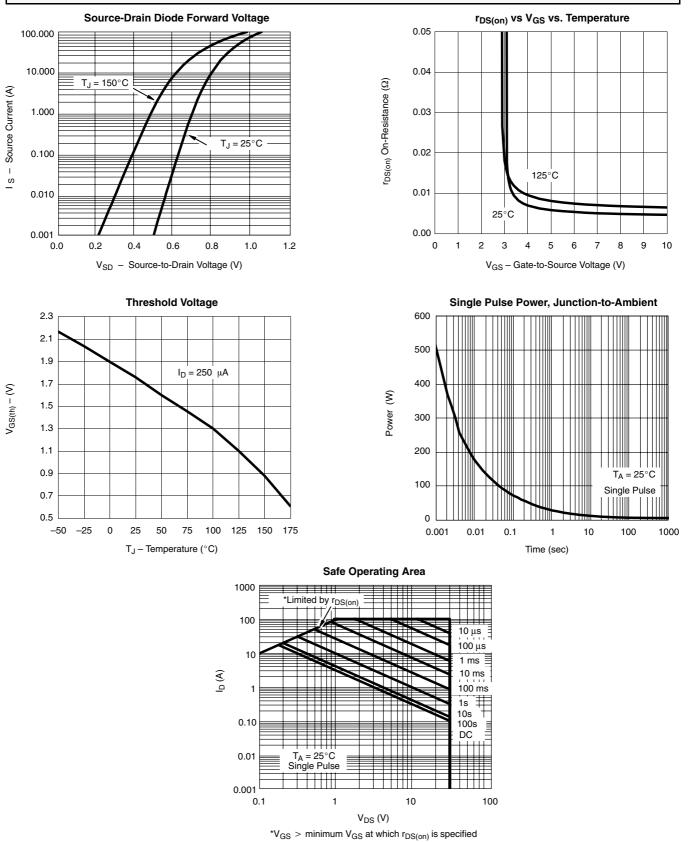
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### **New Product**



### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

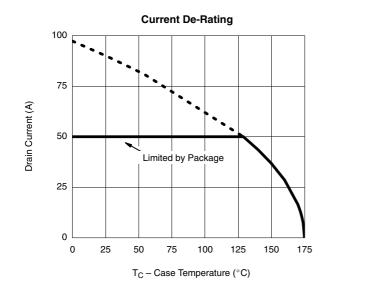


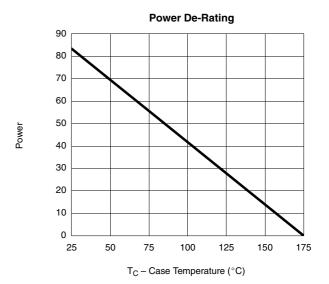


**New Product** 

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### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



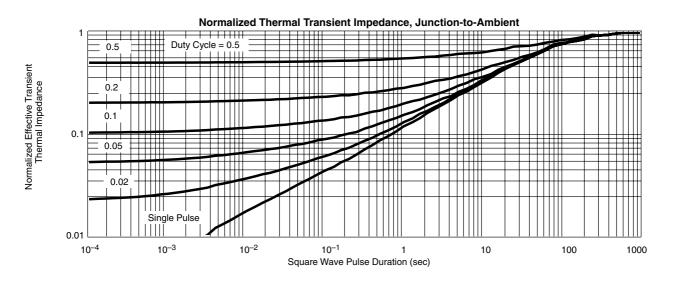


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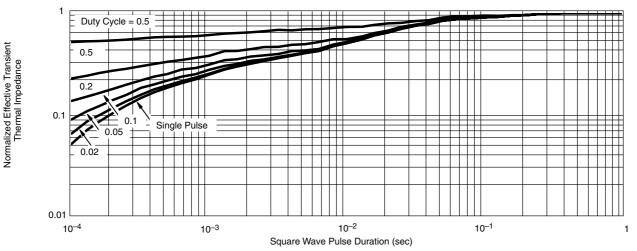
### **New Product**



### TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



#### Normalized Thermal Transient Impedance, Junction-to-Case



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg?73540">http://www.vishay.com/ppg?73540</a>.

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