Contents

| 1 | Pin configuration |
|---|------------------------------|
| 2 | Truth tables 5 |
| 3 | Maximum ratings 6 |
| 4 | Electrical characteristics 7 |
| 5 | Application circuits10 |
| 6 | Timing diagrams11 |
| 7 | Package mechanical data |
| 8 | Revision history |



ST3243EB, ST3243EC Pin configuration

1 Pin configuration

Figure 1. Pin configuration

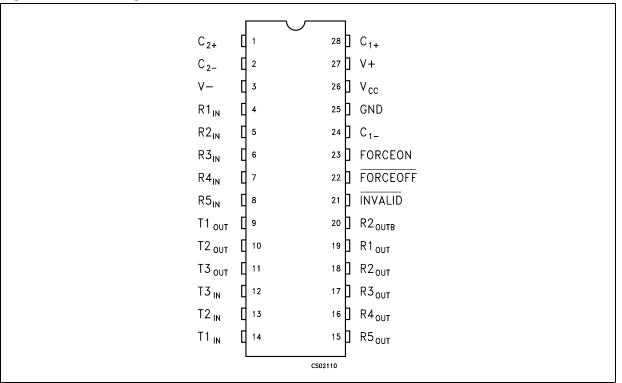


Table 2. Pin description

| Pin n° | Symbol | Name and function |
|--------|-------------------|--|
| 1 | C ₂ + | Positive terminal of inverting charge pump capacitor |
| 2 | C ₂ - | Negative terminal of inverting charge pump capacitor |
| 3 | V- | -5.5 V Generated by the charge pump |
| 4 | R1 _{IN} | First receiver input voltage |
| 5 | R2 _{IN} | Second receiver input voltage |
| 6 | R3 _{IN} | Third receiver input voltage |
| 7 | R4 _{IN} | Fourth receiver input voltage |
| 8 | R5 _{IN} | Fifth receiver input voltage |
| 9 | T1 _{OUT} | First transmitter output voltage |
| 10 | T2 _{OUT} | Second transmitter output voltage |
| 11 | T3 _{OUT} | Third transmitter output voltage |
| 12 | T3 _{IN} | Third transmitter input voltage |
| 13 | T2 _{IN} | Second transmitter input voltage |
| 14 | T1 _{IN} | First transmitter input voltage |

Table 2. Pin description (continued)

| Pin n° | Symbol | Name and function |
|--------|--------------------|--|
| 15 | R5 _{OUT} | Fifth receiver output voltage |
| 16 | R4 _{OUT} | Fourth receiver output voltage |
| 17 | R3 _{OUT} | Third receiver output voltage |
| 18 | R2 _{OUT} | Second receiver output voltage |
| 19 | R1 _{OUT} | First receiver output voltage |
| 20 | R2 _{OUTB} | Non-inverting complementary receiver output, always active for wake-up |
| 21 | INVALID | Output of the valid signal detector. Indicates if a valid RS-232 level is present on receiver inputs logic "1" |
| 22 | FORCEOFF | Drive low to shut down transmitters and on-board power supply. This over-rides all automatic circuitry and FORCEON |
| 23 | FORCEON | Drive high to override automatic circuitry keeping transmitters on (FORCEOFF must be high) |
| 24 | C ₁ - | Negative terminal of voltage-charge pump capacitor |
| 25 | GND | Ground |
| 26 | V _{CC} | Supply voltage |
| 27 | V+ | 5.5 V Generated by the charge pump |
| 28 | C ₁ + | Positive terminal of voltage-charge pump capacitor |

ST3243EB, ST3243EC Truth tables

2 Truth tables

Table 3. Invalid truth table

| RS-232 Signal present at any receiver input | INVALID output |
|---|----------------|
| YES | Н |
| NO | L |

Table 4. Output control truth table

| Force ON | Force OFF | Valid receiver level | Operation status | T _{OUT} | R _{OUT} | R _{2OUTB} |
|-------------|--------------|----------------------|------------------------------------|------------------|------------------|--------------------|
| Х | 0 | Х | Shutdown (Force OFF) | HIGH Z | HIGH Z | ACTIVE |
| 1 | 1 | Х | Normal operating (Force ON) | ACTIVE | ACTIVE | ACTIVE |
| 0 | 1 | YES | Normal operating (Auto power-down) | ACTIVE | ACTIVE | ACTIVE |
| 0 | 1 | NO | Shutdown (Auto power-down) | HIGH Z | ACTIVE | ACTIVE |

3 Maximum ratings

Table 5. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
|---|--|---------------------------------|------|
| V _{CC} | Supply voltage | -0.3 to 6 | V |
| V+ | Doubled voltage terminal | (V _{CC} -0.3) to 7 | V |
| V- | Inverted voltage terminal | 0.3 to -7 | V |
| V+ + V- | | 13 | V |
| FORCEON, FORCEOFF, T _{IN} | Input voltage | -0.3 to 6 | V |
| R _{IN} | Receiver input voltage range | ± 25 | V |
| T _{OUT} | Transmitter output voltage range | ± 13.2 | V |
| R _{OUT} R _{OUTB} INVALID | Receiver output voltage range | -0.3 to (V _{CC} + 0.3) | V |
| t _{SHORT} | Short circuit duration on T _{OUT} (one at a time) | Continuous | |
| T _{stg} | Storage temperature range | -65 to 150 | °C |

Note:

Absolute maximum ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied.

Table 6. ESD performance: transmitter outputs, receiver inputs

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|--------|------------------------|----------------------------------|------|------|------|------|
| ESD | ESD protection voltage | Human body model | ± 15 | | | kV |
| ESD | ESD protection voltage | IEC 1000-4-2 (contact discharge) | ± 8 | | | kV |

4 Electrical characteristics

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C.

Table 7. Electrical characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|---------------------|--------------------------------|--|------|------|------|------|
| I _{ASHDN} | Supply current auto power-down | FORCEOFF = GND, FORCEON = V _{CC} All R_IN open or grounded | | 1 | 10 | μΑ |
| I _{SUPPLY} | Supply current | FORCEON = FORCEOFF = V _{CC} | | 0.3 | 1 | mA |
| I _{SHDN} | Shutdown supply current | FORCEOFF = GND | | 1 | 10 | μΑ |

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C.

Table 8. Logic input electrical characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|-------------------|------------------------------|---|----------------------|----------------------|-------|--------|
| V _{TIL} | Input logic threshold low | T-IN, FORCEON, FORCEOFF | | | 0.8 | V |
| V _{TIH} | Input logic threshold high | T-IN, FORCEON, FORCEOFF $V_{CC} = 3.3 \text{ V}$ $V_{CC} = 5 \text{ V}$ | 2 2.4 | | | V V |
| V _{THYS} | Transmitter input hysteresis | | | 0.5 | | V |
| I _{IL} | Input leakage current | T-IN, FORCEON, FORCEOFF | | ± 0.01 | ± 1.0 | μΑ |
| I _{OL} | Output leakage current | Receiver disabled | | ± 0.05 | ± 10 | μΑ |
| V _{OL} | Output voltage low | I _{OUT} = 1.6 mA | | | 0.4 | V |
| V _{OH} | Output voltage high | I _{OUT} = -1 mA | V _{CC} -0.6 | V _{CC} -0.1 | | V |

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C, FORCEON = GND, FORCEOFF = V $_{CC}$.

Table 9. Auto power-down electrical characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|-------------------|---|---------------------------|----------------------|------|------|------|
| ., | Receiver input threshold | Positive threshold | | | 2.7 | V |
| V _{RITE} | to INVALID output voltage HIGH (see <i>Figure 3</i>) | Negative threshold | 2.7 | | | ٧ |
| V _{RITD} | Receiver input threshold to INVALID output voltage LOW (see <i>Figure 3</i>) | | -0.3 | | 0.3 | ٧ |
| V _{IOL} | INVALID output voltage LOW | I _{OUT} = 1.6 mA | | | 0.4 | V |
| V _{IOH} | INVALID output voltage HIGH | I _{OUT} = -1 mA | V _{CC} -0.6 | | | V |
| t _{WU} | Receiver or transmitter edge transmitter enabled (see <i>Figure 3</i>) | | | 100 | | μs |
| t _{INVH} | Receiver positive or negative threshold to INVALID HIGH (see Figure 3) | | | 0.2 | | μs |
| t _{INVL} | Receiver positive or negative threshold to INVALID LOW (see Figure 3) | | | 30 | | μs |

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C.

Table 10. Transmitter electrical characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|-------------------|------------------------------|---|------|-------|------|------|
| V _{TOUT} | Output voltage swing | All Transmitter outputs are loaded with $3k\Omega$ to GND | ± 5 | ± 5.4 | | V |
| R _{OUT} | Output resistance | $V_{CC} = V_{+} = V_{-} = 0 \text{ V}, V_{OUT} = \pm 2 \text{ V}$ | 300 | 10M | | Ω |
| I _{SC} | Output short circuit current | V _{CC} = 3.3 V | | ± 40 | ± 60 | mA |
| IL | Output leakage current | V _{CC} = 0 to 5.5V, transmitter output = ±12 V, transmitter disabled | | | ± 25 | μΑ |
| V _{OT} | Transmitter output voltage | T1IN = T2IN = GND,T3IN = V_{CC} T3OUT loaded with 3 k Ω to GND T1OUT and T2OUT loaded with 2.5mA each | ± 5 | | | V |

577

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C.

Table 11. Receiver electrical characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|--------------------|--|--|------------|------------|------------|------|
| V _{RIN} | Receiver input voltage operating range | | -25 | | 25 | V |
| V _{RIL} | RS-232 Input threshold low | $T_A = 25$ °C, $V_{CC} = 3.3 \text{ V}$ $T_A = 25$ °C, $V_{CC} = 5.0 \text{ V}$ | 0.6 0.8 | 1.1 1.4 | | V |
| V _{RIH} | RS-232 Input threshold high | $T_A = 25$ °C, $V_{CC} = 3.3 \text{ V}$ $T_A = 25$ °C, $V_{CC} = 5.0 \text{ V}$ | | 1.6 1.9 | 2.4 2.4 | V |
| V _{RIHYS} | Input hysteresis | | | 0.5 | | V |
| R _{RIN} | Input resistance | T _A = 25°C | 3 | 5 | 7 | kΩ |

C1 - C4 = 0.1 μ F, V $_{CC}$ = 3 V to 5.5 V, T $_{A}$ = -40 to 85 $^{\circ}$ C, unless otherwise specified. Typical values are referred to T $_{A}$ = 25 $^{\circ}$ C.

Table 12. Timing characteristics

| Symbol | Parameter | Test conditions | Min. | Тур. | Max. | Unit |
|--------------------------------------|----------------------------|---|--------|------|----------|--------------|
| D _R | Maximum data rate | $R_L = 3k\Omega$, $C_L = 1000$ pF one transmitter switching | 250 | 400 | | kbps |
| t _{PHL} t _{PLH} | Receiver propagation delay | R_{IN} to R_{OUT} , $C_L = 150 pF$ | | 0.15 | | μs |
| t _{T_SKEW} | Transmitter skew | | | 150 | | ns |
| t _{R_SKEW} | Receiver skew | | | 70 | | ns |
| S _{RT} | Transition slew rate | T_A = 25°C R_L = 3k to 7kΩ, V_{CC} = 3.3 V measured from +3 V to -3 V or -3 V to +3 V C_L = 150 pF to 1000 pF C_L = 150 pF to 2500 pF | 6 4 | | 30 30 | V/µs V/µs |

5 Application circuits

Figure 2. Application circuits

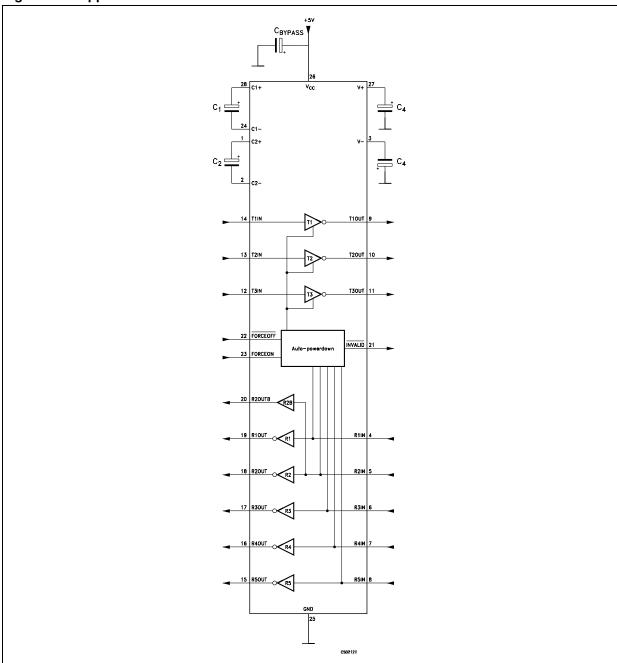


Table 13. Required minimum capacitance value (μF)

| V _{CC} (V) | C ₁ | C ₂ , C ₃ , C ₄ , C _{BYPASS} |
|---------------------|----------------|--|
| 3 to 3.6 | 0.1 | 0.1 |
| 4.5 to 5.5 | 0.047 | 0.33 |

ST3243EB, ST3243EC Timing diagrams

6 Timing diagrams

Figure 3. Auto power-down input levels

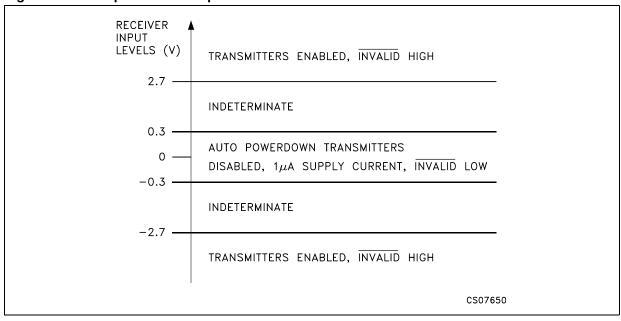
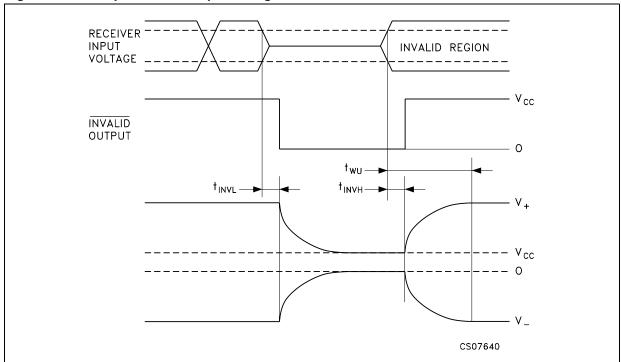


Figure 4. Auto power-down input timing

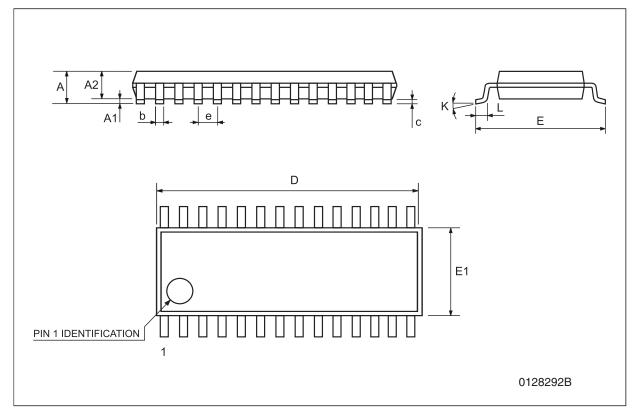


7 Package mechanical data

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TSSOP28 mechanical data

| Dim. | mm. | | | inch. | | |
|------|------|----------|------|-------|------------|--------|
| | Min. | Тур. | Max. | Min. | Тур. | Max. |
| А | | | 1.2 | | | 0.047 |
| A1 | 0.05 | | 0.15 | 0.002 | 0.004 | 0.006 |
| A2 | 0.8 | 1 | 1.05 | 0.031 | 0.039 | 0.041 |
| b | 0.19 | | 0.30 | 0.007 | | 0.012 |
| С | 0.09 | | 0.20 | 0.004 | | 0.0079 |
| D | 9.6 | 9.7 | 9.8 | 0.378 | 0.382 | 0.386 |
| E | 6.2 | 6.4 | 6.6 | 0.244 | 0.252 | 0.260 |
| E1 | 4.3 | 4.4 | 4.48 | 0.169 | 0.173 | 0.176 |
| е | | 0.65 BSC | | | 0.0256 BSC | |
| К | 0° | | 8° | 0° | | 8° |
| L | 0.45 | 0.60 | 0.75 | 0.018 | 0.024 | 0.030 |



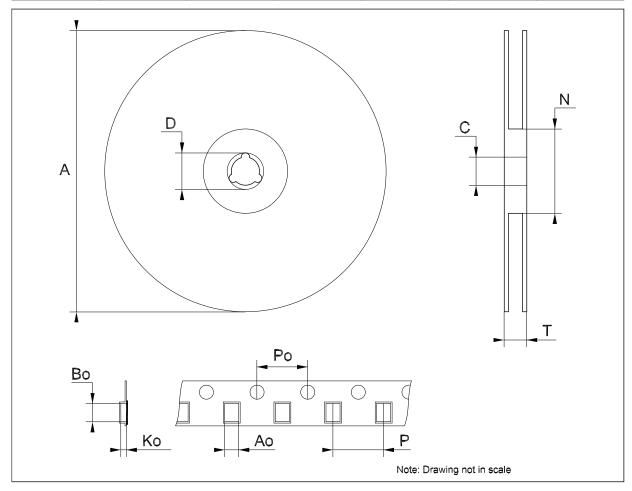
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Doc ID 8648 Rev 12

13/16

Tape & reel TSSOP28 mechanical data

| Dim. | mm. | | | inch. | | |
|------|------|------|------|-------|------|--------|
| | Min. | Тур. | Max. | Min. | Тур. | Max. |
| А | | | 330 | | | 12.992 |
| С | 12.8 | | 13.2 | 0.504 | | 0.519 |
| D | 20.2 | | | 0.795 | | |
| N | 60 | | | 2.362 | | |
| Т | | | 22.4 | | | 0.882 |
| Ao | 6.8 | | 7 | 0.268 | | 0.276 |
| Во | 10.1 | | 10.3 | 0.398 | | 0.406 |
| Ko | 1.7 | | 1.9 | 0.067 | | 0.075 |
| Po | 3.9 | | 4.1 | 0.153 | | 0.161 |
| Р | 11.9 | | 12.1 | 0.468 | | 0.476 |



ST3243EB, ST3243EC Revision history

8 Revision history

Table 14. Document revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 21-Jun-2004 | 6 | Page 6 - I _L (output leakage current) mA ==> μA |
| 31-Mar-2006 | 7 | Order codes updated and new template. |
| 25-Oct-2006 | 8 | Order codes updated. |
| 24-Aug-2007 | 9 | Order codes updated. |
| 09-Jul-2008 | 10 | Removed: SO-28 and SSOP28 packages. |
| 28-Jul-2009 | 11 | Removed: Flip-chip28 package, modified Table 1 on page 1. |
| 16-Oct-2009 | 12 | Modified Table 9 on page 8. |

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