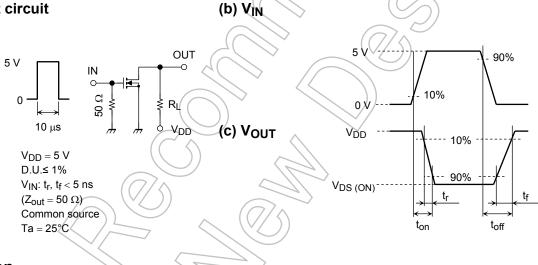
**Electrical Characteristics (Ta = 25°C)** 

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 16~V,~V_{DS}=0$			±1	μA
Drain-Source breakdown voltage		V (BR) DSS	$I_D = 0.1 \text{ mA}, V_{GS} = 0$	30		_	V
Drain Cut-off current		I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$	$\nearrow$		1	μA
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	0.8	_	1.5	V
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	25	)}_		mS
Drain-Source ON resistance		R <sub>DS (ON)</sub>	$I_D = 10 \text{ mA}, V_{GS} = 4 \text{ V}$	$7\pi$	2.2	4.0	Ω
			$I_{D} = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	$\Theta$	4.0	7.0	
Input capacitance		C <sub>iss</sub>	$V_{DS} = 3 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$		7.8		pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 3 V$ , $V_{GS} = 0$ , f = 1 MHz	_	3.6	_	pF
Output capacitance		C <sub>oss</sub>	$V_{DS} = 3 \text{ V}, \text{ V}_{GS} = 0, \text{ f} \neq 1 \text{ MHz}$		8.8	1	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = 5 \text{ V}, \text{ I}_{D} = 10 \text{ mA},$ $V_{GS} = 0 \text{ to } 5 \text{ V}$	_	50	ns	20
	Turn-off time	t <sub>off</sub>		-6	180		IIS

### **Switching Time Test Circuit**



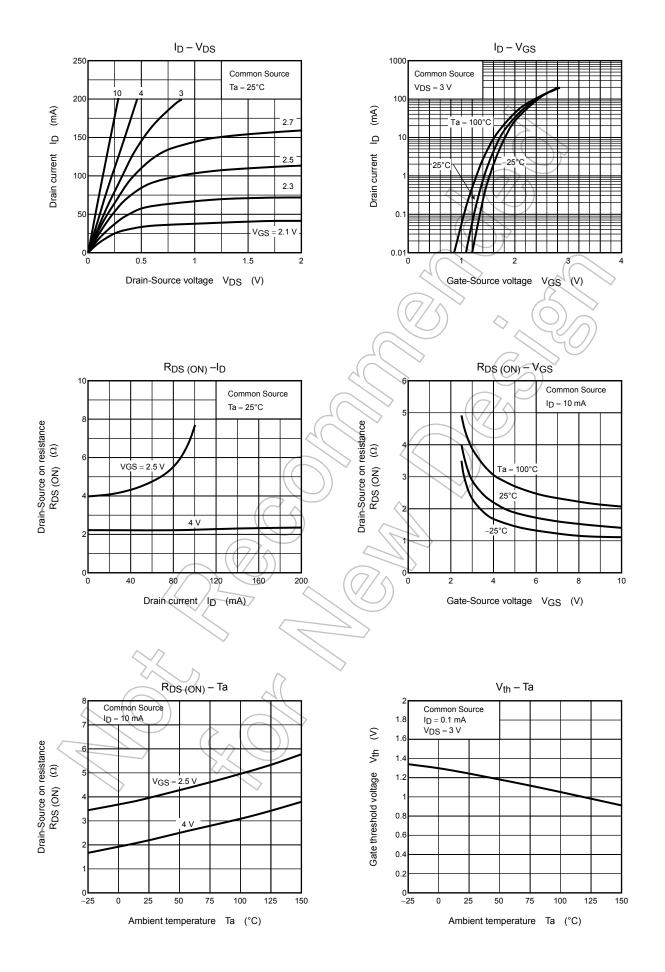


#### Precaution

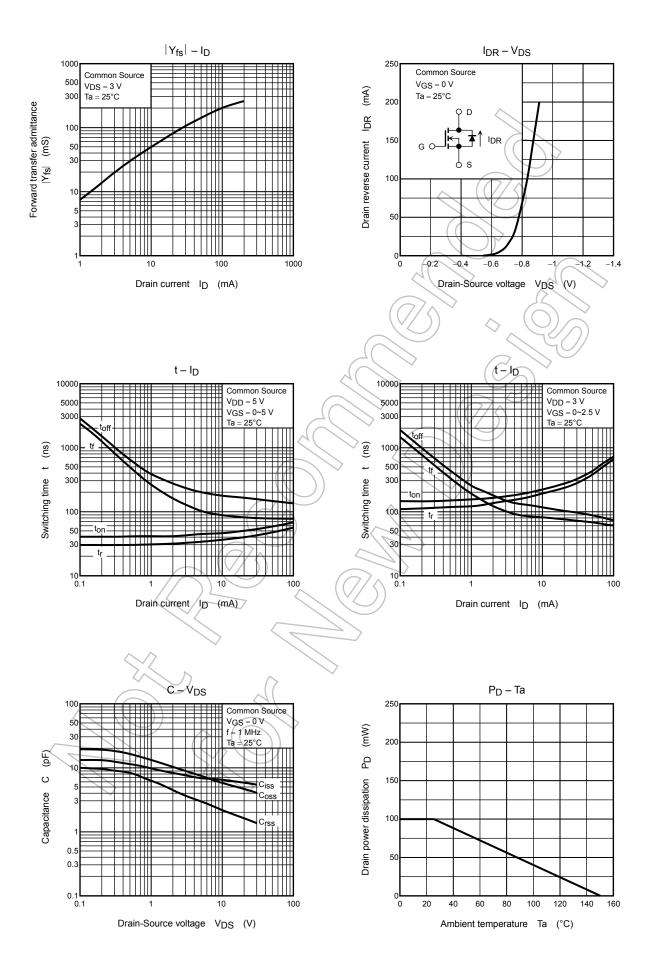
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is  $I_D$  = 100  $\mu A$  for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ .

(relationship can be established as follows:  $V_{GS}$  (off)  $< V_{th} < V_{GS}$  (on) ) Please take this into consideration for using the device.

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