

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _{RM}	40	V
Average Rectified Output Current	lo	2.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (Per Diode)	I _{FSM}	70	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Case (Note 5)	R _{OJC}	15	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

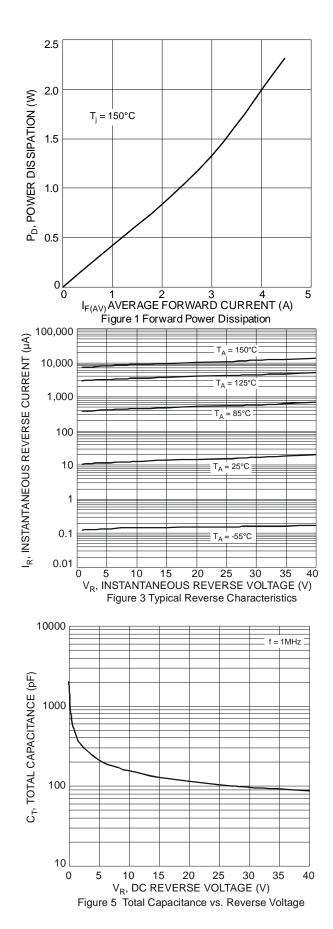
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage (Per Diode)	V _F	-	— 0.42	0.50 0.47	I V	I _F = 2.0A, T _J = +25°C I _F = 2.0A, T _J = +125°C
Reverse Current (Note 6) (Per Diode)	I _R	_	_	0.1 10	I mA	$V_R = 40V, T_J = +25$ °C $V_R = 40V, T_J = +125$ °C
Total Capacitance (Per Diode)	Ст	_	90	_	pF	$V_R = 40V, f = 1.0MHz,$ $T_J = +25^{\circ}C$

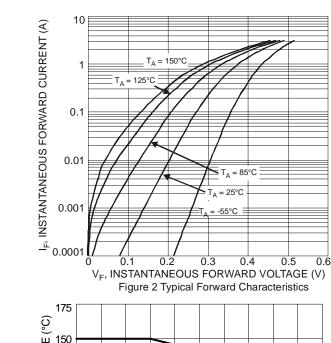
Notes:

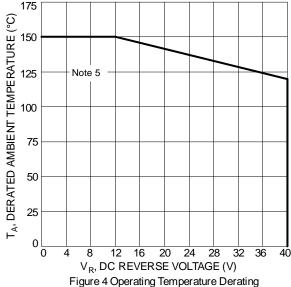
^{5.} Device mounted on FR-4 substrate PCB, with minimum recommended pad layout per https://www.diodes.com/package-outlines.html.

 $[\]hbox{6. Short duration pulse test used to minimize self-heating effect.}\\$





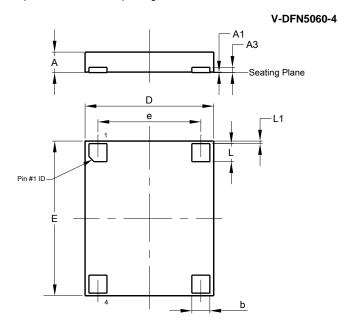






Package Outline Dimensions

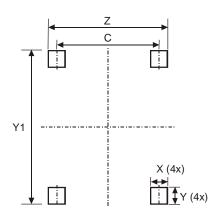
Please see http://www.diodes.com/package-outlines.html for the latest version.



V-DFN5060-4				
Dim	Min	Max	Тур	
Α	0.75	0.85	0.80	
A1	0	0.05	0.02	
A3	1	-	0.203	
b	0.65	0.75	0.70	
D	4.95	5.05	5.00	
е	_	_	4.00	
Е	5.95	6.05	6.00	
١	0.65	0.75	0.70	
L1	0.05	0.15	0.10	
All Dimensions in mm				

Suggested Pad Layout

 $\label{prop:lease} Please see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$



V-DFN5060-4

Dimensions	Value (in	
Difficusions	mm)	
С	4.00	
Х	0.75	
Υ	0.95	
Y1	6.20	
Z	4.75	



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