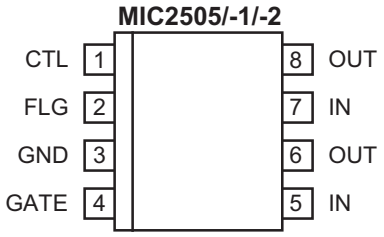
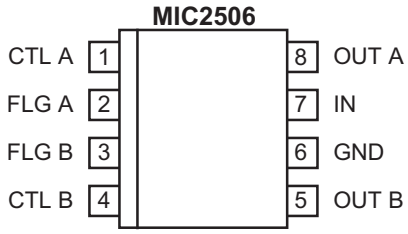
Package Types

MIC2505/-1/-2
8-Pin SOIC (M)
(Top View)

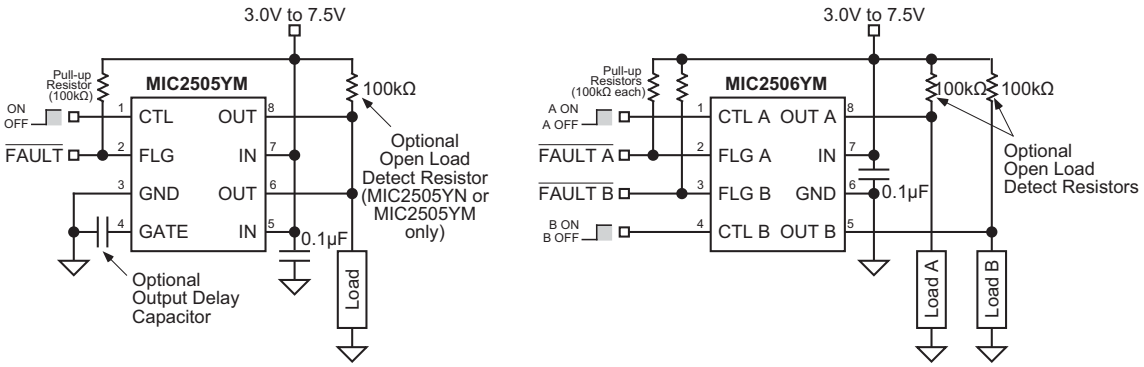


MIC2506
8-Pin SOIC (M)
(Top View)



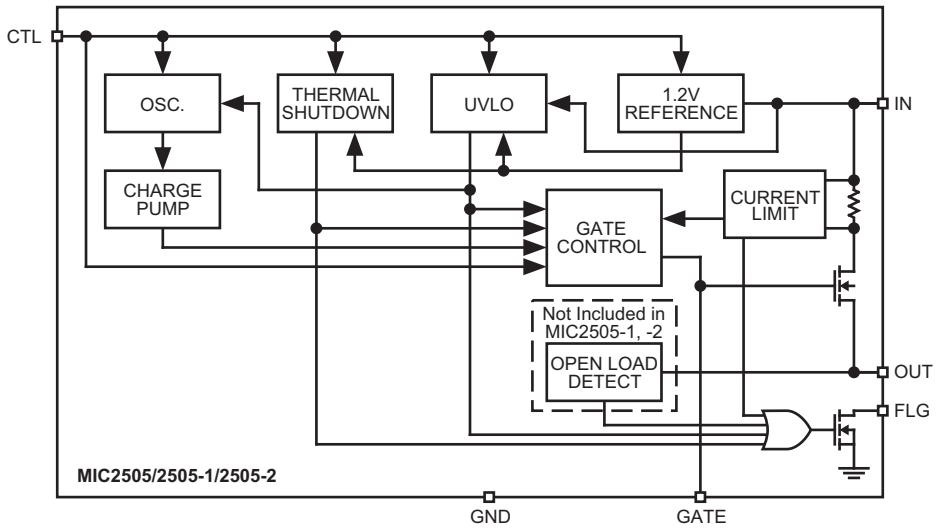
Typical Application Schematics

Single and Dual Switch/Circuit Breakers with Open-Load Detection and Fault Output

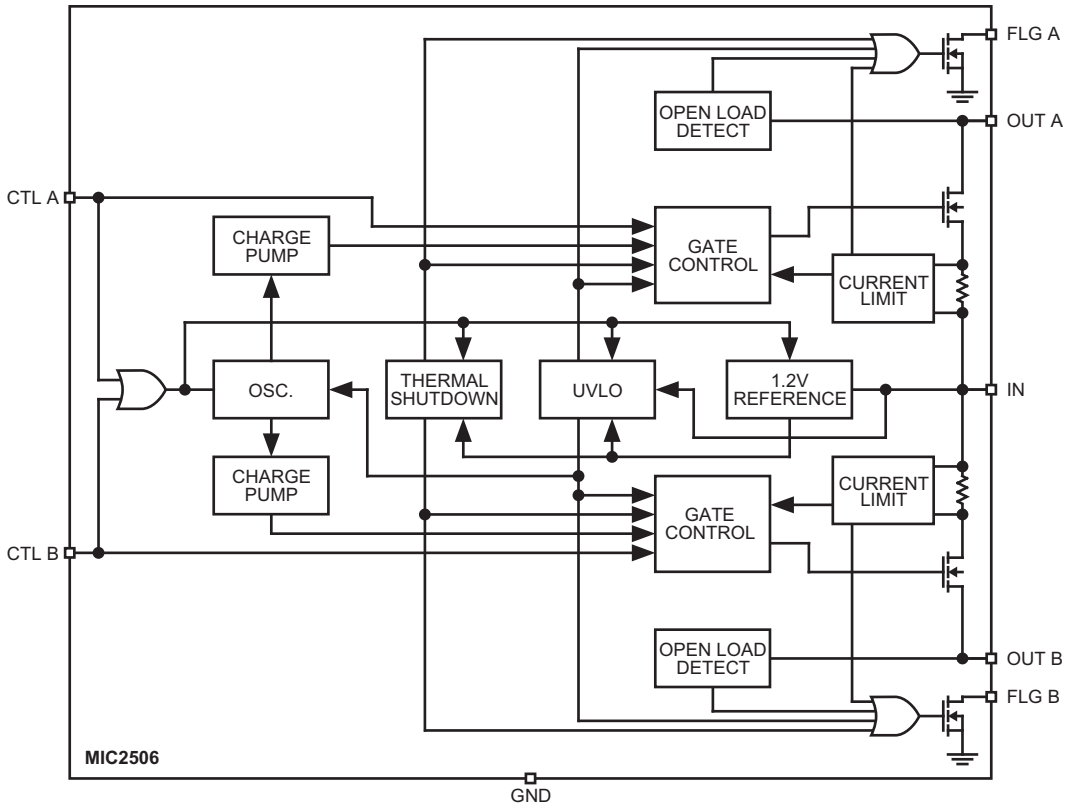


Functional Block Diagrams

MIC2505 Series Block Diagram



MIC2506 Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Supply Voltage (V_{IN}) ..... +8.0V
Fault Flag Voltage (V_{FLG}) ..... +7.5V
Fault Flag Current (I_{FLG}) ..... 50 mA
Output Voltage (V_{OUT}) 7.5V
Output Current (I_{OUT}) ..... Internally Limited
Gate Voltage (V_{GATE}) ..... V_{IN} + 15V
Control Input (V_{CTL}) ..... –0.3V to +15V

Operating Ratings ‡

Supply Voltage (V_{IN}) ..... +3.0V to +7.5V

† **Notice:** Stresses above those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

‡ **Notice:** The device is not guaranteed to function outside its operating ratings.

TABLE 1-1: ELECTRICAL CHARACTERISTICS

Electrical Characteristics: V_{IN} = +5V, GATE = open, T_A = 25°C, **bold** values are valid for –40°C ≤ T_A ≤ +85°C, unless noted. **(Note 1)**.

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Supply Current	I_{DD}	—	0.75	**5**	µA	MIC2505-1, MIC2506, V_{CTL} = logic 0, OUT = open.
		—	110	**160**	µA	MIC2505-1, MIC2506, V_{CTL} = logic 1, OUT = open.
		—	110	**160**	µA	MIC2505-2, V_{CTL} = logic 0, OUT = open.
		—	0.75	**5**	µA	MIC2505-2, V_{CTL} = logic 1, OUT = open.
Control Input Voltage	V_{CTL}	—	2.1	2.4	V	V_{CTL} = logic 0 to logic 1 transition
		0.8	1.9	—	V	V_{CTL} = logic 1 to logic 0 transition
Control Input Current	I_{CTL}	—	0.01	**1**	µA	V_{CTL} = logic 0
		—	0.01	**1**	µA	V_{CTL} = logic 1
Control Input Capacitance	C_{CTL}	—	1	—	pF	—
Output MOSFET Resistance	$R_{DS(ON)}$	—	30	50	mΩ	MIC2505 Series, V_{IN} = 5V, T_A = 25°C.
		—	—	**60**	mΩ	MIC2505 Series, V_{IN} = 5V, –40°C < T_A < +85°C.
		—	35	60	mΩ	MIC2505 Series, V_{IN} = 3.3V, T_A = 25°C.
		—	—	**75**	mΩ	MIC2505 Series, V_{IN} = 3.3V, –40°C < T_A < +85°C.
		—	75	125	mΩ	MIC2506, V_{IN} = 5V, T_A = 25°C.
		—	—	**150**	mΩ	MIC2506, V_{IN} = 5V, –40°C < T_A < +85°C.
		—	80	135	mΩ	MIC2506, V_{IN} = 3.3V, T_A = 25°C.
		—	—	**165**	mΩ	MIC2506, V_{IN} = 3.3V, –40°C < T_A < +85°C.
Output Turn-On Delay	t_{ON}	**200**	850	**2000**	µs	MIC2505 Series, R_L = 10Ω, C_{GATE} = 0.
		100	700	**2000**	µs	MIC2506, R_L = 10Ω each output.
Output Turn-On Rise Time	t_R	**500**	3000	**7500**	µs	MIC2505 Series, R_L = 10Ω, C_{GATE} = 0.
		200	2000	**6000**	µs	MIC2506, R_L = 10Ω each output.

Note 1: Devices are ESD protected; however, handling precautions recommended. All limits guaranteed by testing or statistical analysis.

2: MIC2505-1 and -2 versions have no open load detect feature.

3: Open load threshold is the output voltage (V_{OUT}) where FLG becomes active (low) when CTL is low. OUT is pulled high by a 100 kΩ external resistor to V_{IN}.

TABLE 1-1: ELECTRICAL CHARACTERISTICS (CONTINUED)

Electrical Characteristics: V_{IN} = +5V, GATE = open, T_A = 25°C, **bold** values are valid for –40°C ≤ T_A ≤ +85°C, unless noted. **(Note 1)**.

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Output Turn-Off Delay	t_{OFF}	—	0.7	**20**	µs	MIC2505 Series, R_L = 10Ω, C_{GATE} = 0.
		—	0.8	**20**	µs	MIC2506, R_L = 10Ω each output.
Output Turn-Off Fall Time	t_F	—	1.5	**20**	µs	MIC2505 Series, R_L = 10Ω, C_{GATE} = 0.
		—	0.7	**20**	µs	MIC2506, R_L = 10Ω each output.
Output Leakage Current	I_{LKG}	—	—	**10**	µA	—
Current Limit Threshold	I_{LIM}	**2**	4	—	A	MIC2505 Series
		1	2	3	A	MIC2506
Open Load Threshold (Note 2)	V_{OPENL_TH}	**0.5**	1	**1.5**	V	V_{CTL} = logic low, Note 3
Overtemperature Shutdown Threshold	T_{SD}	—	135	—	°C	T_J increasing
		—	125	—	°C	T_J decreasing
Error Flag Output Resistance	R_{FLG}	—	10	**25**	Ω	V_{IN} = 5V, I_L = 10 mA
		—	15	**40**	Ω	V_{IN} = 3.3V, I_L = 10 mA
Error Flag Off Current	I_{FLG_OFF}	—	0.01	**1**	µA	V_{FLAG} = 5V
UVLO Threshold	V_{UVLO_TH}	**2.2**	2.5	**3.0**	V	V_{IN} increasing
UVLO Hysteresis	$V_{UVLO\_TH\_HYST}$	—	215	—	mV	MIC2505
		—	235	—	mV	MIC2506

Note 1: Devices are ESD protected; however, handling precautions recommended. All limits guaranteed by testing or statistical analysis.

2: MIC2505-1 and -2 versions have no open load detect feature.

3: Open load threshold is the output voltage (V_{OUT}) where FLG becomes active (low) when CTL is low. OUT is pulled high by a 100 kΩ external resistor to V_{IN}.

TEMPERATURE SPECIFICATIONS

Parameters	Sym.	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Ambient Operating Temperature	T_A	–40	—	+85	°C	—
Storage Temperature Range	T_S	–65	—	+150	°C	—
Lead Temperature	—	—	—	+260	°C	Soldering, 5s
Package Thermal Resistances						
Thermal Resistance, SOIC	θ_{JA}	—	160	—	°C/W	—

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

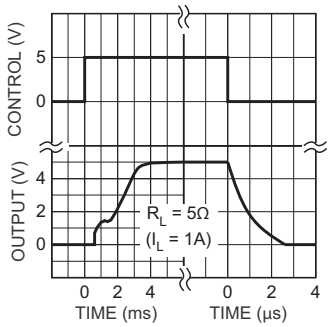


FIGURE 2-1: *MIC2505 Turn-On, Turn-Off Characteristics.*

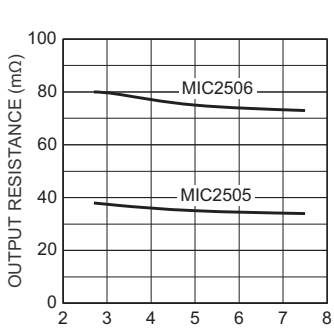


FIGURE 2-2: *Output On Resistance vs. Supply Voltage.*

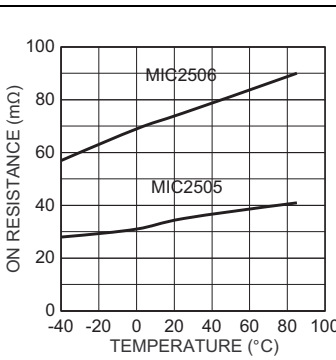


FIGURE 2-3: *Output On Resistance vs. Temperature.*

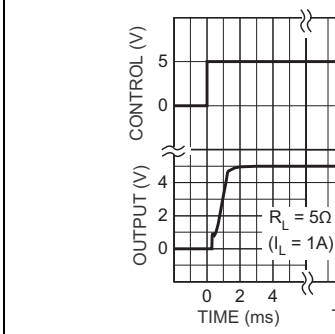


FIGURE 2-4: *MIC2506 Turn-On, Turn-Off Characteristics.*

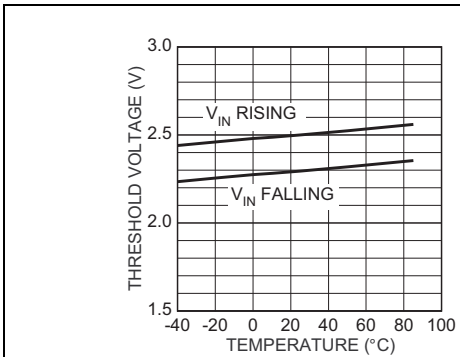


FIGURE 2-5: *UVLO Threshold Voltage vs. Temperature.*

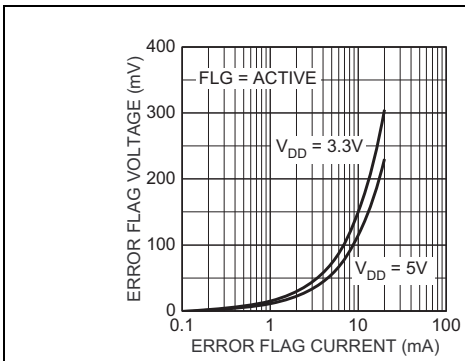


FIGURE 2-6: *Error Flag Voltage vs. Error Flag Current.*

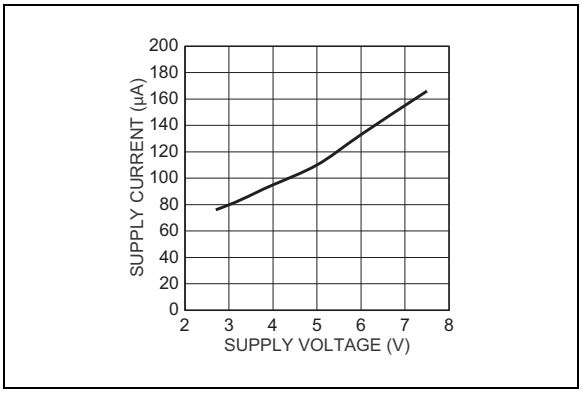


FIGURE 2-7: *On-State Supply Current vs. Supply Voltage.*

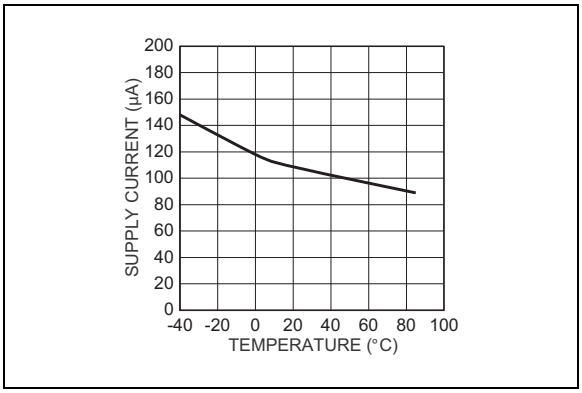


FIGURE 2-10: *On-State Supply Current vs. Temperature.*

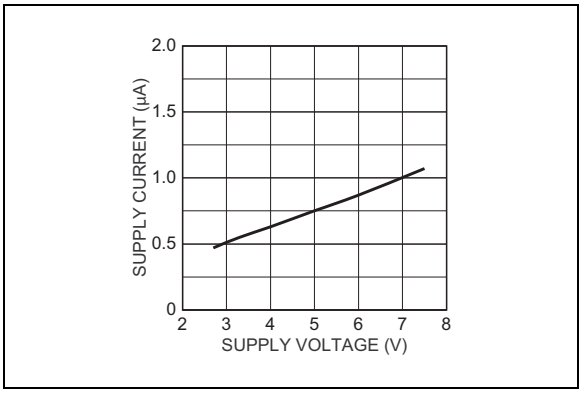


FIGURE 2-8: *Off-State Supply Current vs. Supply Voltage.*

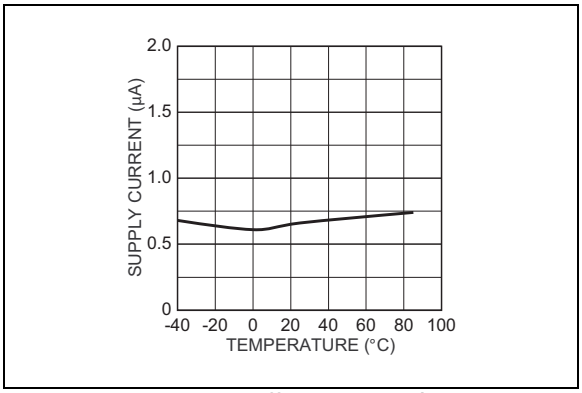


FIGURE 2-11: *Off-State Supply Current vs. Temperature.*

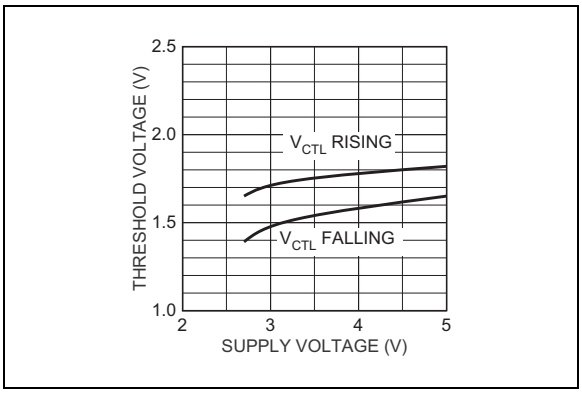


FIGURE 2-9: *Control Threshold vs. Supply Voltage.*

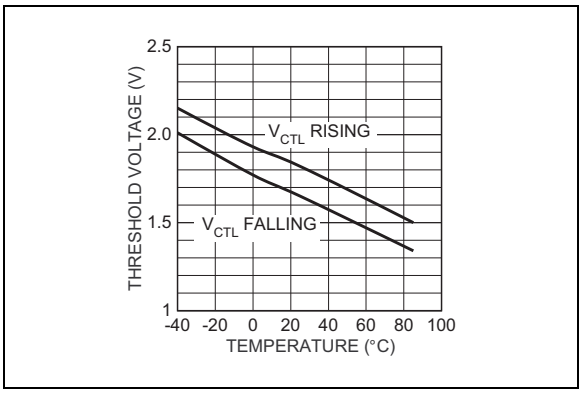


FIGURE 2-12: *Control Threshold vs. Temperature.*

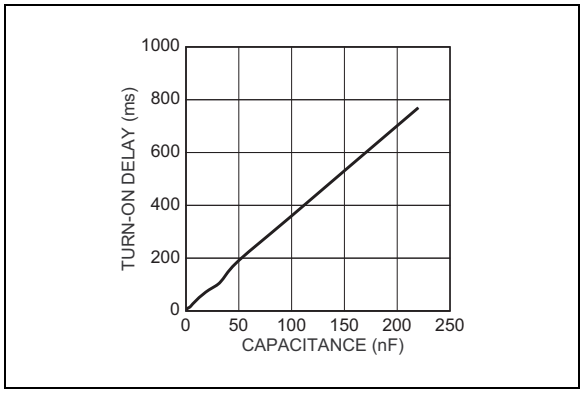


FIGURE 2-13: *MIC2505 Turn-On Delay with External Gate Capacitance*.

3.0 TEST CIRCUITS

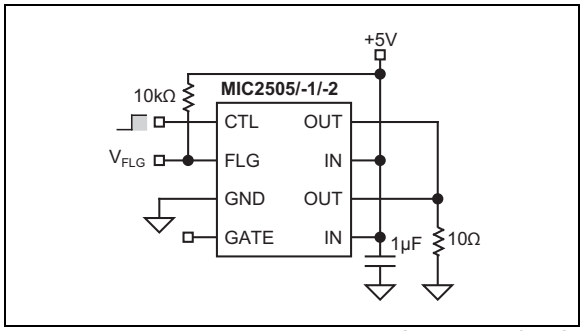


FIGURE 3-1: *MIC2505 Series Test Circuit.*

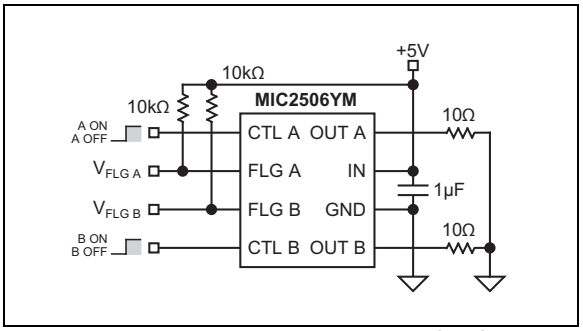


FIGURE 3-2: *MIC2506 Test Circuit.*

4.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 4-1.

TABLE 4-1: PIN FUNCTION TABLE

Pin Number MIC2505 Series	Pin Number MIC2506	Pin Name	Description
1	1, 4	CTL (A/B)	Control (Input): TTL-compatible control input. MIC2505, MIC2505-1, and MIC2506 are active-high. MIC2505-2 is active-low.
2	2, 3	FLG (A/B)	Fault Flag (Output): Active-low, open-drain output. If CTL is low, indicates open load. If CTL is high, indicates current limit, thermal shutdown, or UVLO. MIC2505-1 and -2 do not support open-load detect.
3	6	GND	Ground: Return.
4	—	GATE	Output MOSFET Gate: Open for fastest rise time. Connect capacitor to ground to slow rise time. (See Figure 2-13)
5, 7	7	IN	Supply Input: Output MOSFET drain. Also supplies IC's internal circuitry. Connect to supply. MIC2505 series only: Pins 5 and 7 must be externally connected together.
6, 8	8, 5	OUT (A/B)	Switch Output: Output MOSFET source. Typically connect to switched side of load. Output voltage can be pulled above input voltage in off mode. MIC2505 series only: Pins 6 and 8 must be externally connected together.

5.0 FUNCTIONAL DESCRIPTION

The MIC2505-series and MIC2506 are high-side N-Channel switches. The MIC2505, MIC2505-1, and MIC2506 have active-high enable inputs. The MIC2505-2 has an active-low input. Fault conditions inhibit output transistor turn-on or turn-off when enabled.

5.1 Control Input

CTL (control input) activates the oscillator, thermal shutdown, UVLO, 1.2V reference, and gate control circuits. If there are no fault conditions, the output MOSFET turns on when enabled.

5.2 Reference

A 1.2V bandgap reference supplies a regulated voltage to the thermal shutdown and undervoltage lockout circuits. The reference is only active when CTL is enabled.

5.3 Oscillator/Charge Pump

The oscillator produces an 80 kHz square wave output that drives the charge pump. The oscillator is enabled when CTL is active.

The charge pump is a voltage quintupler (5x). The charge pump capacitors are self contained.

5.4 Gate Control

The gate control circuit charges the output MOSFET gate from the charge pump output or discharges the MOSFET gate to ground as determined by CTL, thermal shutdown, or undervoltage lockout (UVLO).

An optional, external capacitor may be connected to the MIC2505 GATE to lengthen the rise time. This slows the turn on of the MOSFET output switch. (See Figure 2-13) Because this pin connects directly to the MOSFET gate, use ESD precautions when contacting components connected to this pin. Leakage resistance may increase turn on times.

5.5 Input and Output

IN (input) is the supply connection to the logic circuitry and the drain of the output MOSFET. OUT (output) is the source of the output MOSFET. In a typical circuit, current flows through the switch from IN to OUT toward the load.

The output MOSFET and driver circuitry are also designed to allow the MOSFET source to be externally forced to a higher voltage than the drain ($V_{OUT} > V_{IN}$) when the output switch is off and V_{IN} > UVLO minimum. In this situation, the MIC2505/6 avoids undesirable drain to body diode reverse current flow by grounding the body when the switch is off. The conventional method for optimum turn-on threshold has the source connected to the body. This would allow a large current to flow when $V_{SOURCE} > V_{DRAIN}$ + 0.6V.

5.5.1 MIC2505 SERIES ONLY

Duplicate IN and OUT leads are not internally connected. Connect both IN pins to the supply. Connect both OUT leads to the load.

5.6 Thermal Shutdown

Thermal shutdown shuts off the output MOSFET and signals the fault flag if the die temperature exceeds 135°C. 10°C of hysteresis prevents the switch from turning on until the die temperature drops to 125°C.

Overtemperature detection functions only when the control input is enabled (output MOSFET is on). Both MIC2506 outputs are shut off during overtemperature, and both flags will go low.

5.7 Undervoltage Lockout

UVLO (undervoltage lockout) prevents the output MOSFET from turning on until V_{IN} (input voltage) exceeds 2.5V typical. After the switch turns on, if V_{IN} drops below 2.3V typical, UVLO shuts off the output MOSFET and turns the fault flag on (active-low) until V_{IN} drops below 1.5V.

Undervoltage detection functions only when the control input is enabled (output MOSFET is on).

5.8 Overcurrent Limit

The overcurrent limit is preset internally. The preset level prevents damage to the output MOSFET, but allows a minimum current of 2A through the output MOSFET of the MIC2505-series and 1A for each output MOSFET of the MIC2506. Output current is monitored by sensing the voltage drop across the output MOSFET drain metal resistance.

Overcurrent detection functions only when the control input is enabled (output MOSFET is on) and V_{IN} is above the UVLO threshold.

5.9 Open-Load Detection

Open-load detection is available only on the MIC2505 and MIC2506. The open-load detection feature is not included in the MIC2505-1 or -2 versions.

Open-load detection indicates the absence of an output load by activating the fault flag. Open-load detection is optional and is enabled by connecting a high-value pull-up resistor between IN and OUT. If there is no load, the circuit detects a high OUT (output) voltage (typically ≥1V) and signals the fault flag. Under normal conditions, the low resistance of a typical load pulls OUT low. Open-load detection functions only when the control input is low (output MOSFET is off).

5.10 Fault Flag

FLG is an N-channel, open-drain MOSFET output. The fault flag is active (low) for one or more of the following conditions: open load (except MIC2505-1 and -2 versions), undervoltage, current limit, or thermal shutdown. The flag output MOSFET is capable of sinking a 10 mA load to typically 100 mV above ground.

6.0 APPLICATION INFORMATION

6.1 Supply Filtering

A 0.1 µF to 1 µF bypass capacitor from IN to GND, located at the device is strongly recommended to control supply transients. Without a bypass capacitor, an output short may cause sufficient ringing on the input (from supply lead inductance) to destroy the internal control circuitry.

Input transients must not exceed the absolute maximum supply voltage ($V_{IN(MAX)}$ = 7.5V) even for a short duration.

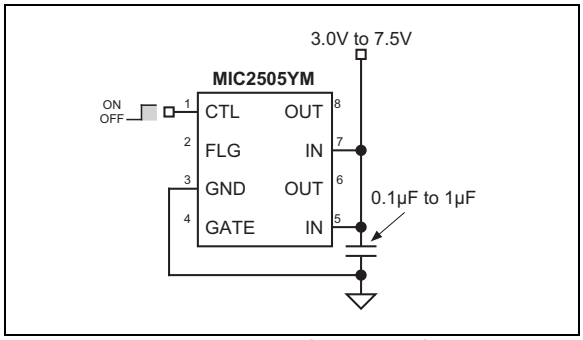


FIGURE 6-1: *Supply Bypassing.*

The bypass capacitor may be omitted only if board design precautions are followed, such as using extremely short supply leads or power and ground planes.

6.2 Control Input

CTL must be driven logic high or logic low, or be pulled high or low for a clearly defined input. Floating the input may cause unpredictable operation. Add a diode clamp if negative spikes may occur. See Figure 6-2.

6.3 Open-Load Detection

Refer to the Typical Application Schematics. Open-load detection is available only on the MIC2505 and MIC2506. For USB power distribution applications, the open-load detection feature is not included in the MIC2505-1 or -2 versions.

The optional open-load detection resistor supplies a small pull-up current to the load when the output switch is off. A 100 kΩ resistor will draw 50 µA from a 5V supply. Normally, the load dominates, pulling OUT low. If the load is absent, the optional resistor pulls OUT high, activating the fault flag if CTL is off.

When a load is switched off with CTL, capacitance on the output may cause the open-load function to pull the flag low until the capacitor is discharged below approximately 2.4V.

Omit the pull-up resistor when open load detection is not required and for minimum off-state supply current.

6.4 Power Bus Switch

The MIC2505/6 family features a MOSFET reverse current flow prevention circuit. This prevents current from flowing backwards (from OUT to IN) when CTL is disabled as long as V_{IN} is above UVLO minimum. In Figure 6-2, when U1 is on and U2 is off, this feature prevents current flow from the load (5V) backward through U2 to the 3.3V supply. If a discrete MOSFET and driver were used, the MOSFET's internal body diode would short the 5V load to the 3.3V supply.

FLG will be active (low) on any switch that is off whenever the load voltage is greater than the open load threshold (approximately 1V) except for MIC2505-1 and MIC2505-2.

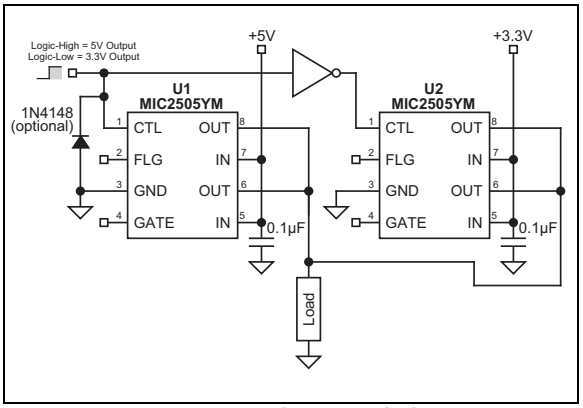


FIGURE 6-2: *5V/3.3V Switch Concept.*

This circuit's function would otherwise require a dual driver, two MOSFETs, plus two diodes (or a dual driver plus four MOSFETs).

6.5 Hot Plug-In Applications (Soft-Start)

The MIC2505/6 family can be used to protect the socket-side and card-side of a supply circuit from transients caused when a capacitive load is connected to an active supply.

The switch presents a high impedance when off, and slowly becomes a low impedance as it turns on. This reduces the inrush current and related voltage drop that result from charging a capacitive load.

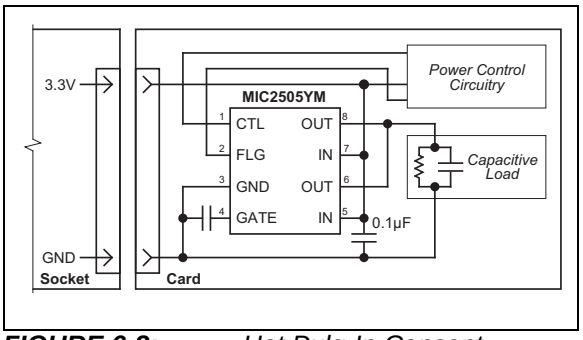


FIGURE 6-3: *Hot Pulg-In Concept.*

A gate capacitor may be added to the MIC2505 to slow the turn on time even more, reducing the inrush current. See Figure 2-13. The UVLO feature ensures that each time the card is removed and V_{IN} = 0 that the gate of the output switch is discharged to zero volts. A controlled turn-on is executed each time a board is plugged in, even with multiple insertions.

6.6 USB Application

Figure 6-4 depicts a low cost and robust implementation of a four-port, self-powered USB hub circuit employing ganged overcurrent protection.

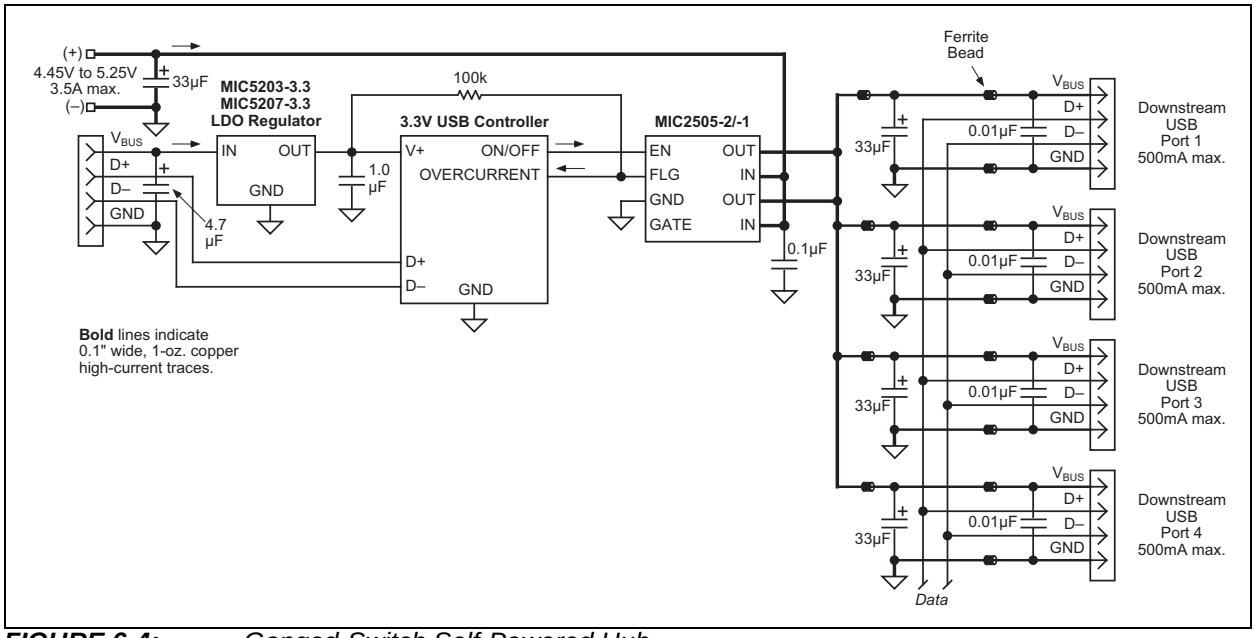


FIGURE 6-4: *Ganged-Switch Self-Powered Hub.*

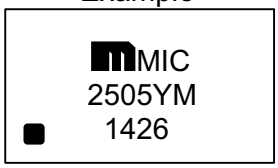
7.0 PACKAGING INFORMATION

7.1 Package Marking Information

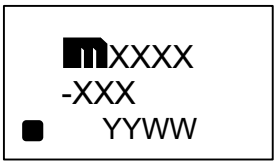
8-Pin SOIC*

for MIC2505

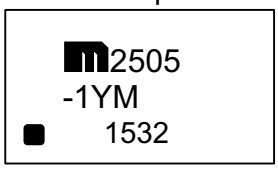
Example



for MIC2505-1/-2

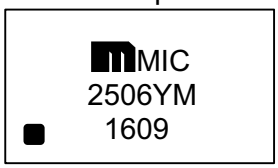


Example



for MIC2506

Example



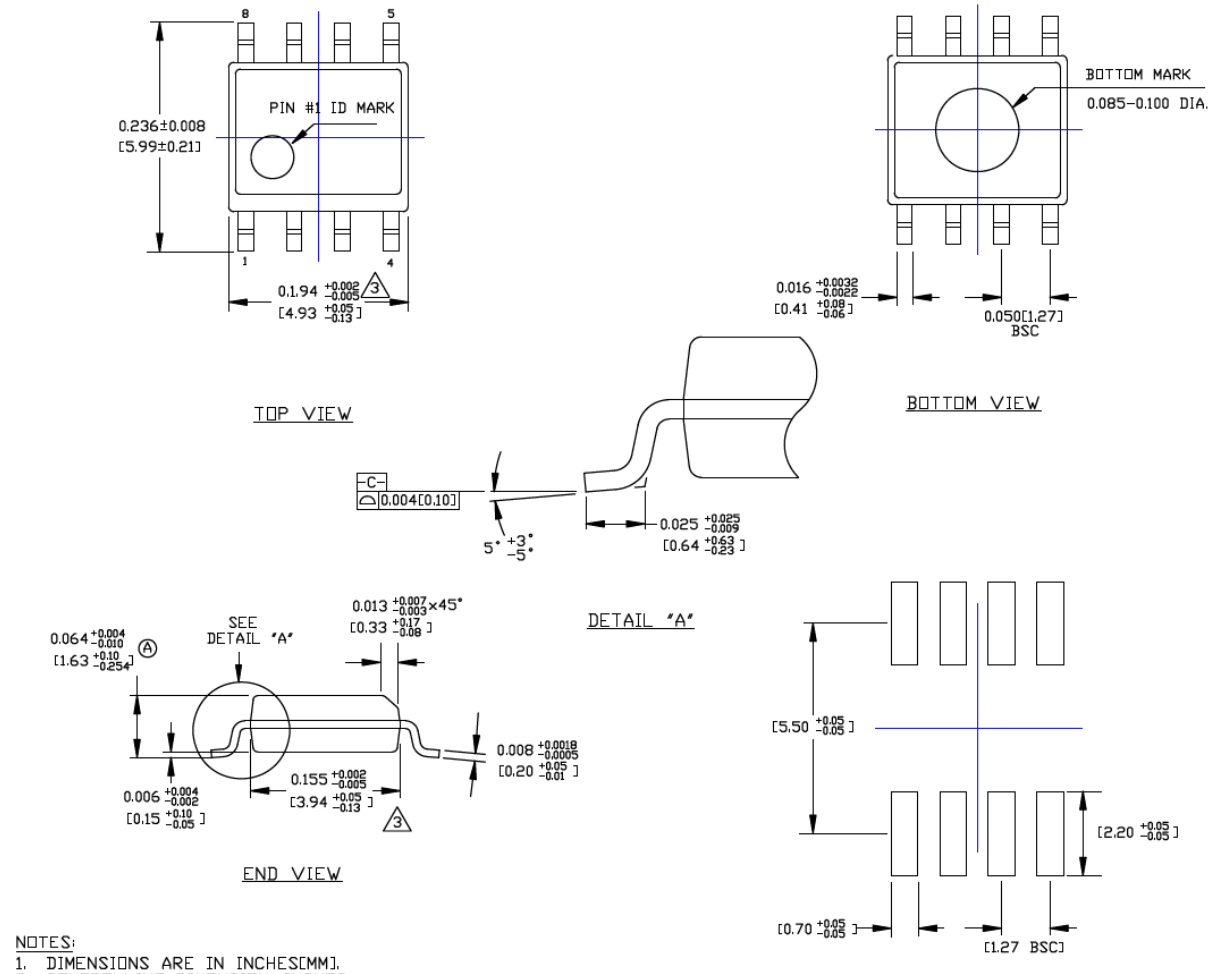
Legend:	XX...X	Product code or customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week ‘01’)
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.
	●, ▲, ▼	Pin one index is identified by a dot, delta up, or delta down (triangle mark).
Note:	In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.	
	Underbar (_) symbol may not be to scale.	

8-Lead SOIC Package Outline and Recommended Land Pattern

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging

TITLE
8 LEAD SOICN PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

DRAWING #	SOICN-8LD-PL-1	UNIT	INCH [MM]



NOTES:
1. DIMENSIONS ARE IN INCHES[MM].
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.010[0.25] PER SIDE.

NOTES:

APPENDIX A: REVISION HISTORY

Revision A (August 2016)

- Converted Micrel document MIC2505/6 to Microchip data sheet DS20005579A.
- Minor text changes throughout.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO.	–	X	X	X	–	X
Device		Control/ Enable and Open-Load Detect	Temperature	Package		Media Type

Device:	MIC2505:		Single 2A High-Side Switch
	MIC2506:		Dual 1A High-Side Switch, (Note 1)
Control/Enable and Open-Load Detect:	Blank	=	Active-High with Open-Load Detect
	1	=	Active-High without Open-Load Detect
	2	=	Active-Low without Open-Load Detect
Temperature:	Y	=	–40°C to +85°C
Package:	M	=	8-Pin SOIC
Media Type:	TR	=	2,500/Reel
	none	=	95/Tube

Note 1: MIC2506 is only available in an Active-High with Open-Load Detect configuration.

Examples:

a) MIC2505YM: Single 2A High-Side Switch, Active-High with Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 95/Tube

b) MIC2505YM-TR: Single 2A High-Side Switch, Active-High with Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 2,500/Reel

c) MIC2505-1YM: Single 2A High-Side Switch, Active-High without Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 95/Tube

d) MIC2505-1YM-TR: Single 2A High-Side Switch, Active-High without Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 2,500/Reel

e) MIC2505-2YM: Single 2A High-Side Switch, Active-Low without Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 95/Tube

f) MIC2505-2YM-TR: Single 2A High-Side Switch, Active-Low without Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 2,500/Reel

g) MIC2506YM: Dual 1A High-Side Switch, Active-High with Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 95/Tube

h) MIC2506YM-TR: Dual 1A High-Side Switch, Active-High with Open-Load Detect, –40°C to +85°C Temp. Range, 8-Pin SOIC, 2,500/Reel

NOTES:

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
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- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

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Microchip received ISO/TS-16949:2009 certification for its worldwide headquarters, design and wafer fabrication facilities in Chandler and Tempe, Arizona; Gresham, Oregon and design centers in California and India. The Company's quality system processes and procedures are for its PIC® MCUs and dsPIC® DSCs, KEELOQ® code hopping devices, Serial EEPROMs, microperipherals, nonvolatile memory and analog products. In addition, Microchip's quality system for the design and manufacture of development systems is ISO 9001:2000 certified.

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