

# VS-MBR6045WTPbF, VS-MBR6045WT-N3

Vishay Semiconductors

ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS				
	V <sub>FM</sub> <sup>(1)</sup>	30 A	T <sub>.1</sub> = 25 °C	0.62				
Maximum forward voltage drop per leg See fig. 1		60 A	1j=25 C	0.75	V			
		30 A	T <sub>J</sub> = 125 °C	0.55				
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	1	mA			
See fig. 2		T <sub>J</sub> = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	150				
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.27	V			
Forward slope resistance	r <sub>t</sub>			7.3	mΩ			
Maximum junction capacitance per leg	CT	$V_R = 5 V_{DC}$ (test signal range	1400	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	7.5	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

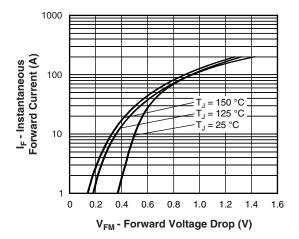
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

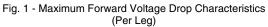
THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C			
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	1.0				
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	0.5	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24				
				6	g			
Approximate weight				0.21	oz.			
Mounting torquo	minimum				kgf ⋅ cm			
Mounting torque	maximum			12 (10)	(lbf · in)			
Marking device			Case style TO-247AC (JEDEC)	MBR60	045WT			

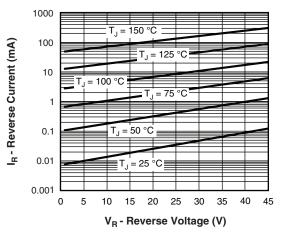


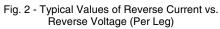
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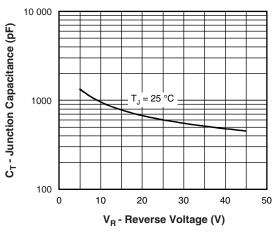
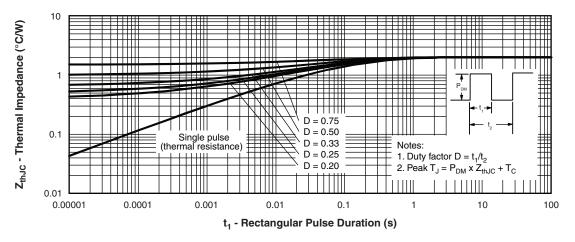


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





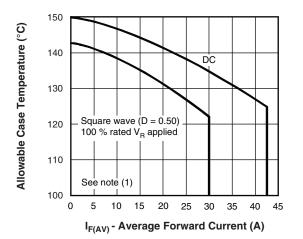
 Revision: 30-Aug-11
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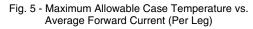
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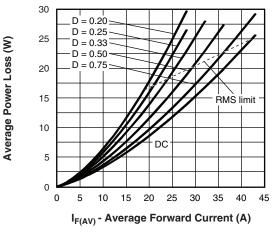


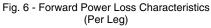
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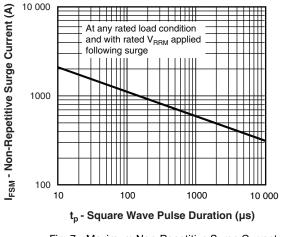
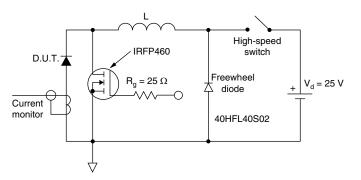


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)





### Note

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<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6);

 $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R \text{ at } V_{R1}$  = 100 % rated  $V_R$ 

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### **ORDERING INFORMATION TABLE**

Device code	VS-	MBR	60	45	WT	PbF			
	1	2	3	4	5	6			
	<ol> <li>Vishay Semiconductors product</li> <li>Schottky MBR series</li> </ol>								
	3       -       Current rating (60 = 60 A)         4       -       Voltage rating (45 = 45 V)         5       -       Circuit configuration:         6       Center tap (dual) TO-247								
	<ul> <li>Environmental digit</li> </ul>								
		• □	PhF = Ic	ad (Ph)	-froo an				

- PbF = Lead (Pb)-free and RoHS compliant
- -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-MBR6045WTPbF	25	500	Antistatic plastic tube						
VS-MBR6045WT-N3	25	500	Antistatic plastic tube						

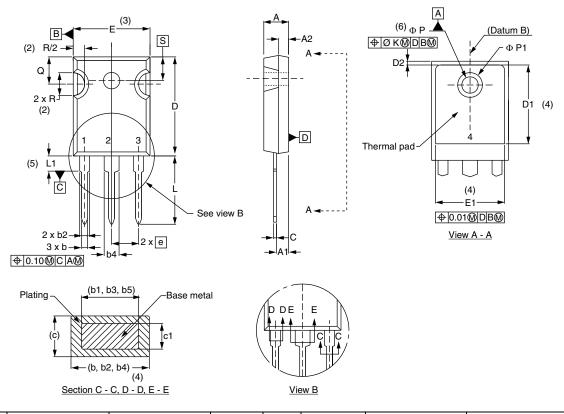
LINKS TO RELATED DOCUMENTS						
Dimensions		www.vishay.com/doc?95223				
Part marking information	TO-247AC PbF	www.vishay.com/doc?95226				
	TO-247AC -N3	www.vishay.com/doc?95007				



**Vishay Semiconductors** 

**TO-247AC** 

### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES	STWBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209		D2	0.51	1.30	0.020	0.051	
A1	2.21	2.59	0.087	0.102		E	15.29	15.87	0.602	0.625	3
A2	1.50	2.49	0.059	0.098		E1	13.72	-	0.540	-	
b	0.99	1.40	0.039	0.055		е	5.46	BSC	0.215	5 BSC	
b1	0.99	1.35	0.039	0.053		ØК	2.	54	0.0	010	
b2	1.65	2.39	0.065	0.094		L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092		L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135		ØΡ	3.56	3.66	0.14	0.144	
b5	2.59	3.38	0.102	0.133		Ø P1	-	6.98	-	0.275	
С	0.38	0.89	0.015	0.035		Q	5.31	5.69	0.209	0.224	
c1	0.38	0.84	0.015	0.033		R	4.52	5.49	0.178	0.216	
D	19.71	20.70	0.776	0.815	3	S	5.51 BSC 0.217 BSC		' BSC		
D1	13.08	-	0.515	-	4						

### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension c

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