

Absolute Maximum Ratings

IN to GND -60V to +70V
 EN to GND -0.3V to ($V_{\text{IN}} + 0.3\text{V}$)
 $\overline{\text{HOLD}}$ to GND -0.3V to ($V_{\text{OUT}} + 0.3\text{V}$)
 SET, WDI, OUT to GND -0.3V to +13.2V
 $\overline{\text{RESET}}$ to GND (Open Drain) -0.3V to +13.2V
 Short-Circuit Duration ($V_{\text{IN}} \leq +14\text{V}$) Continuous
 Maximum Current to Any Pin (Except IN, OUT) $\pm 20\text{mA}$

Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)
 8-Pin SO-EP (derate 19.2mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$) 1538mW
 Operating Temperature Range -40°C to $+125^\circ\text{C}$
 Junction Temperature $+150^\circ\text{C}$
 Storage Temperature Range -65°C to $+150^\circ\text{C}$
 Lead Temperature (soldering, 10s) $+300^\circ\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package Thermal Characteristics (Note 1)

SO-EP

Junction-to-Ambient Thermal Resistance (θ_{JA}) 41°C/W

Junction-to-Case Thermal Resistance (θ_{JC}) 2°C/W

Note 1: Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

Electrical Characteristics

($V_{\text{IN}} = +14\text{V}$, $I_{\text{OUT}} = 1\text{mA}$, $C_{\text{IN}} = 10\mu\text{F}$, $C_{\text{OUT}} = 15\mu\text{F}$, $V_{\text{EN}} = +2.4\text{V}$, $\overline{\text{HOLD}} = \text{open}$, $10\text{k}\Omega$ from $\overline{\text{RESET}}$ to OUT, $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$, unless otherwise noted. Typical specifications are at $T_A = +25^\circ\text{C}$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Voltage Range	V_{IN}	V_{IN} must be at least 1.5V greater than V_{OUT}	6.5		65	V
Reverse Input Current	I_{REVERSE}	$V_{\text{IN}} = -60\text{V}$		0.1	10	μA
Supply Current	I_{Q}	Measured at GND				μA
		$I_{\text{OUT}} = 0\text{mA}$		58	140	
		$I_{\text{OUT}} = 150\text{mA}$		2000		
Shutdown Supply Current	I_{SHDN}	$V_{\text{EN}} \leq +0.4\text{V}$		6	16	μA
REGULATOR						
Guaranteed Output Current	I_{OUT}	$V_{\text{OUT}} = +5\text{V}$	150			mA
Output Voltage	V_{OUT}	SET = GND, $I_{\text{OUT}} = 1\text{mA}$ to 150mA, 5V version	4.8	5	5.2	V
		SET = GND, $I_{\text{OUT}} = 1\text{mA}$ to 150mA, 3.3V version	3.168	3.3	3.432	
		$I_{\text{OUT}} = 5\text{mA}$, adjustable version (MAX5024)	2.5		11	
Dropout Voltage	ΔV_{DO}	$I_{\text{LOAD}} = 150\text{mA}$, $V_{\text{OUT}} = +5\text{V}$ (Note 3)		0.9	1.5	V
Startup Response Time		Rising edge of V_{IN} to V_{OUT} , $R_{\text{L}} = 500\Omega$, SET = GND		400		μs
Line Regulation	$\frac{\Delta V_{\text{OUT}}}{\Delta V_{\text{IN}}}$	$+8\text{V} \leq V_{\text{IN}} \leq +65\text{V}$				mV/V
		+5V version	-1		+1	
		+3.3V version	-0.5		0.5	

Electrical Characteristics (continued)

($V_{IN} = +14V$, $I_{OUT} = 1mA$, $C_{IN} = 10\mu F$, $C_{OUT} = 15\mu F$, $V_{EN} = +2.4V$, $\overline{HOLD} = \text{open}$, $10k\Omega$ from $\overline{RESET}$ to OUT, $T_A = -40^\circ C$ to $+125^\circ C$, unless otherwise noted. Typical specifications are at $T_A = +25^\circ C$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Enable Voltage	V _{EN}	EN = high, regulator on	2.4			V
		EN = low, regulator off			0.4	
Enable Input Current	I _{EN}	V _{EN} = +2.4V		0.5		μA
		V _{EN} = +14V		4		
$\overline{\text{HOLD}}$ Voltage	V _{IL}	Regulator on, EN transition from high to low			0.4	V
$\overline{\text{HOLD}}$ Release Voltage	V _{IH}	EN = low, regulator shuts off	V _{OUT} - 0.4V			V
$\overline{\text{HOLD}}$ Pullup Current	I _{$\overline{\text{HOLD}}$}	Internally connected to OUT		4		μA
SET Reference Voltage	V _{SET}	I _{OUT} = 10mA	1.223	1.248	1.273	V
SET Input Leakage Current	I _{SET}			0.5	100	nA
Load Regulation	$\Delta V_{\text{OUT}} / \Delta I_{\text{OUT}}$	I _{OUT} = 1mA to 150mA		1		mV/mA
Power-Supply Rejection Ratio	PSRR	I _{OUT} = 10mA, f = 100Hz, 500mV _{P-P} , V _{OUT} = +5V		54		dB
Short-Circuit Current	I _{SC}	V _{IN} = +8V	175	300		mA
Thermal-Shutdown Temperature	T _{J(SHDN)}			150		°C
Thermal-Shutdown Hysteresis	ΔT _{J(SHDN)}			20		°C
RESET CIRCUIT						
Reset Threshold	V _{TH}	MAX502_L, SET = GND	4.50	4.625	4.75	V
		MAX502_M, SET = GND	4.25	4.375	4.50	
		MAX502_T, SET = GND	2.970	3.052	3.135	
		MAX502_S, SET = GND	2.805	2.887	2.970	
		MAX5024L/T, SET = Divider (Figure 1) (Note 4)	0.925 x V _{OUT}			
		MAX5024M/S, SET = Divider (Figure 1) (Note 4)	0.875 x V _{OUT}			
Reset Timeout Period	t _{RP}		140	200	260	ms
V _{OUT} to Reset Delay	t _{RD}	V _{OUT} falling		5		μs

Electrical Characteristics (continued)

($V_{\text{IN}} = +14\text{V}$, $I_{\text{OUT}} = 1\text{mA}$, $C_{\text{IN}} = 10\mu\text{F}$, $C_{\text{OUT}} = 15\mu\text{F}$, $V_{\text{EN}} = +2.4\text{V}$, $\overline{\text{HOLD}} = \text{open}$, $10\text{k}\Omega$ from $\overline{\text{RESET}}$ to OUT, $T_{\text{A}} = -40^{\circ}\text{C}$ to $+125^{\circ}\text{C}$, unless otherwise noted. Typical specifications are at $T_{\text{A}} = +25^{\circ}\text{C}$.) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Open-Drain $\overline{\text{RESET}}$ Output Voltage (Note 5)	V_{OL}	$V_{\text{OUT}} \geq +1.0\text{V}$, $I_{\text{SINK}} = 50\mu\text{A}$, $\overline{\text{RESET}}$ asserted			0.3	V
		$V_{\text{OUT}} \geq +2.85\text{V}$, $I_{\text{SINK}} = 1.2\text{mA}$, $\overline{\text{RESET}}$ asserted			0.3	
		$V_{\text{OUT}} \geq +4.25\text{V}$, $I_{\text{SINK}} = 3.2\text{mA}$, $\overline{\text{RESET}}$ asserted			0.4	
Open-Drain $\overline{\text{RESET}}$ Output-Leakage Current	I_{LKG}	$\overline{\text{RESET}}$ not asserted, $V_{\overline{\text{RESET}}} = +11\text{V}$			1.0	μA
WATCHDOG FUNCTION						
Watchdog Timeout Period	t_{WD}		1.12	1.6	2.08	s
WDI Pulse Width	t_{WDI}	(Note 6)		50		ns
WDI Input Voltage	V_{IL}				0.4	V
	V_{IH}		2.4			
WDI Input Current	I_{WDI}	WDI = GND	-1		+1	μA

Note 2: Limits at -40°C are guaranteed by characterization and not production tested.

Note 3: Dropout voltage is defined as $V_{\text{IN}} - V_{\text{OUT}}$ when V_{OUT} is 100mV below the value of V_{OUT} for $V_{\text{IN}} = V_{\text{OUT}} + 3\text{V}$.

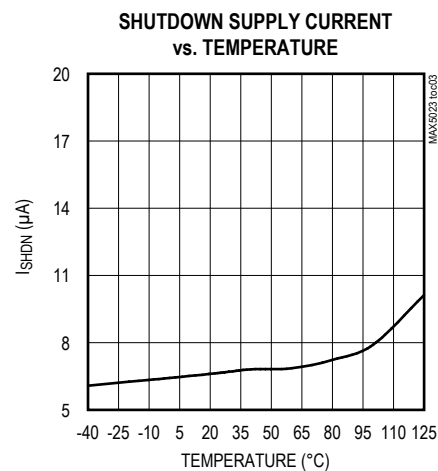
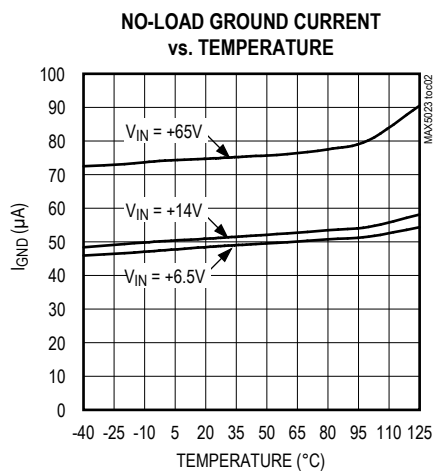
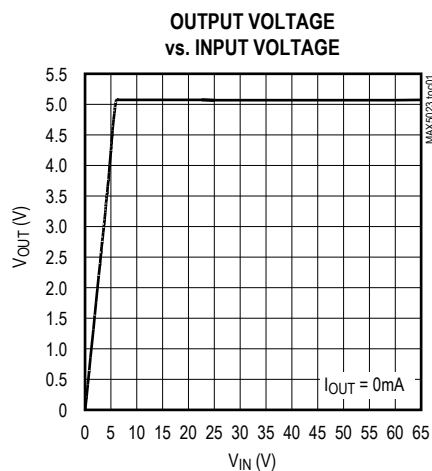
Note 4: $V_{\text{OUT}} = V_{\text{SET}} (1 + R1/R2) = 1.248\text{V} (1 + R1/R2)$.

Note 5: $\overline{\text{RESET}}$ is guaranteed to be in the correct logic state for $V_{\text{OUT}} > +1\text{V}$.

Note 6: Guaranteed by design, not production tested.

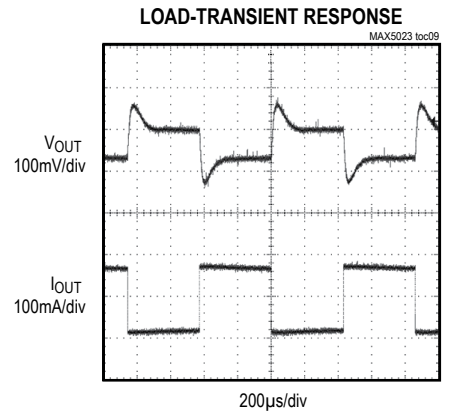
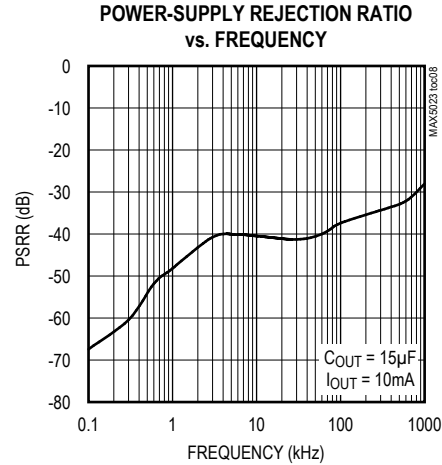
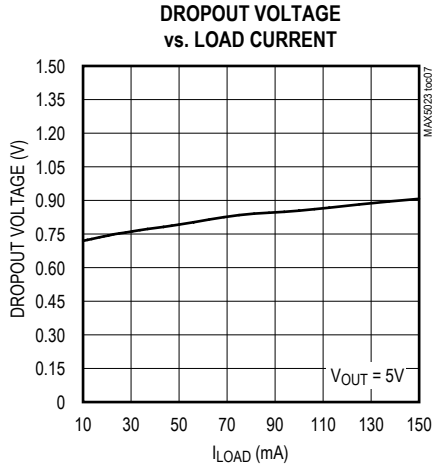
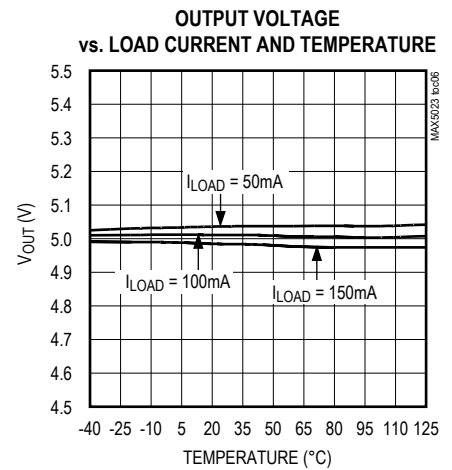
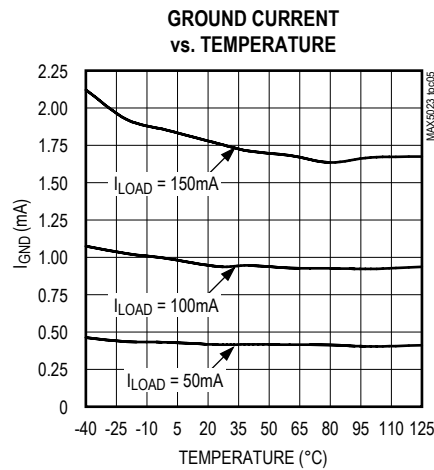
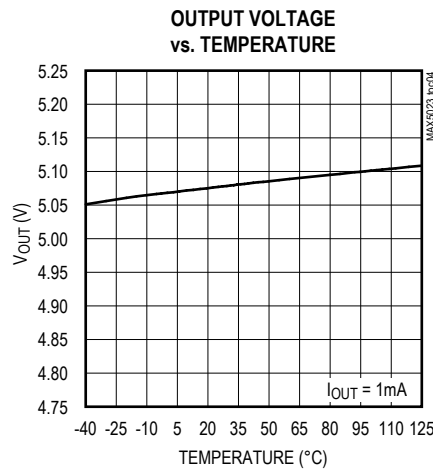
Typical Operating Characteristics

($V_{\text{IN}} = +14\text{V}$, $C_{\text{IN}} = 10\mu\text{F}$, $C_{\text{OUT}} = 15\mu\text{F}$, $V_{\text{EN}} = +2.4\text{V}$, $V_{\text{OUT}} = +5\text{V}$, SET = GND, $T_{\text{A}} = +25^{\circ}\text{C}$, unless otherwise specified.)

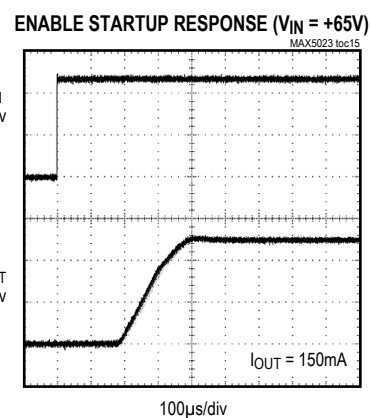
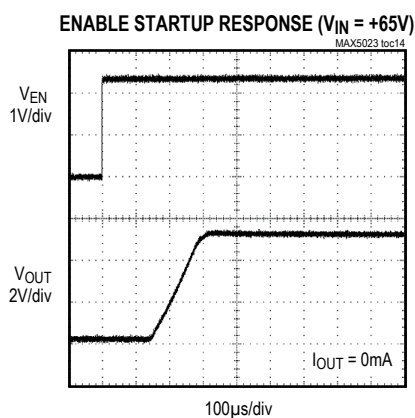
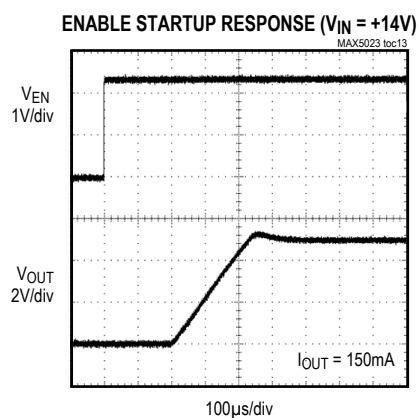
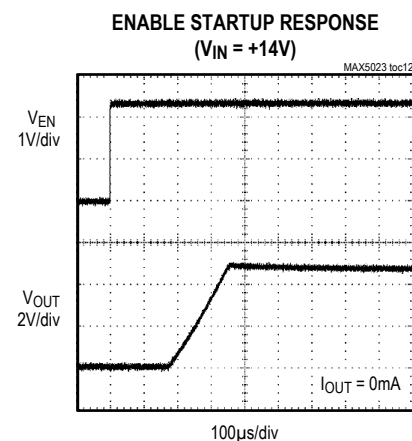
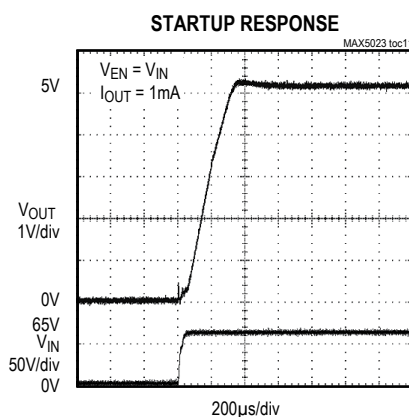
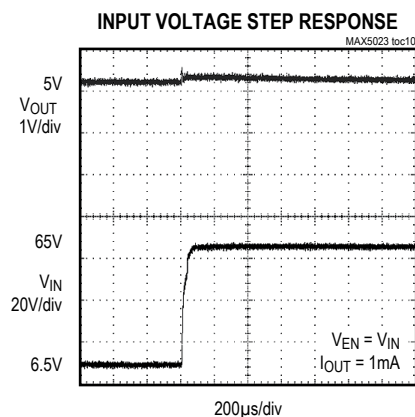


Typical Operating Characteristics (continued)

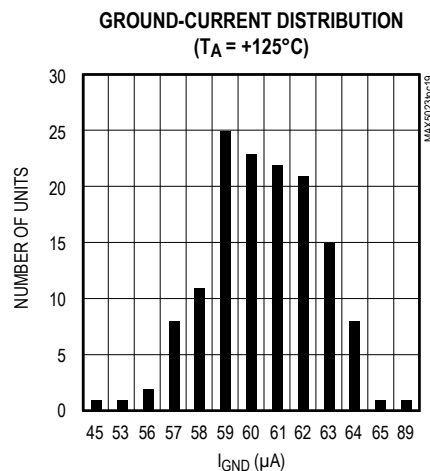
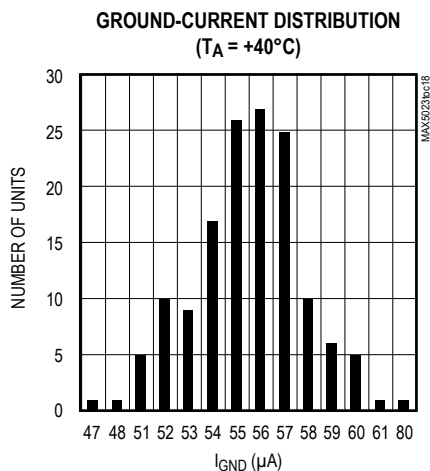
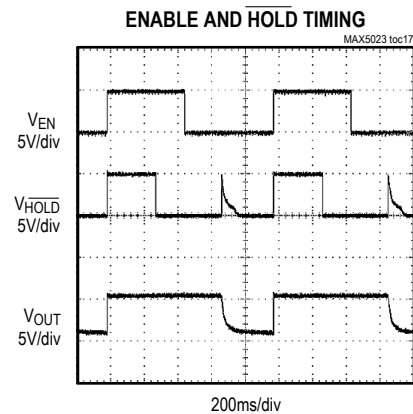
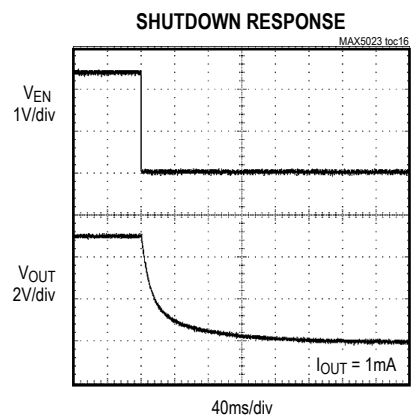
($V_{IN} = +14V$, $C_{IN} = 10\mu F$, $C_{OUT} = 15\mu F$, $V_{EN} = +2.4V$, $V_{OUT} = +5V$, SET = GND, $T_A = +25^\circ C$, unless otherwise specified.)



Typical Operating Characteristics (continued)

(V_{IN} = +14V, C_{IN} = 10 μ F, C_{OUT} = 15 μ F, V_{EN} = +2.4V, V_{OUT} = +5V, SET = GND, T_A = +25°C, unless otherwise specified.)

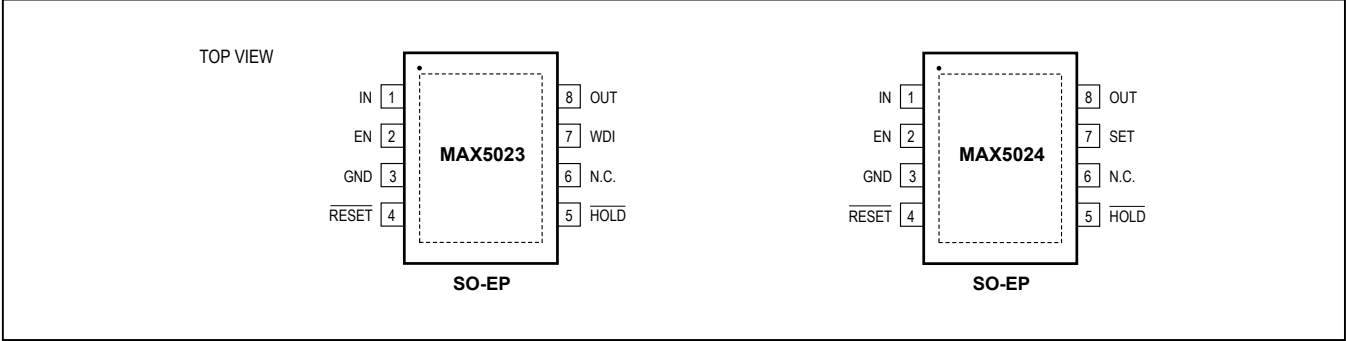
Typical Operating Characteristics (continued)

(V_{IN} = +14V, C_{IN} = 10 μ F, C_{OUT} = 15 μ F, V_{EN} = +2.4V, V_{OUT} = +5V, SET = GND, T_A = +25°C, unless otherwise specified.)

MAX5023/MAX5024

65V, Low-Quiescent-Current, High-Voltage Linear
Regulators with μ P Reset and Watchdog Timer

Pin Configurations



Pin Description

PIN		NAME	FUNCTION
MAX5023	MAX5024		
1	1	IN	Regulator Input. Supply voltage ranges from +6.5V to +65V. Bypass with a 10 μ F capacitor to GND.
2	2	EN	Enable Input. Force EN high to turn on the regulator. Pull EN low and force $\overline{\text{HOLD}}$ high (or open circuit) to place the device in shutdown mode. Internally connected to ground through a 5M Ω resistor.
3	3	GND	Ground. GND also functions as a heatsink. Solder to large pads or the circuit-board ground plane to maximize thermal dissipation.
4	4	$\overline{\text{RESET}}$	Active-Low Open-Drain Reset Output. $\overline{\text{RESET}}$ remains low while V_{OUT} is below the reset threshold or when WDI is not pulsed within 1.6s. $\overline{\text{RESET}}$ remains low for the duration of the reset timeout period after the reset conditions are terminated.
5	5	$\overline{\text{HOLD}}$	Regulator Hold Input. When $\overline{\text{HOLD}}$ is forced low, the regulator stores the on state of the output allowing the regulator to function even if EN is pulled low. To shutdown the regulator, release $\overline{\text{HOLD}}$ after EN is pulled low. If $\overline{\text{HOLD}}$ is unused, either float $\overline{\text{HOLD}}$ or connect to OUT. Internally connected to OUT through a 4 μ A pullup current source (see Table 1, Truth Table).
6	6	N.C.	No Connection. Not internally connected.
7	—	WDI	Watchdog Timer Input (MAX5023 only). The watchdog timer asserts a reset if WDI does not transition within the 1.6s watchdog timeout period. WDI cannot be disabled.
8	8	OUT	Regulator Output. Fixed (+3.3V or +5V) or adjustable (+2.5V to +11V). Bypass with a 15 μ F capacitor (min).
—	7	SET	Feedback Input for Setting the Output Voltage. Connect to GND to set the output voltage to the preset fixed value (+3.3V or +5V). Connect to an external resistor-divider network for adjustable output operation (MAX5024 only).
—	—	EP	Exposed Pad. Connect pad to GND.

Detailed Description

The MAX5023/MAX5024 high-voltage linear regulators include an integrated μ P reset circuit and watchdog timer/adjustable output voltage. The devices guarantee 150mA load drive and are available with preset output voltages of +3.3V or +5V. The MAX5023 features a watchdog timer (WDI) with a 1.6s timeout period. The MAX5024 offers an adjustable output voltage using an external resistive-divider network between SET and OUT. The internal reset circuit monitors the regulator output voltage and asserts a reset output when the regulator output falls below the μ P supply tolerance. Other features include reverse-voltage protection to -60V, enable (EN) and hold (HOLD) regula-

tor control inputs, 16 μ A (max) shutdown current, short-circuit protection, and thermal shutdown.

Regulator

The regulator accepts an input voltage range from +6.5V to +65V. The MAX5023/MAX5024 offer fixed output voltages of +3.3V and +5V. The MAX5024 also features an adjustable output voltage that is implemented with an external resistive-divider network connected between OUT, SET, and GND (Figure 1). The MAX5024 automatically determines the feedback path depending on the voltage at SET. Featured characteristics include reverse-voltage protection to -60V and enable and hold regulator control inputs. The *Typical Operating Circuit* shows a self-holding configuration for the MAX5023.

Functional Diagram

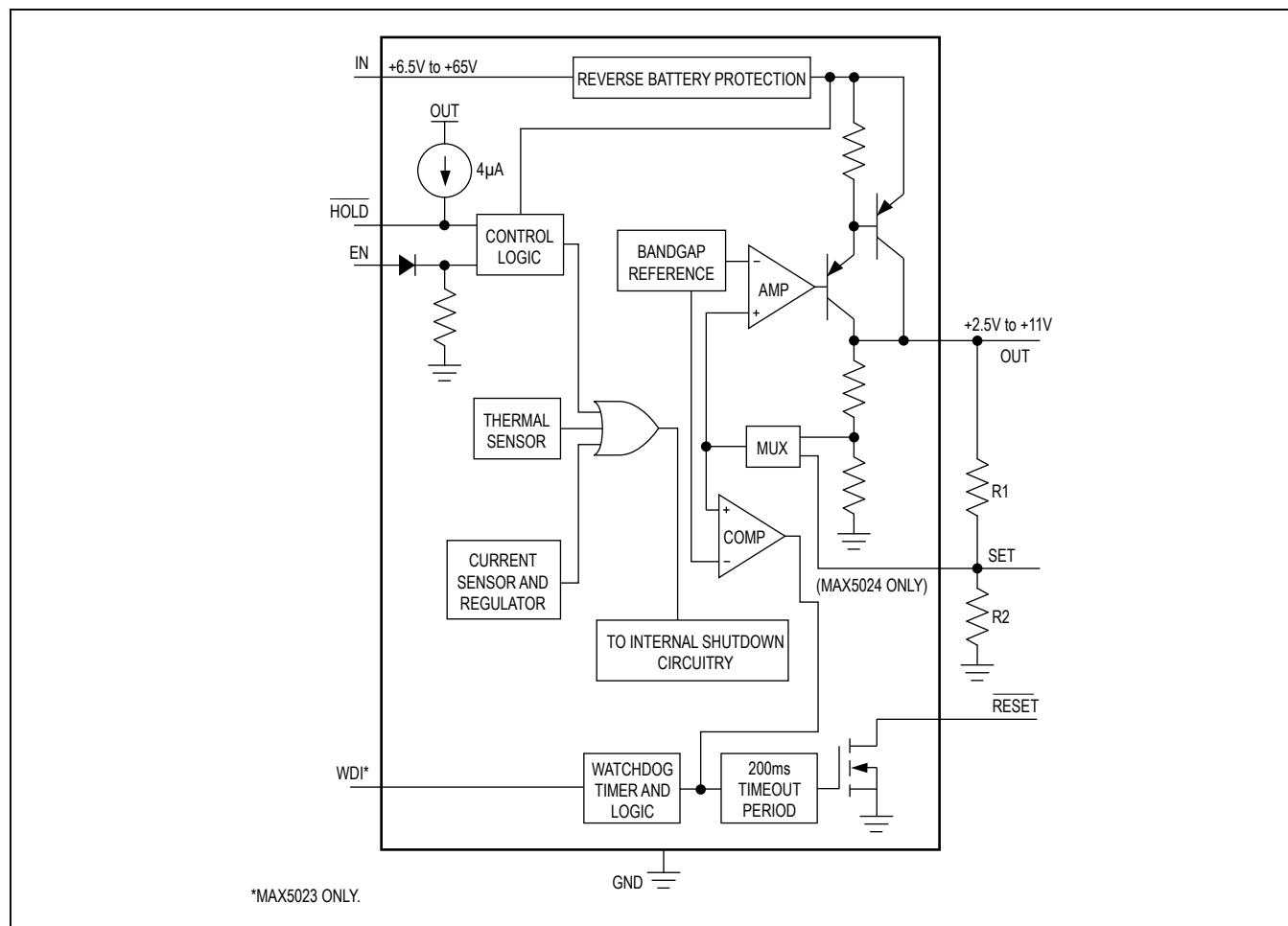


Figure 1. Functional Diagram

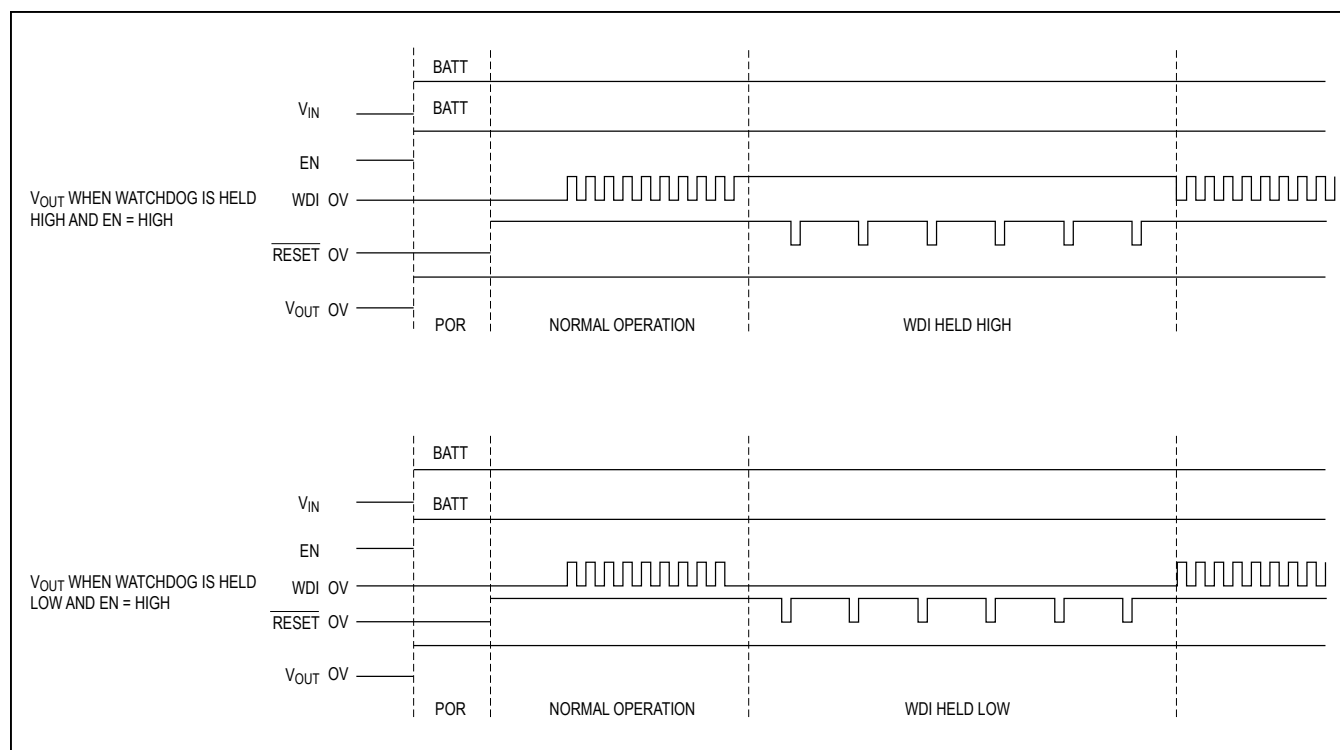


Figure 2. Watchdog Operation Timing Diagram

Reset Output

The reset supervisor circuit is fully integrated in the MAX5023/MAX5024 and uses the same reference voltage as the regulator. $\overline{\text{RESET}}$ asserts during power-up/down and brownout conditions. $\overline{\text{RESET}}$ goes low if V_{OUT} drops below the preset output voltage threshold, and remains low for 200ms (reset timeout period, t_{RP}) after V_{OUT} rises above the reset voltage threshold. For the MAX5023 only, $\overline{\text{RESET}}$ also asserts when WDI does not transition for 1.6s (watchdog timeout period, t_{WD}). Two supply tolerance reset thresholds, -7.5% and -12.5%, are available for each device type.

Watchdog Timer (MAX5023 only)

A watchdog timer asserts $\overline{\text{RESET}}$ if the watchdog input (WDI) is not toggled for 1.6s (watchdog timeout period, t_{WD}). $\overline{\text{RESET}}$ remains low for 200ms (reset timeout period, t_{RP}). If the watchdog is not updated for lengthy periods of time, the reset output appears as a pulse train, asserted for 200ms, deasserted for 1.6s, until WDI is toggled again. Once $\overline{\text{RESET}}$ asserts, it stays low for the entire reset timeout period ignoring any WDI transition during the reset timeout period. Figure 2 shows the

Watchdog Operation Timing Diagram. To prevent the watchdog from asserting $\overline{\text{RESET}}$, toggle WDI with a valid rising or falling edge prior to t_{WD} (min) = 1.12s. The watchdog counter clears when WDI toggles prior to t_{WD} or when $\overline{\text{RESET}}$ asserts. The watchdog resumes counting after $\overline{\text{RESET}}$ deasserts.

Enable and Hold Inputs

The MAX5023/MAX5024 support two logic inputs, EN (active high) and $\overline{\text{HOLD}}$ (active low), making these devices “automotive friendly.” For example, the ignition drives EN high, the regulator turns on and remains on even if EN goes low, as long as $\overline{\text{HOLD}}$ is forced low and stays low after initial regulator power-up. This feature makes it possible to implement a self-holding circuit without external components. Release $\overline{\text{HOLD}}$ (an internal current source connects HOLD to OUT) to turn the regulator off.

Force EN low and $\overline{\text{HOLD}}$ high to place the MAX5023/MAX5024 into shutdown mode. Shutdown mode draws less than 16 μ A of supply current. Table 1 shows the state of the regulator output with respect to the voltage level at

EN and $\overline{\text{HOLD}}$. Figure 3 shows the timing diagram for the enable and hold functions. Connecting $\overline{\text{HOLD}}$ to OUT or floating $\overline{\text{HOLD}}$ allows the EN input to act as a standard ON/OFF switch for the regulator output.

Thermal Protection

When the junction temperature exceeds $T_J = +150^\circ\text{C}$, an internal thermal sensor signals the shutdown logic that turns off the pass transistor and allows the IC to cool. The thermal sensor turns the pass transistor on again after the IC's junction temperature cools by 20°C , resulting in a cycled output during continuous thermal-overload conditions. Thermal protection protects the MAX5023/

MAX5024 in the event of fault conditions. For continuous operation, do not exceed the absolute maximum junction temperature rating of $T_J = +150^\circ\text{C}$.

Applications Information

Output-Voltage Selection (MAX5024 only)

The MAX5024 features Dual Mode™ operation; it operates in either a preset voltage mode or an adjustable mode. In preset voltage mode, internal trimmed feedback resistors set the MAX5024's internal linear regulator to $+3.3\text{V}$ or $+5\text{V}$ (see the *Selector Guide*).

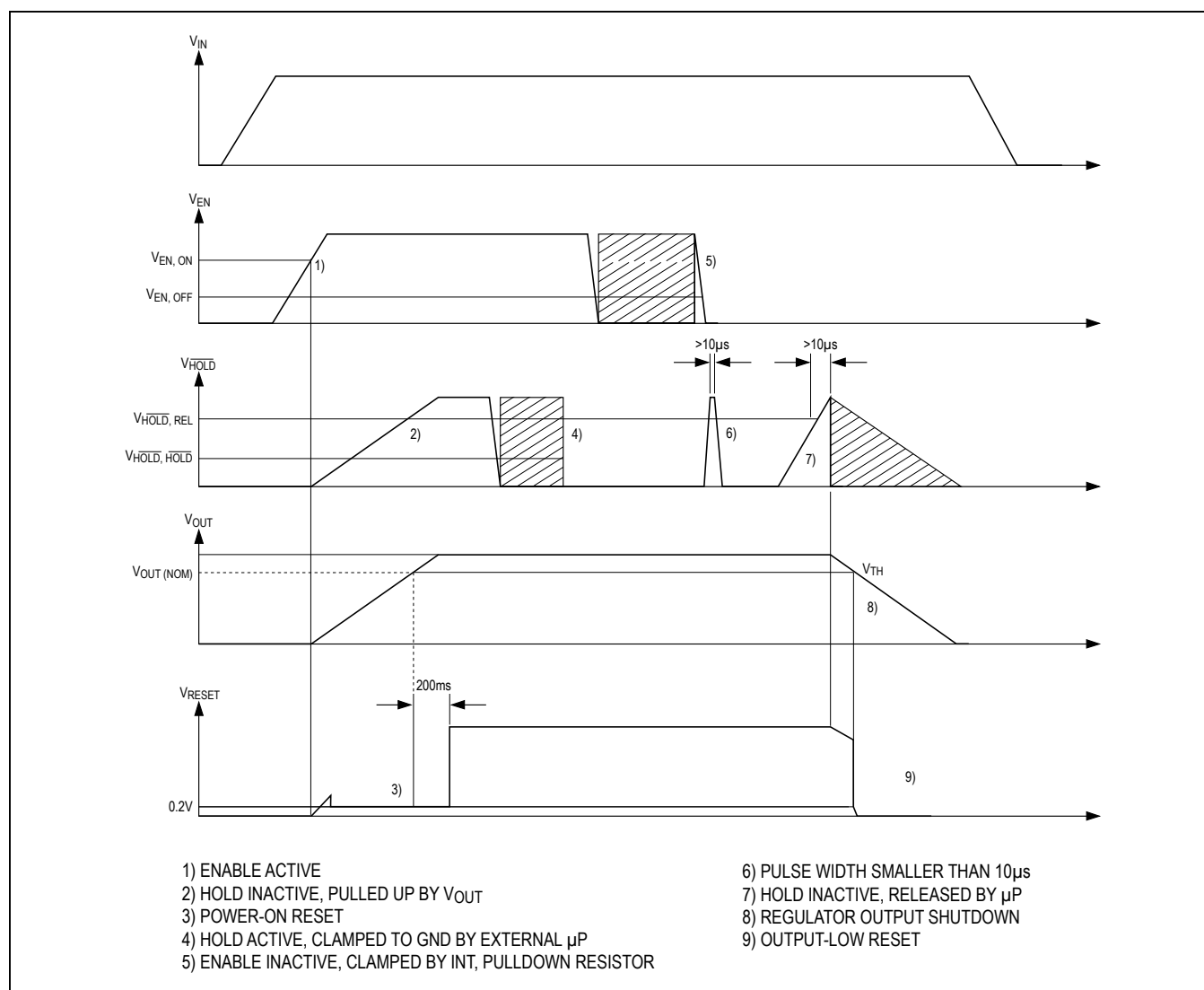


Figure 3. Enable and Hold Behavior

Dual Mode is a trademark of Maxim Integrated Products, Inc.

Table 1. Truth Table for Regulator Output State

ORDER	EN	HOLD	OUT	Comments
1	Low	X	Off	Initial state. EN has 5M Ω internal resistor to ground. HOLD has internal current source to OUT.
2	High	X	On	Regulator output is active when EN is pulled high.
3	High	Low	On	Hold is asserted forcing the regulator output on even if EN goes low.
4	X	Low	On	Self-holding state. Regulator output stays on regardless of the state of EN.
5	Low	High	Off	Regulator output is shutdown by releasing HOLD while EN remains low.

X = Don't care.

Select preset voltage mode by connecting SET to ground. In adjustable mode, select an output between +2.5V and +11V using two external resistors connected as a voltage-divider to SET (Figure 4). Set the output voltage using the following equation:

$$V_{\text{OUT}} = V_{\text{SET}} (1 + R1/R2)$$

where $V_{\text{SET}} = 1.248\text{V}$ and $R2 \approx 100\text{k}\Omega$.

Available Output-Current Calculation

The MAX5023/MAX5024 high-voltage regulator provides up to 150mA of output current. The input voltage extends to +65V. Package power dissipation limits the amount of output current available for a given input/output voltage and ambient temperature. Figure 5 depicts the maximum power dissipation curve for these devices. The graph assumes that the exposed metal pad of the MAX5023/MAX5024 package is soldered to 1in² of PCB copper.

Use Figure 5 to determine the allowable package dissipation for a given ambient temperature. Alternately, use the following formula to calculate the allowable package dissipation:

$$P_D = \begin{cases} 1.538\text{ W for } T_A \leq +70^\circ\text{C} \\ 1.538 - 0.01923 (T_A - 70^\circ\text{C}) \text{ for } +70^\circ\text{C} < T_A \leq +125^\circ\text{C} \end{cases}$$

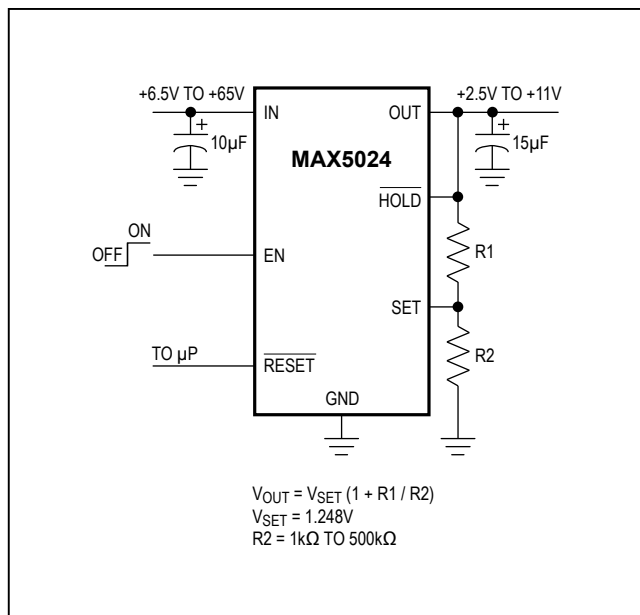


Figure 4. Setting the MAX5024 Adjustable Output Voltage

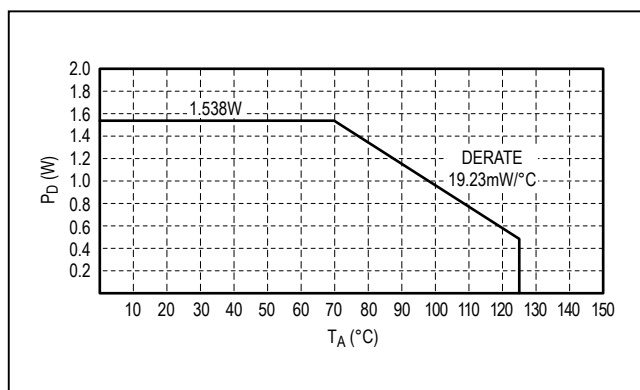


Figure 5. Maximum Power Dissipation vs. Temperature

After determining the allowable package dissipation, calculate the maximum output current using the following formula:

$$I_{\text{OUT(MAX)}} \cong \frac{P_D}{V_{\text{IN}} - V_{\text{OUT}}} \leq 150\text{ mA}$$

The above equations do not include the negligible power dissipation from self-heating due to the IC ground current.

Example 1:

$$T_A = +95^\circ\text{C}$$

$$V_{IN} = +14\text{V}$$

$$V_{OUT} = +5\text{V}$$

Find the maximum allowable output current. First calculate package dissipation at the given temperature as follows:

$$P_D = 1.538\text{ W} - (0.01923\text{ W}/^\circ\text{C}) (95^\circ\text{C} - 70^\circ\text{C}) \\ = 1.057\text{ W}$$

Then determine the maximum output current:

$$I_{OUT(MAX)} = \frac{(1.057\text{ W})}{(14\text{ V}) - (5\text{ V})} = 117.4\text{ mA}$$

Example 2:

$$T_A = +125^\circ\text{C}$$

$$V_{IN} = +14\text{V}$$

$$V_{OUT} = +3.3\text{V}$$

Calculate package dissipation at the given temperature as follows:

$$P_D = 1.538\text{ W} - (0.01923\text{ W}/^\circ\text{C}) (125^\circ\text{C} - 70^\circ\text{C}) \\ = 480.4\text{ mW}$$

And establish the maximum current:

$$I_{OUT(MAX)} = \frac{(480.4\text{ mW})}{(14\text{ V}) - (3.3\text{ V})} = 44.89\text{ mA}$$

Example 3:

$$T_A = +50^\circ\text{C}$$

$$V_{IN} = +14\text{V}$$

$$V_{OUT} = +5\text{V}$$

Calculate package dissipation at the given temperature as follows:

$$P_D = 1.538\text{ W}$$

And find the maximum output current:

$$I_{OUT(MAX)} = \frac{(1.538\text{ W})}{(14\text{ V}) - (5\text{ V})} = 170.9\text{ mA} \Rightarrow I_{OUT(MAX)} = 150\text{ mA}$$

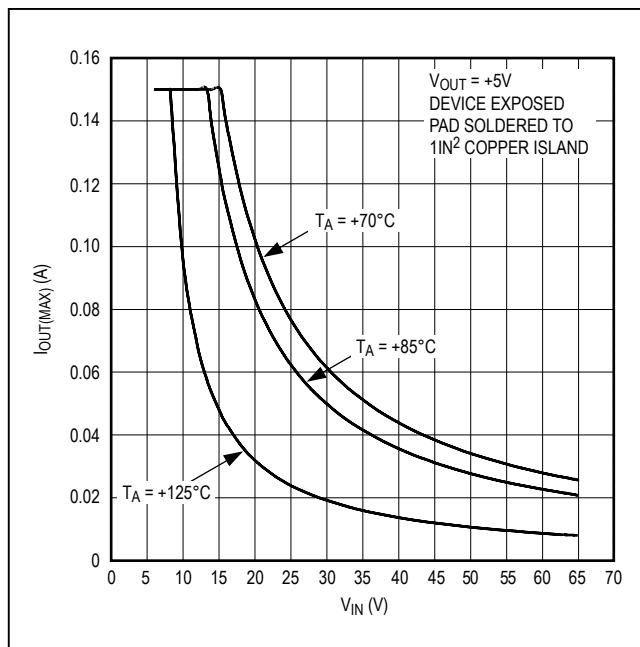


Figure 6. Maximum Output Current vs. Input Voltage

In Example 3, the maximum output current is calculated as 170.9mA, however, the maximum output current cannot exceed 150mA. Use Figure 6 to quickly determine allowable maximum output current for selected ambient temperatures.

Capacitor Selection and Regulator Stability

For stable operation over the full temperature range and with load currents up to 150mA, use a 15 μ F (min) output capacitor with an ESR < 0.5 Ω . To reduce noise and improve load-transient response, stability, and power-supply rejection, use larger output capacitor values such as 22 μ F.

Some ceramic dielectrics exhibit large capacitance and ESR variation with temperature. For dielectric capacitors such as Z5U and Y5V, use 22 μ F or more to ensure stability at temperatures below -10 $^\circ$ C. With X7R or X5R dielectrics, 15 μ F should be sufficient at all operating temperatures. For high-ESR tantalum capacitors use 22 μ F or more to maintain stability. To improve power-supply rejection and transient response use a minimum 10 μ F capacitor between IN and GND.

Selector Guide

PART	PRESET V_{OUT}		RESET THRESHOLD (V_{TH})				V _{OUT} RESET TOLERANCE		WATCHDOG TIMER	ADJUSTABLE REGULATOR OUTPUT	/V
	5V	3.3V	4.63V	4.38V	3.05V	2.89V	V _{OUT} - 7.5%	V _{OUT} - 12.5%			
MAX5023L	✓	—	✓	—	—	—	✓	—	✓	—	—
MAX5023M	✓	—	—	✓	—	—	—	✓	✓	—	—
MAX5023T	—	✓	—	—	✓	—	✓	—	✓	—	—
MAX5023S	—	✓	—	—	—	✓	—	✓	✓	—	—
MAX5024L	✓	—	✓	—	—	—	✓	—	—	✓	—
MAX5024M	✓	—	—	✓	—	—	—	✓	—	✓	—
MAX5024T	—	✓	—	—	✓	—	✓	—	—	✓	—
MAX5024S	—	✓	—	—	—	✓	—	✓	—	✓	✓

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX5023_ASA	-40°C to +125°C	8 SO-EP*
MAX5023_ASA/V+**	-40°C to +125°C	8 SO-EP*
MAX5024_ASA	-40°C to +125°C	8 SO-EP*
MAX5024_ASA/V+	-40°C to +125°C	8 SO-EP*

Note: These parts offer a choice of reset thresholds, reset threshold tolerances, and regulator output voltages. From the Selector Guide, insert the desired suffix letter into the blank to complete the part number.

/V denotes an automotive qualified part.

+Denotes a lead(Pb)-free/RoHS-compliant package.

*EP = Exposed pad.

**Future product—contact factory for availability.

Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8L SOIC	S8E+14	21-0111	90-0151

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
3	5/14	No /V OPNs; removed automotive reference from <i>Applications</i> section	1
4	3/15	Added /V OPNs to <i>Ordering Information</i> and updated Selector Guide; added Automotive to <i>Applications</i> section; renumbered notes in <i>Electrical Characteristics</i> section; moved <i>Ordering Information</i> , <i>Pin Configurations</i> , and <i>Typical Operating Circuit</i>	1–4, 8, 13
5	5/15	Changed WDI Pulse Width in <i>Electrical Characteristics</i> table from 50ns (min) to 50ns (typ); updated package code in <i>Package Information</i> section	4, 14
6	5/15	Updated junction-to-ambient thermal resistance in <i>Package Thermal Characteristics</i> section	2
7	8/15	Changed MAX5023_ASA/V+ to future product in <i>Ordering Information</i>	14
8	3/16	Updated package type in <i>Package Information</i> section	14

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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