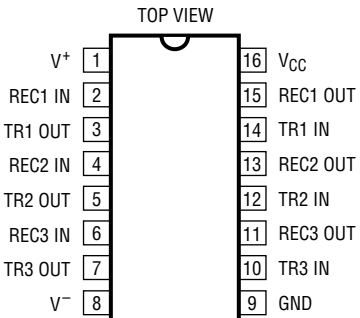
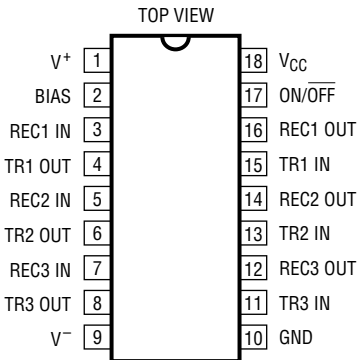


LT1039A/LT1039A-16

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage		Short-Circuit Duration	Indefinite
Driver (V^+ , V^-)	$\pm 16V$	Operating Temperature Range	
Receiver (V_{CC})	7V	LT1039AC	0°C to 70°C
Logic Inputs	V^- to 25V	LT1039AI	-40°C to 85°C
Receiver Inputs	$\pm 30V$	LT1039AM (OBSOLETE)	-55°C to 125°C
ON/OFF Input	GND to 12V	Storage Temperature Range	-65°C to 150°C
Driver Outputs	$V^- + 30V$ to $V^+ - 30V$	Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION

 N PACKAGE 16-LEAD PDIP SW PACKAGE 16-LEAD PLASTIC SO $T_{JMAX} = 125^\circ C$, $\theta_{JA} = 90^\circ C/W$, $\theta_{JC} = 50^\circ C/W$ (N) $T_{JMAX} = 125^\circ C$, $\theta_{JA} = 95^\circ C/W$, $\theta_{JC} = 27^\circ C/W$ (SW)	ORDER PART NUMBER	 N PACKAGE 18-LEAD PDIP SW PACKAGE 18-LEAD PLASTIC SO $T_{JMAX} = 125^\circ C$, $\theta_{JA} = 79^\circ C/W$, $\theta_{JC} = 36^\circ C/W$ (N) $T_{JMAX} = 125^\circ C$, $\theta_{JA} = 90^\circ C/W$, $\theta_{JC} = 26^\circ C/W$ (SW)	ORDER PART NUMBER
	LT1039ACN16 LT1039ACSW16		LT1039ACN LT1039ACSW LT1039AIN LT1039AISW
Consult Factory for Availability		J PACKAGE 18-LEAD CERDIP $T_{JMAX} = 150^\circ C$, $\theta_{JA} = 88^\circ C/W$, $\theta_{JC} = 22^\circ C/W$ (J) OBSOLETE PACKAGE Consider the N Package for Alternate Source	
		LT1039AMJ	

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ C$.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Driver $V^+ = 12V$, $V^- = -12V$, $V_{ON/OFF} = 2.5V$ (Note 2)					
Output Voltage Swing	Load = 3k to Ground				
	Positive	● $V^+ - 2.0$	$V^+ - 1.3$		V
	Negative	● $V^- + 1.5$	$V^- + 1.0$		V
Logic Input Voltage Levels	Input Low Level ($V_{OUT} = \text{High}$)	●	1.4	0.8	V
	Input High Level ($V_{OUT} = \text{Low}$)	● 2.0	1.4		V
Logic Input Current	$V_{IN} \geq 2.0V$		1	20	μA
	$V_{IN} \leq 0.8V$		5	20	μA
Output Short-Circuit Current	Sourcing Current, $V_{OUT} = 0V$	20	30		mA
	Sinking Current, $V_{OUT} = 0V$	-15	-30		mA

1039afa

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
Output Leakage Current	Shutdown (Notes 3, 4), $V_{OUT} = \pm 18\text{V}$, $V_{IN} = 0\text{V}$	●		10	200	μA
Supply Leakage Current	Shutdown (Note 3)	●		1	100	μA
Slew Rate	$R_L = 3\text{k}$, $C_L = 51\text{pF}$ to 2500pF		4	15	30	$\text{V}/\mu\text{s}$
Supply Current	$V_{OUT} = \text{Low}$			1	5	mA
Prop Delay (t_{PLH}) (t_{PHL})				0.6 0.8	1.2 1.2	μs μs

Receiver $V_{CC} = 5\text{V}$, $V_{ON/OFF} = 2.5\text{V}$ (Note 2)

Input Voltage Thresholds	Input Low Level ($V_{OUT} = \text{High}$) Input High Level ($V_{OUT} = \text{Low}$)	● ●	0.5 1.7	1.3 2.8		V V
Hysteresis		●	0.1	0.4	1.0	V
Input Resistance		●		30		$\text{k}\Omega$
Output Voltage	Output Low, $I_{OUT} = -1.6\text{mA}$ Output High, $I_{OUT} = 160\mu\text{A}$	● ●	3.5	0.4 4.8	0.5	V V
Output Short-Circuit Current	Sourcing Current, $V_{OUT} = V_{CC}$ Sinking Current, $V_{OUT} = 0\text{V}$	● ●	-10 15	-30 25		mA mA
Output Leakage Current	Shutdown (Note 2), $0\text{V} \leq V_{OUT} \leq V_{CC}$, $V_{IN} = 0\text{V}$	●		1	10	μA
Supply Current	(Note 5)	●		2	5	mA
Supply Leakage Current	Shutdown (Note 3)	●		1	100	μA
ON/OFF Pin Current	$0\text{V} \leq V_{ON/OFF} \leq 5\text{V}$	●	-15		80	μA
Prop Delay (t_{PLH}) (t_{PHL})				200 300	600 600	ns ns

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

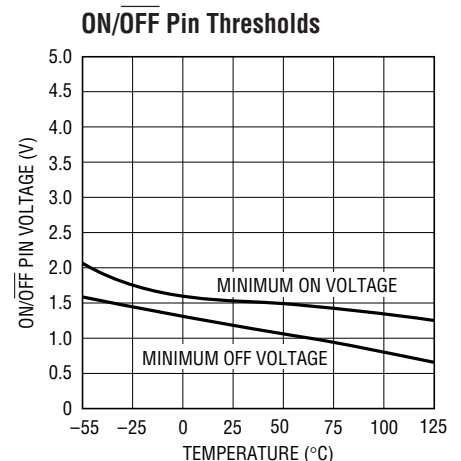
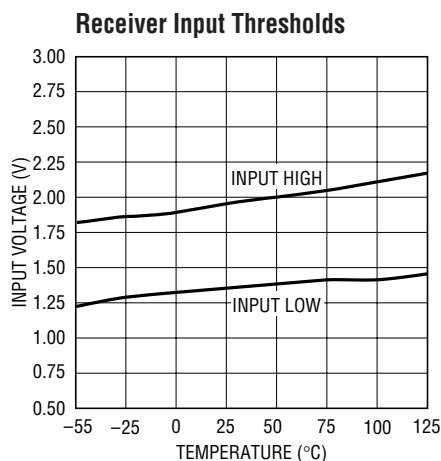
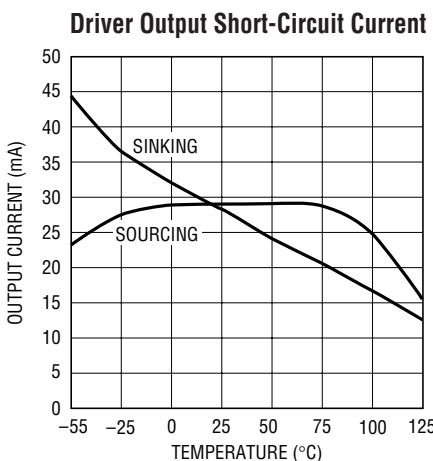
Note 2: $V_{ON/OFF} = 5\text{V}$ for LT1039AM grade devices.

Note 3: $V_{ON/OFF} = 0.4\text{V}$ for $-55^\circ\text{C} \leq T_A \leq 100^\circ\text{C}$ and $V_{ON/OFF} = 0.2\text{V}$ for $100^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$. Does not apply to LT1039A-16 part.

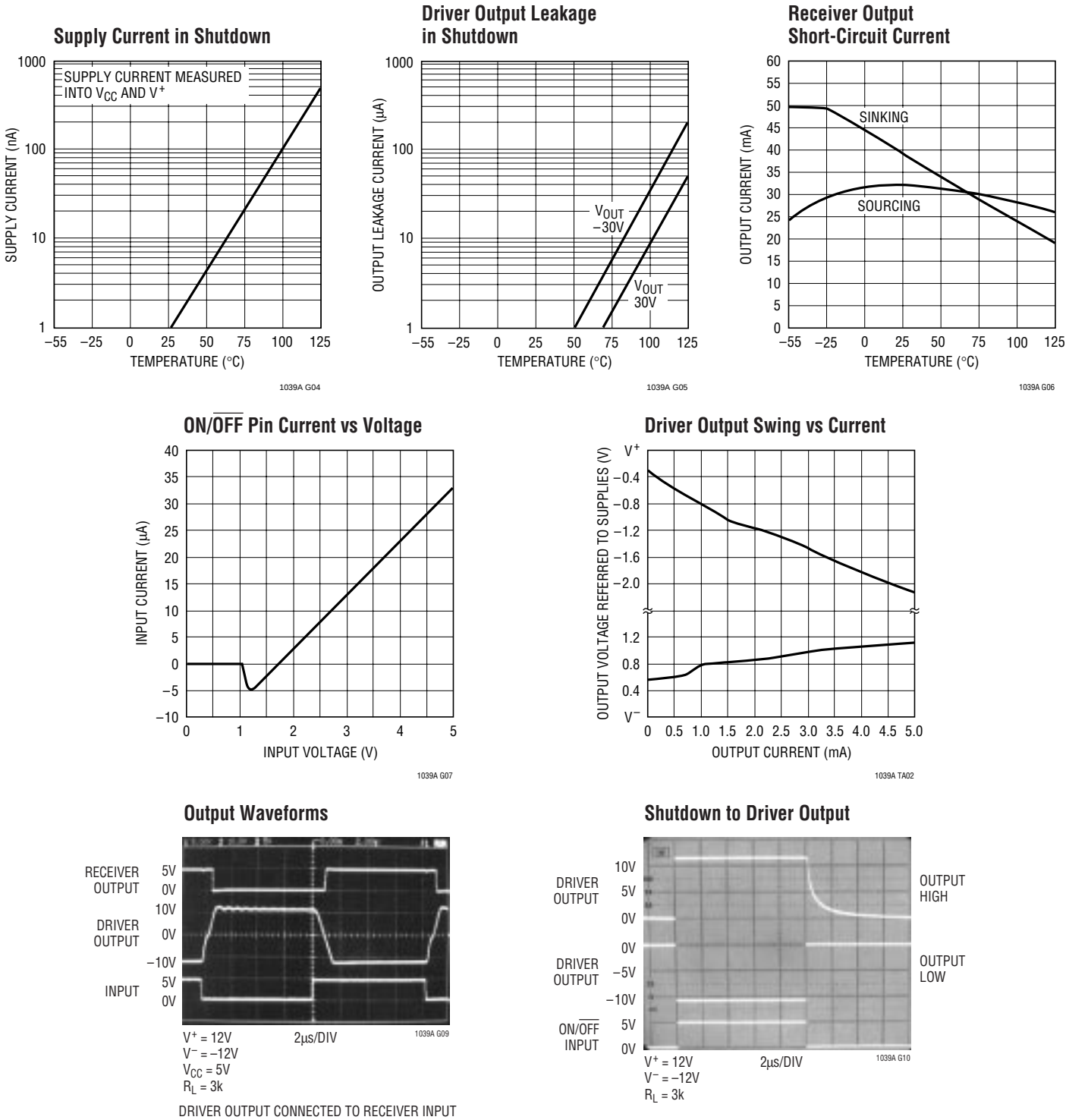
Note 4: For $T_A \geq 100^\circ\text{C}$ leakage current is $350\mu\text{A}$ max.

Note 5: Bias pin open on 18-pin version.

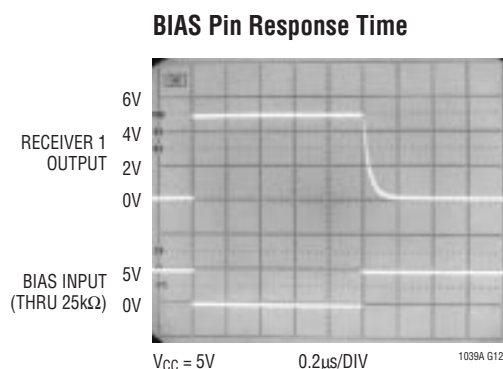
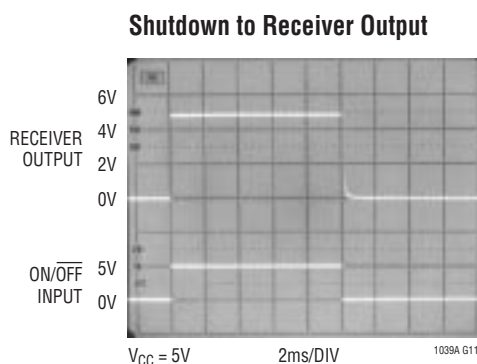
TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS



PIN FUNCTIONS (Pin numbers listed are for 18-pin device)

V⁺, V⁻ (Pins 1, 9): Driver Supply Pins. Supply current drops to zero in shutdown mode. Driver outputs are in a high impedance state when V⁺ and V⁻ = 0V.

BIAS (Pin 2): Keeps receiver 1 on while the LT1039A is in the shutdown mode. Leave BIAS pin open when not in use. See Applications Information for proper use.

REC IN (Pins 3, 5, 7): Receiver Input Pins. Accepts RS232 voltage levels ($\pm 30V$) and has 0.4V of hysteresis to provide noise immunity. Input impedance is nominally 30kΩ. Receiver input pins are internally protected from ESD transients. In order to insure proper functioning of the ESD protection devices, the V_{CC} and V⁻ supply pins must be bypassed with low ESR capacitors located close to the pins. A 0.1μF ceramic capacitor works well.

TR OUT (Pins 4, 6, 8): Driver Outputs with RS232 Voltage Levels. Outputs are in a high impedance state when in the shutdown mode or when power is off (V⁺ and V⁻ = 0V) to allow data line sharing. Outputs are fully short-circuit protected from V⁻ + 30V to V⁺ - 30V with power on, off or in the shutdown mode. Typical output breakdowns are greater than $\pm 45V$ and higher applied voltages will not damage the device if moderately current limited. Driver

output pins are internally protected from ESD transients. In order to insure proper functioning of the ESD protection devices, the V⁺ and V⁻ supply pins must be bypassed with low ESR capacitors located close to the pins. 0.1μF ceramic capacitors work well.

GND (Pin 10): Ground Pin.

TR IN (Pins 11, 13, 15): RS232 Driver Input Pins. Inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to V_{CC}.

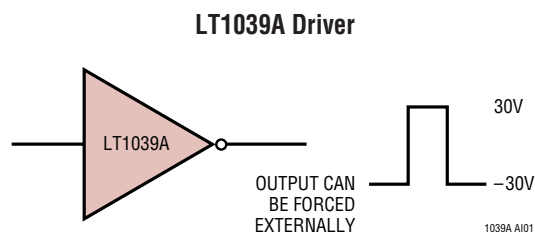
REC OUT (Pins 12, 14, 16): Receiver Outputs with TTL/CMOS Voltage Levels. Outputs are in a high impedance state when in the shutdown mode to allow data line sharing. Outputs are fully short-circuit protected to ground or V_{CC} with power on, off or in the shutdown mode.

ON/OFF (Pin 17): Controls the operation mode of the LT1039A and is TTL/CMOS compatible. A logic low puts the device in the shutdown mode which reduces input supply current to zero and places both driver and receiver outputs in a high impedance state.

V_{CC} (Pin 18): 5V Power for Receivers.

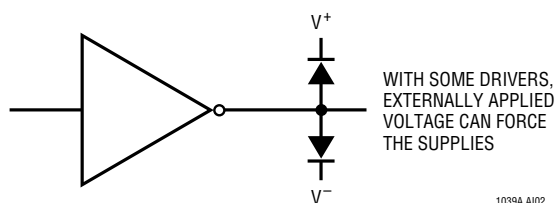
APPLICATIONS INFORMATION

The driver output stage of the LT1039A offers significantly improved protection over older bipolar and CMOS designs. In addition to current limiting, the driver output can be externally forced to $\pm 30V$ with no damage or excessive current flow.



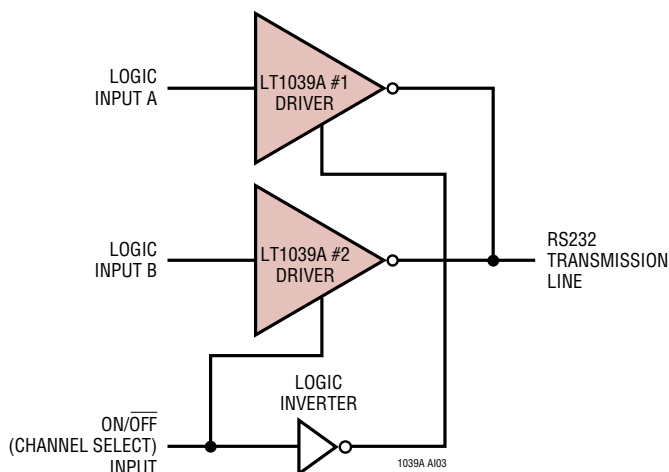
The driver outputs utilize high impedance overvoltage protection, eliminating the flow of fault currents into supplies, as will happen with conventional diode clamp configurations.

Older RS232 Drivers and Other CMOS Drivers

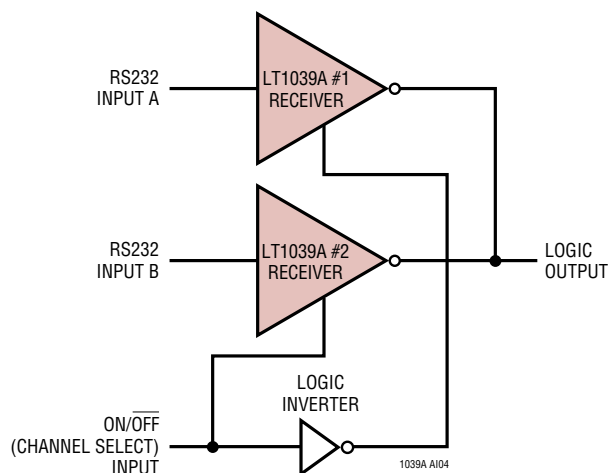


Placing the LT1039A in the shutdown mode (Pin 17 low) puts both the driver and receiver outputs in a high impedance state. This allows data line sharing and transceiver applications.

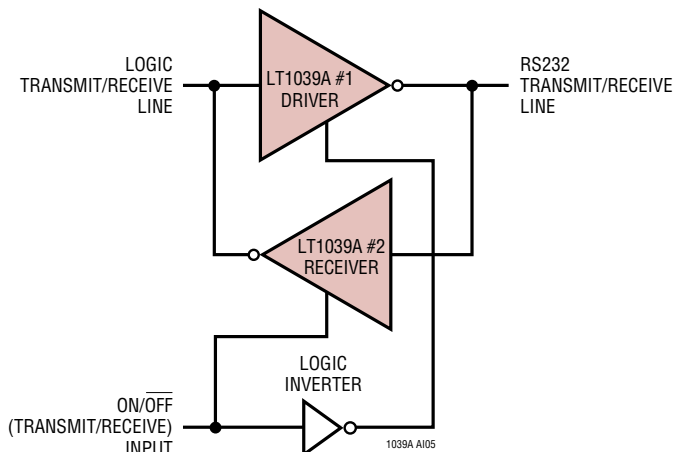
Sharing a Transmitter Line



Sharing a Receiver Line



Transceiver

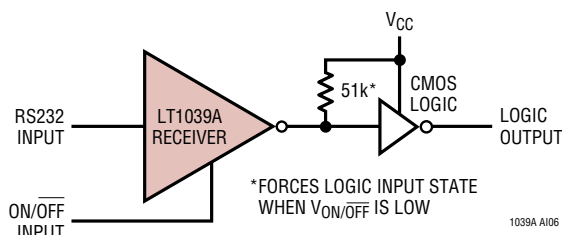


The shutdown mode also drops all supply currents (V_{CC} , V^+ , V^-) to zero for power conscious systems.

APPLICATIONS INFORMATION

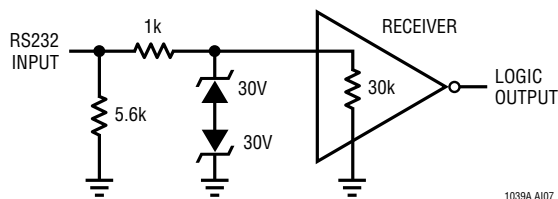
When driving CMOS logic from a receiver that will be used in the shutdown mode and there is no other active receiver on the line, a 51k resistor can be placed from the logic input to V_{CC} to force a definite logic level when the receiver output is in a high impedance state.

Driving CMOS Logic from a Receiver



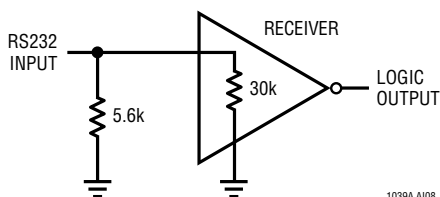
To protect against receiver input overloads in excess of $\pm 30V$, a voltage clamp can be placed on the data line and still maintain RS232 compatibility.

Input Overvoltage Protection



The receiver input impedance of the LT1039A is nominally 30k Ω . For applications requiring a 5k Ω input impedance, a 5.6k resistor can be connected from the receiver input to ground.

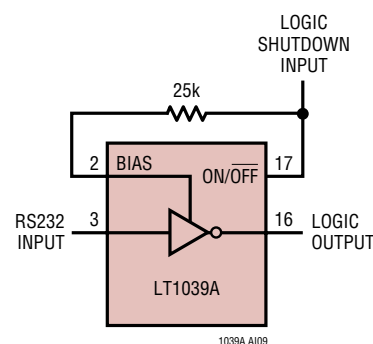
5k Ω Impedance Matching



Driver inputs should not be allowed to float. Any unused inputs should be tied to V_{CC} .

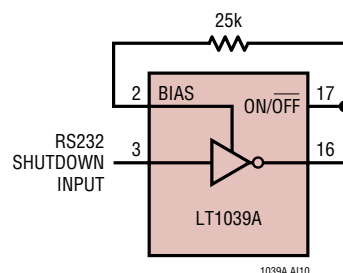
The BIAS pin is used to "keep alive" one receiver while in the shutdown mode (all other circuitry being inactive). This allows a system to be in shutdown and still have one active receiver for transferring data.

Keeping Alive One Receiver While in Shutdown



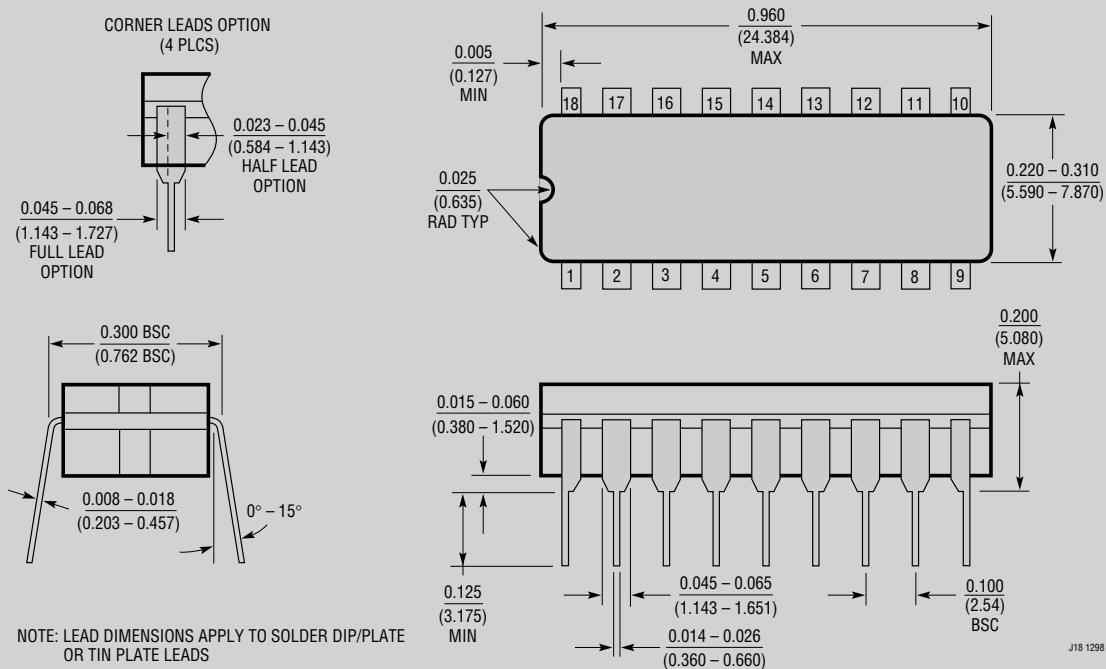
It can also be used to make an RS232 compatible shutdown control line. Driving the BIAS pin low through a resistance of 24k to 30k keeps the receiver active. Do not drive the BIAS pin directly from a logic output without the series resistor. An unused BIAS pin should be left open.

RS232 Compatible Shutdown Control Line



PACKAGE DESCRIPTION

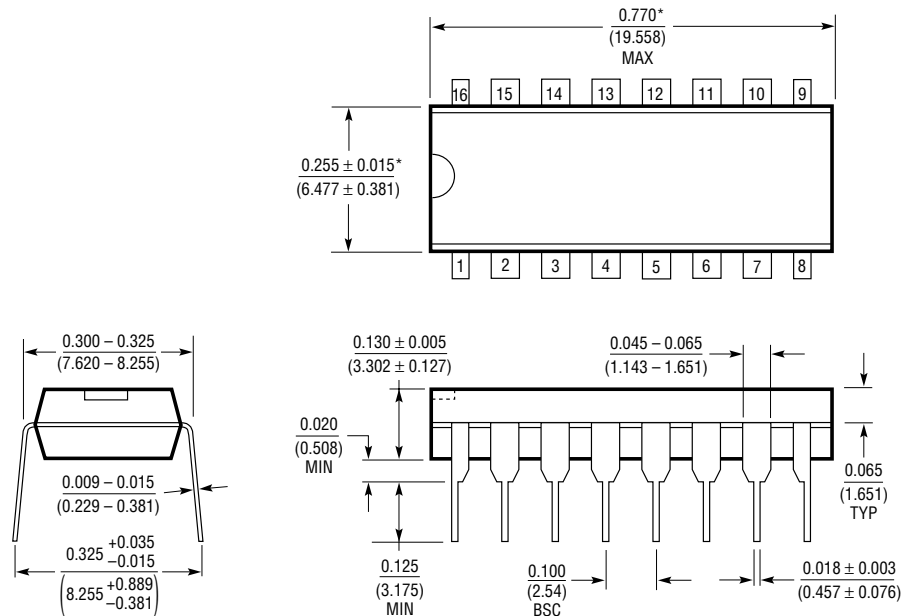
J Package 18-Lead Cerdip (Narrow .300 Inch, Hermetic) (Reference LTC DWG # 05-08-1110)



OBSOLETE PACKAGE

PACKAGE DESCRIPTION

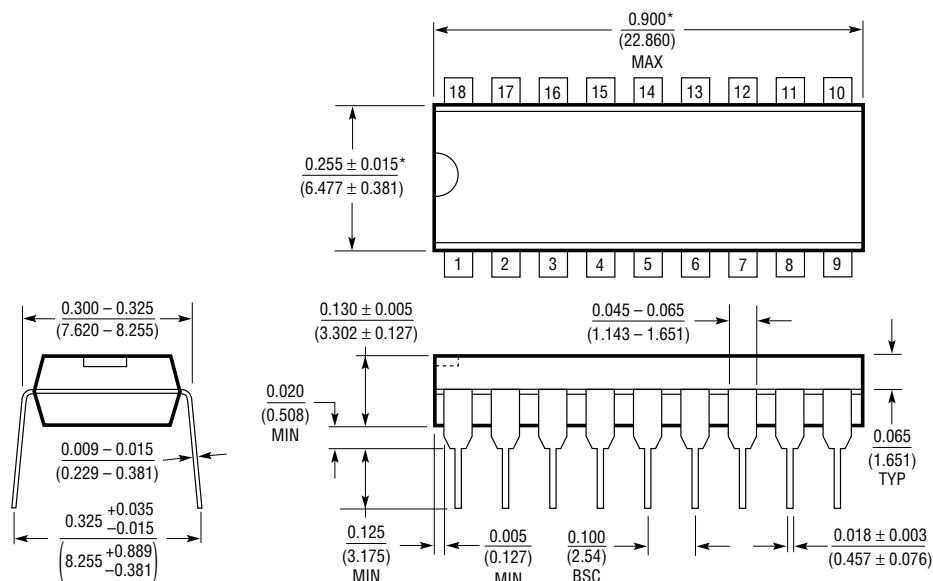
N Package 16-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510)



*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

N16 1098

N Package 18-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510)

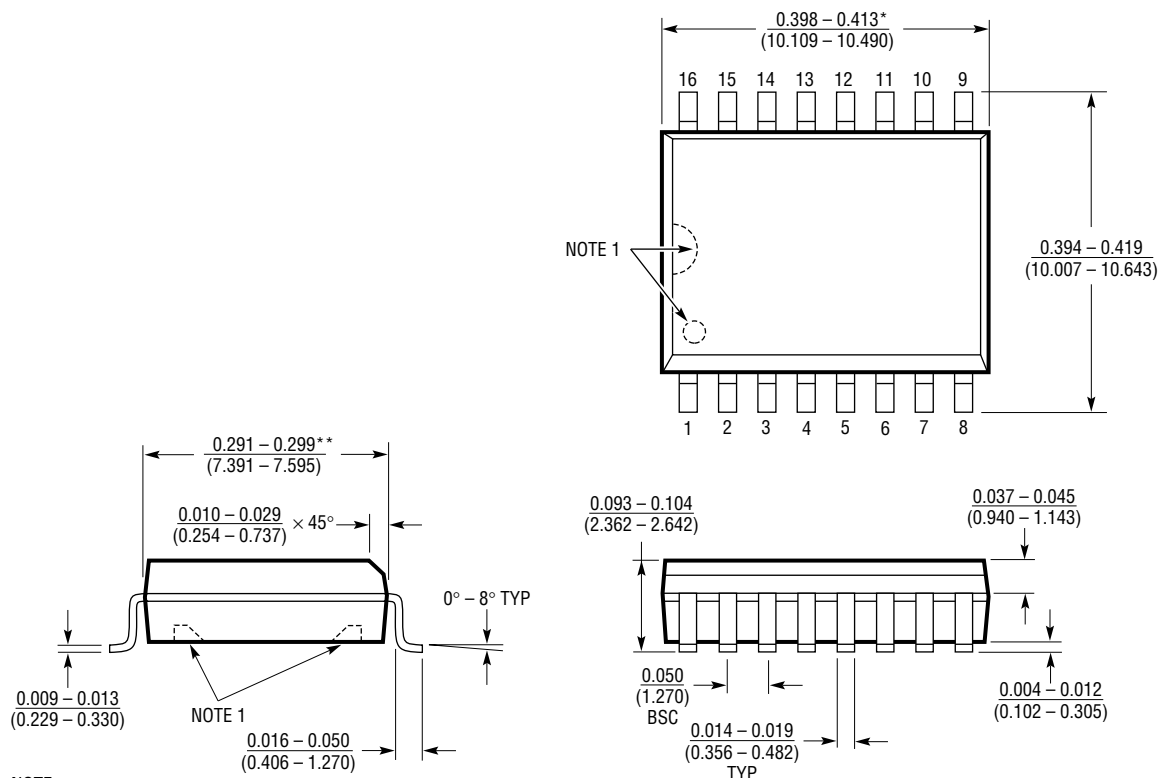


*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

N18 1098

PACKAGE DESCRIPTION

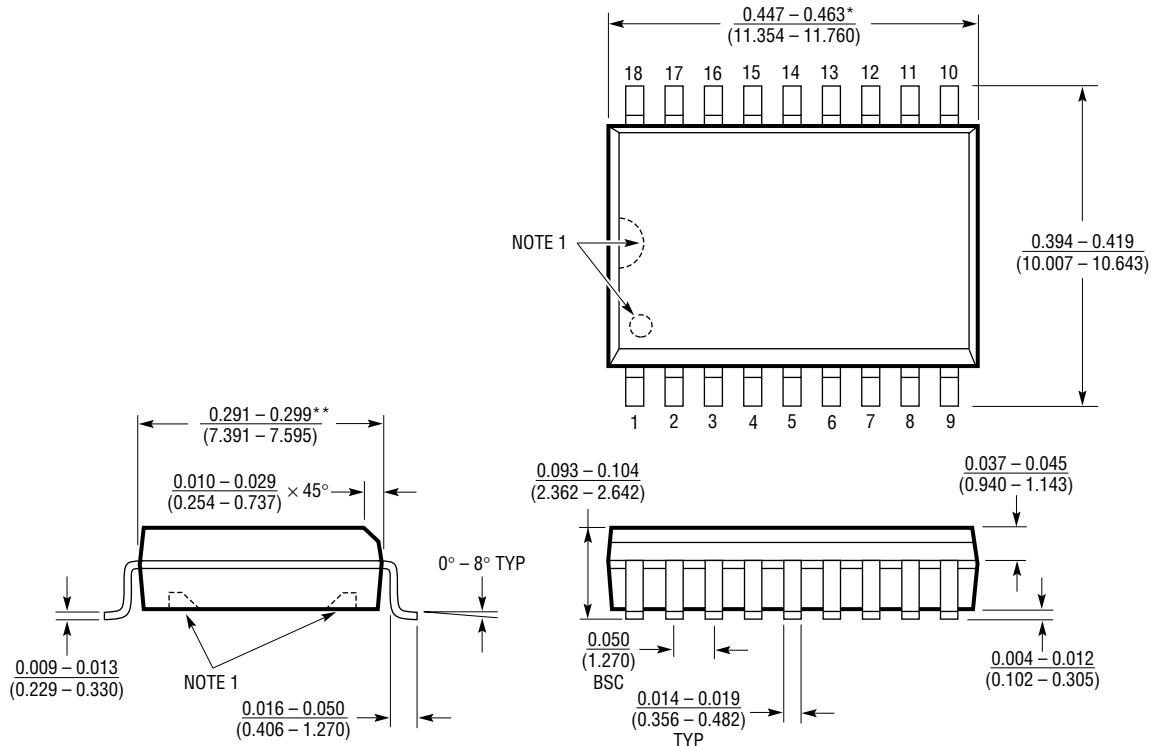
SW Package 16-Lead Plastic Small Outline (Wide .300 Inch) (Reference LTC DWG # 05-08-1620)



S16 (WIDE) 1098

PACKAGE DESCRIPTION

SW Package 18-Lead Plastic Small Outline (Wide .300 Inch) (Reference LTC DWG # 05-08-1620)



NOTE:

1. PIN 1 IDENT, NOTCH ON TOP AND CAVITIES ON THE BOTTOM OF PACKAGES ARE THE MANUFACTURING OPTIONS. THE PART MAY BE SUPPLIED WITH OR WITHOUT ANY OF THE OPTIONS

*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

S18 (WIDE) 1098



PART NUMBER	DESCRIPTION	COMMENTS
LTC®485	Low Power RS485 Transceiver	Industry Standard
LT1137A	5V 3 Driver/5 Receiver RS232 Transceiver	±15kV ESD Protection
LT1180A/81A	5V 2 Driver/2 Receiver RS232 Transceiver	Industry Standard, 0.1µF Capacitors
LTC1345	V.35 Transceiver	Single 5V Supply Using 0.1µF Capacitors
LTC1348	3.3V 3 Driver/5 Receiver RS232 Transceiver	Operates from 3.3V to 5V Supplies, 5 Receivers Active in Shutdown