

**THERMAL RESISTANCE RATINGS**

Parameter		Symbol	Channel-1		Channel-2		Unit
			Typ.	Max.	Typ.	Max.	
Maximum Junction-to-Ambient <sup>a</sup>	$t \leq 10$ s	$R_{thJA}$	54	65	47	60	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	32	38	30	35	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Maximum under Steady State conditions is 112 °C/W for Channel 1 and 107 °C/W for Channel 2.

**MOSFET SPECIFICATIONS**  $T_J = 25$  °C, unless otherwise noted

Parameter	Symbol	Test Conditions		Min.	Typ. <sup>a</sup>	Max.	Unit		
Static									
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	Ch-1 Ch-2	30 30			V		
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA	Ch-1 Ch-2		24 25		mV/°C		
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>		Ch-1 Ch-2		- 6 - 6		V		
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	Ch-1 Ch-2	1.5 1.5		3.0 2.7			
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 20 V	Ch-1 Ch-2			100 100	nA		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	Ch-1 Ch-2			1 100	μA		
			V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	Ch-1 Ch-2				15 2000	
		On-State Drain Current <sup>b</sup>		I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	Ch-1 Ch-2		20 20	
			Drain-Source On-State Resistance <sup>b</sup>			R <sub>DS(on)</sub>		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	Ch-1
V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10.5 A	Ch-2			0.015	0.018				
V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 8.5 A	Ch-1			0.019	0.023				
V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 9.3 A	Ch-2			0.018	0.022				
Forward Transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 A	Ch-1		30		S		
		V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10.5 A	Ch-2		35				
Diode Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V	Ch-1		0.75	1.1	V		
		I <sub>S</sub> = 1 A, V <sub>GS</sub> = 0 V	Ch-2		0.47	0.5			
Dynamic <sup>a</sup>									
Total Gate Charge	Q <sub>g</sub>	Channel-1 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A	Ch-1 Ch-2		6.6 8.9	10 14	nC		
Gate-Source Charge	Q <sub>gs</sub>		Ch-1 Ch-2		2.9 3.4				
Gate-Drain Charge	Q <sub>gd</sub>	Channel-2 V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = - 10.5 A	Ch-1 Ch-2		2.3 2.4				
Gate Resistance	R <sub>g</sub>		Ch-1 Ch-2	0.5 0.5	1.9 2.3	2.9 3.5		Ω	



<b>MOSFET SPECIFICATIONS</b> $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ. <sup>a</sup>	Max.	Unit
<b>Dynamic<sup>a</sup></b>						
Turn-On Delay Time	$t_{d(on)}$	Channel-1 $V_{DD} = 15\text{ V}$ , $R_L = 15\text{ }\Omega$ $I_D \cong 1\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 6\text{ }\Omega$	Ch-1	8	15	ns
			Ch-2	9	15	
Rise Time	$t_r$		Ch-1	11	18	
			Ch-2	13	20	
Turn-Off Delay Time	$t_{d(off)}$	Channel-2 $V_{DD} = 15\text{ V}$ , $R_L = 15\text{ }\Omega$ $I_D \cong 1\text{ A}$ , $V_{GEN} = 10\text{ V}$ , $R_g = 6\text{ }\Omega$	Ch-1	21	32	
			Ch-2	27	40	
Fall Time	$t_f$		Ch-1	6	10	
			Ch-2	9	15	
Source-Drain Reverse Recovery Time	$t_{rr}$	$I_F = 1.3\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$	Ch-1	28	40	
		$I_F = 2.2\text{ A}$ , $dI/dt = 100\text{ }\mu\text{A}/\mu\text{s}$	Ch-2	24	35	
Body Diode Reverse Recovery Charge	$Q_{rr}$	$I_F = 1.3\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$	Ch-1	17		nC
		$I_F = 2.2\text{ A}$ , $dI/dt = 100\text{ }\mu\text{A}/\mu\text{s}$	Ch-2	12		
Reverse Recovery Fall Time	$t_a$	$I_F = 1.3\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$	Ch-1	12		ns
		$I_F = 2.2\text{ A}$ , $dI/dt = 100\text{ }\mu\text{A}/\mu\text{s}$	Ch-2	11		
Reverse Recovery Rise Time	$t_b$	$I_F = 1.3\text{ A}$ , $dI/dt = 100\text{ A}/\mu\text{s}$	Ch-1	16		
		$I_F = 2.2\text{ A}$ , $dI/dt = 100\text{ }\mu\text{A}/\mu\text{s}$	Ch-2	13		

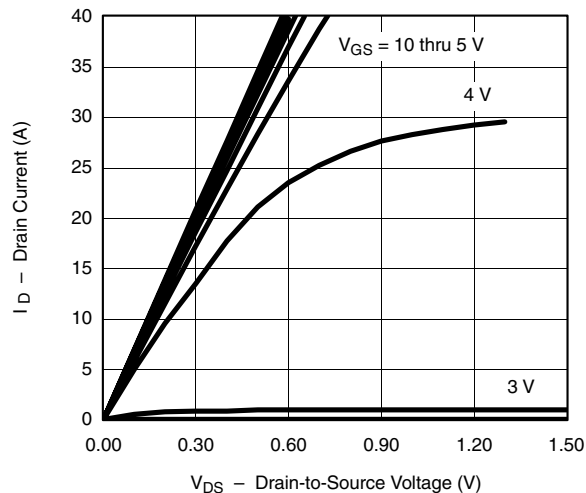
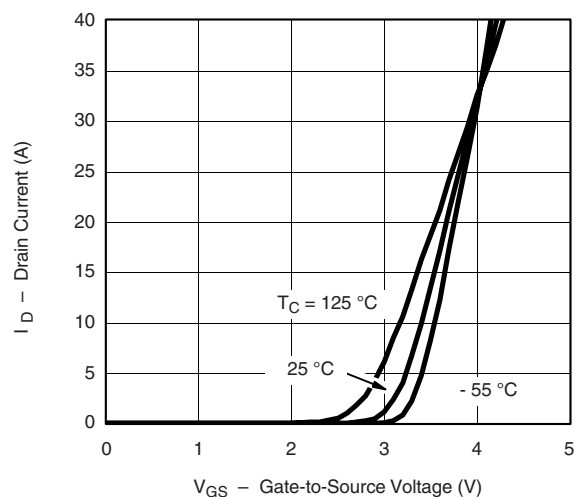
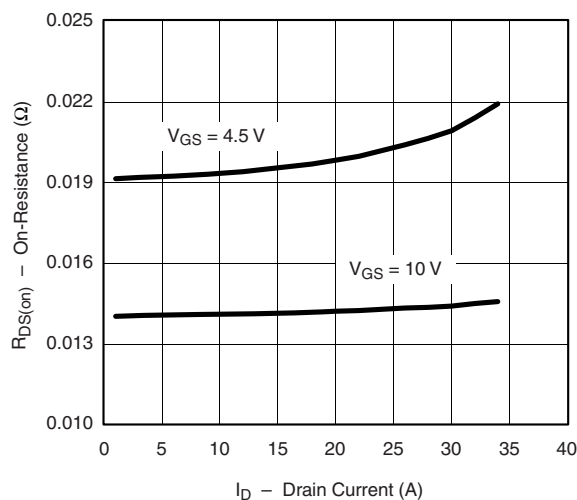
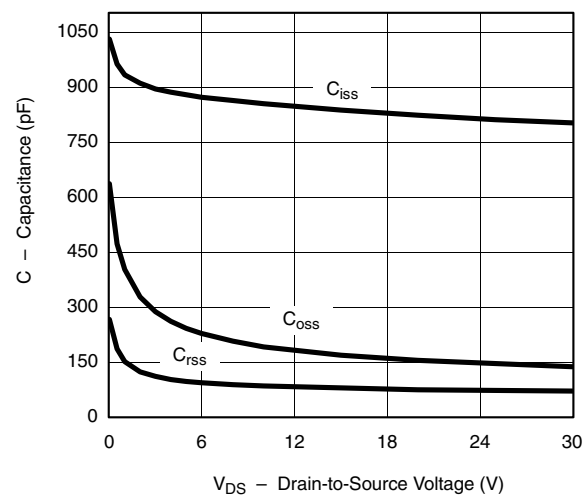
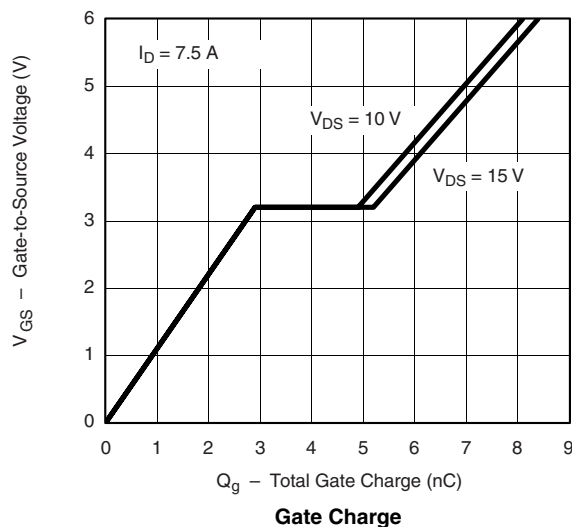
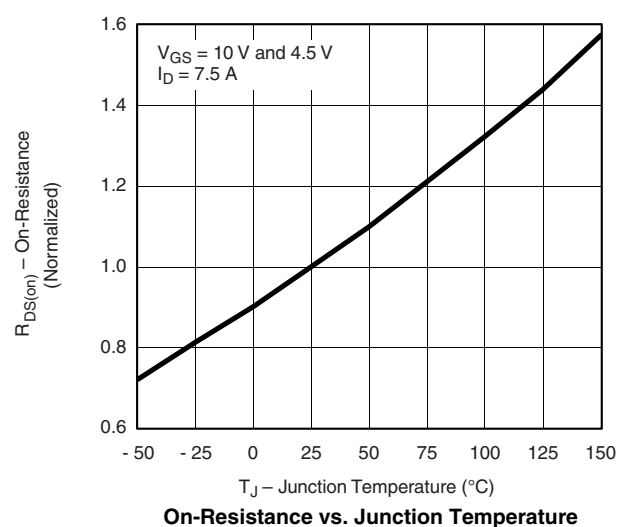
Notes:

a. Guaranteed by design, not subject to production testing.

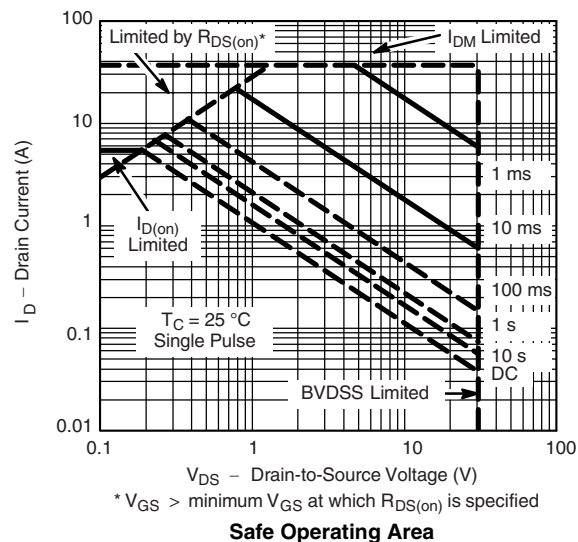
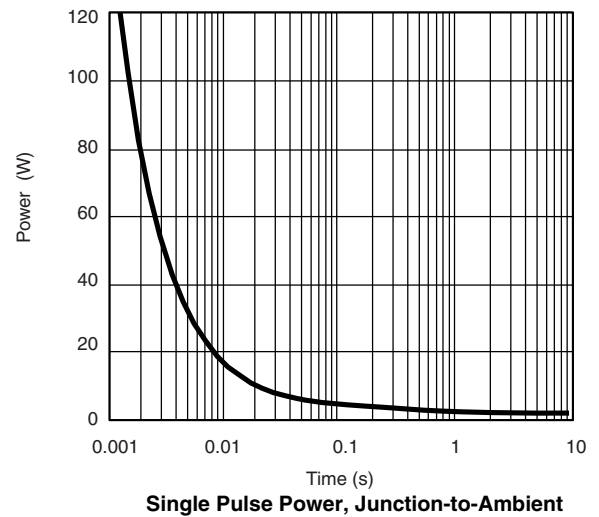
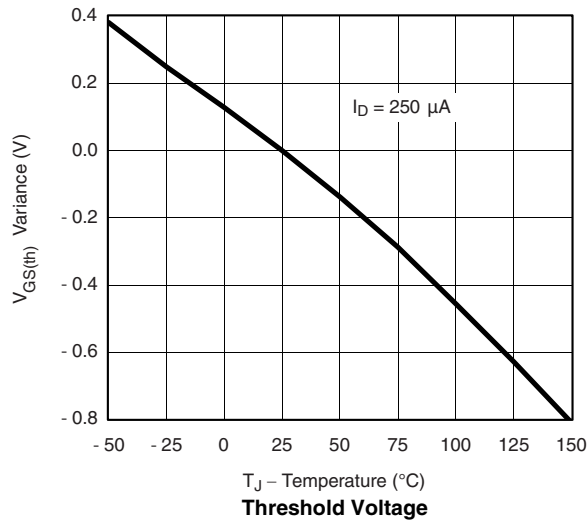
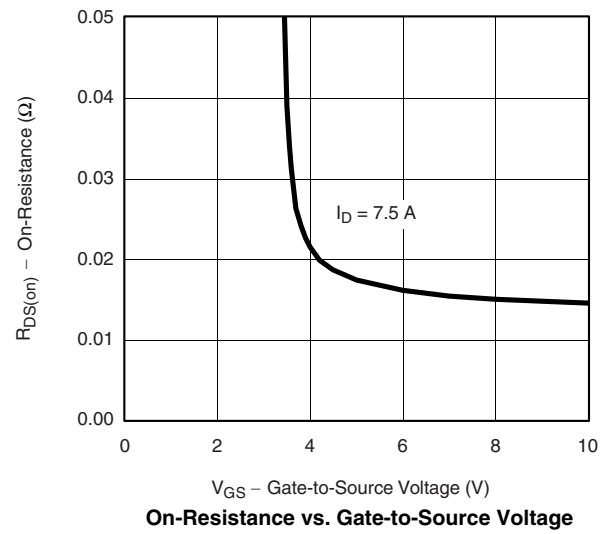
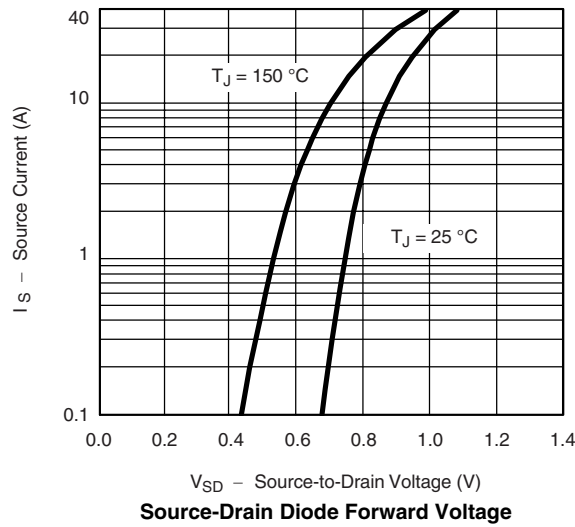
b. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .

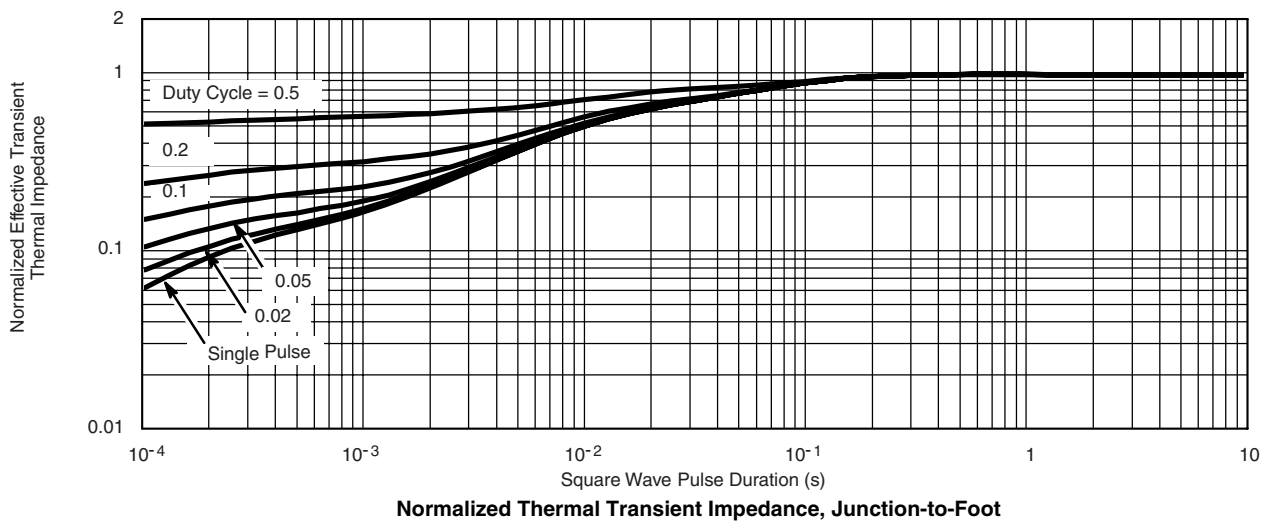
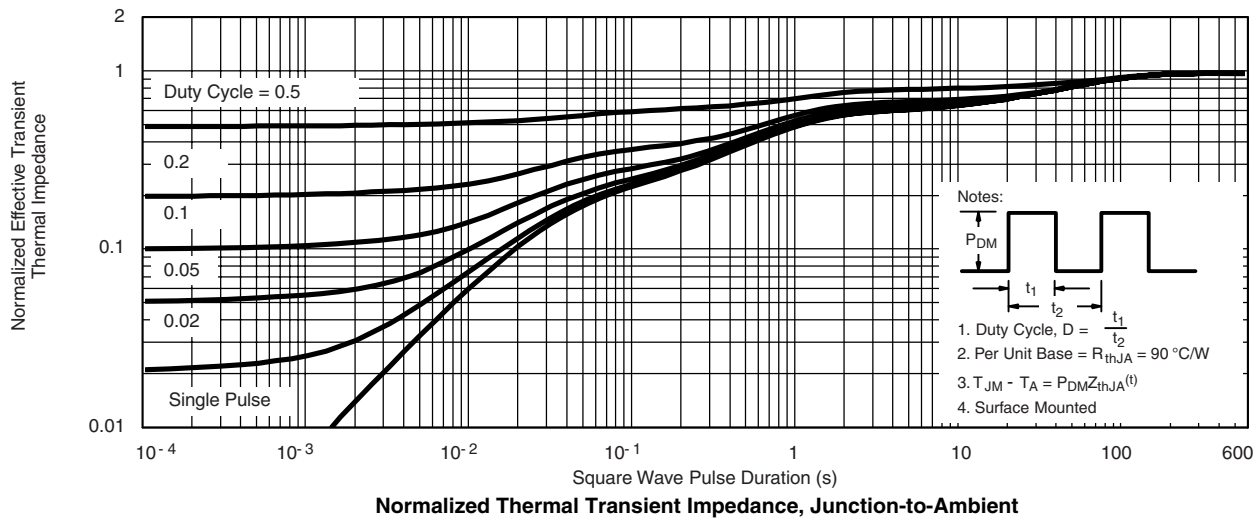
<b>SCHOTTKY SPECIFICATIONS</b> $T_J = 25\text{ }^{\circ}\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Forward Voltage Drop	$V_F$	$I_F = 1.0\text{ A}$		0.47	0.50	V
		$I_F = 1.0\text{ A}$ , $T_J = 125\text{ }^{\circ}\text{C}$		0.36	0.42	
Maximum Reverse Leakage Current	$I_{rm}$	$V_R = 30\text{ V}$		0.004	0.100	mA
		$V_R = 30\text{ V}$ , $T_J = 100\text{ }^{\circ}\text{C}$		0.7	10	
		$V_R = -30\text{ V}$ , $T_J = 125\text{ }^{\circ}\text{C}$		3.0	20	
Junction Capacitance	$C_T$	$V_R = 10\text{ V}$		50		pF

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

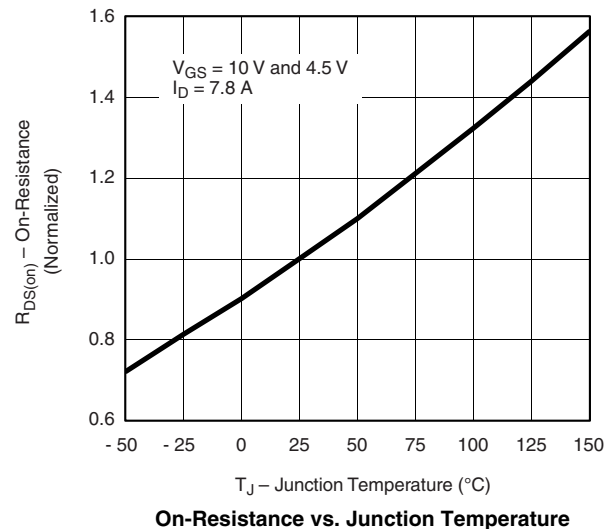
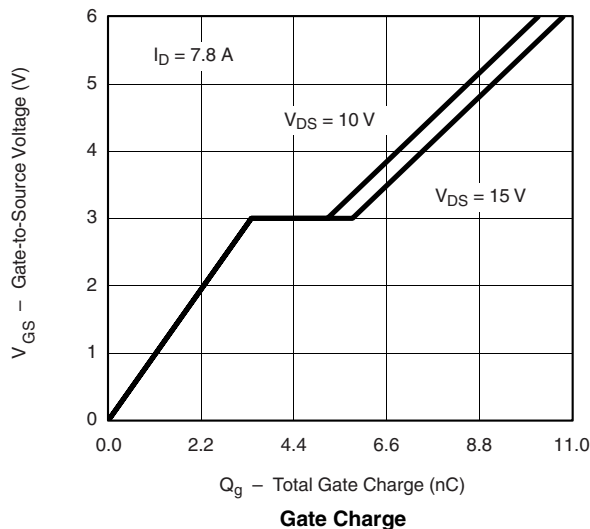
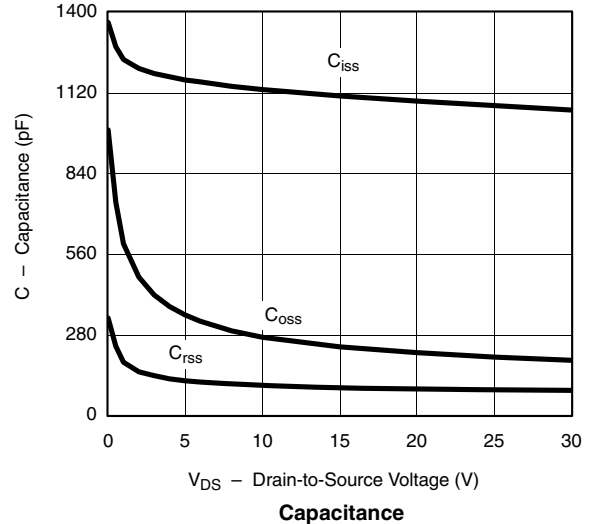
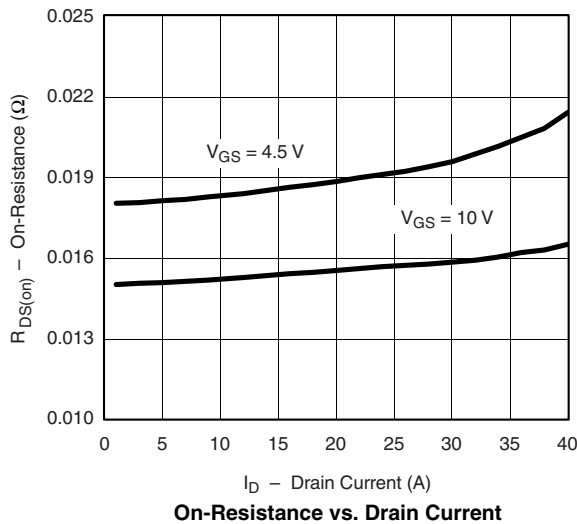
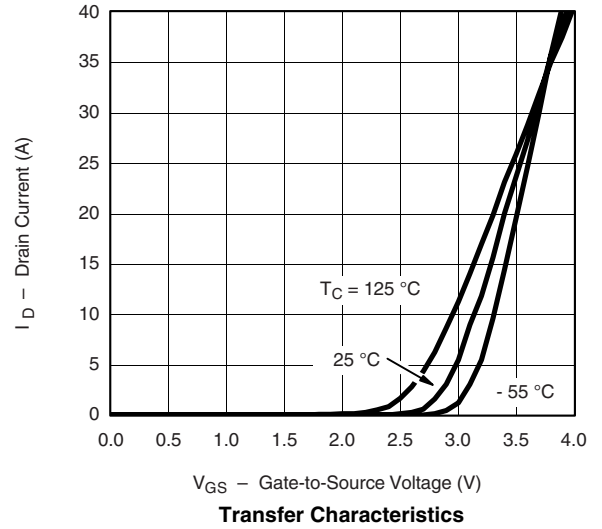
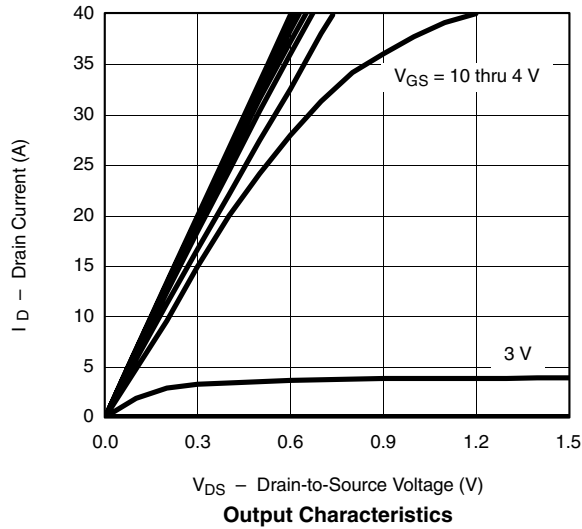
**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted**Output Characteristics****Transfer Characteristics****On-Resistance vs. Drain Current****Capacitance****Gate Charge****On-Resistance vs. Junction Temperature**

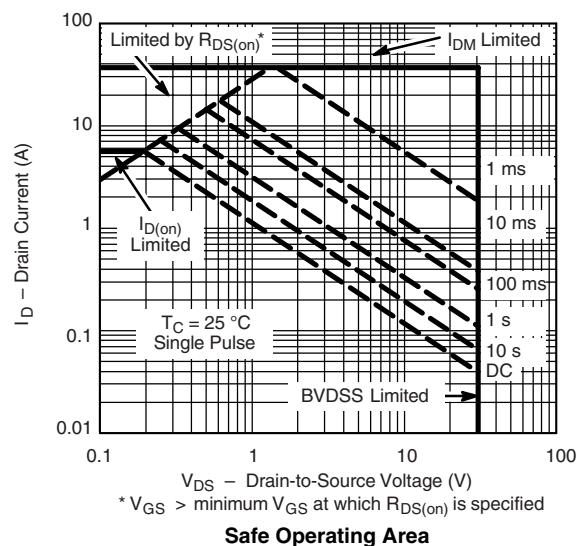
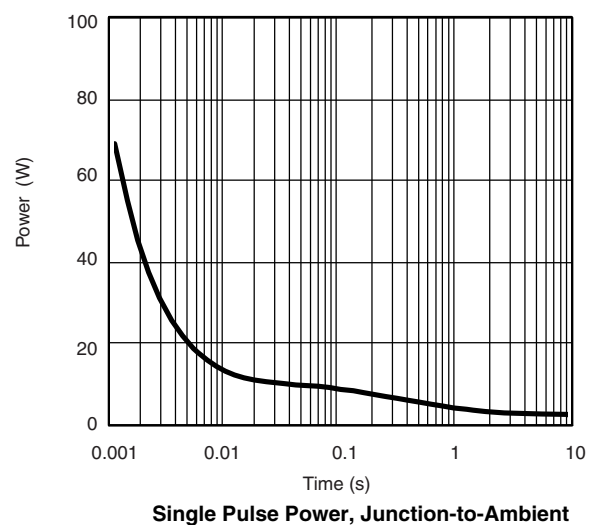
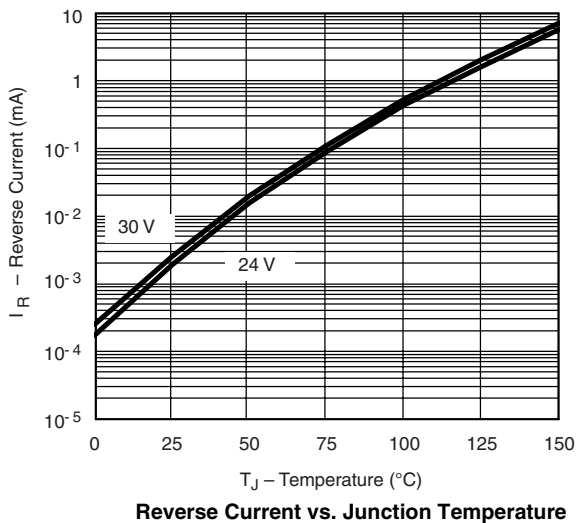
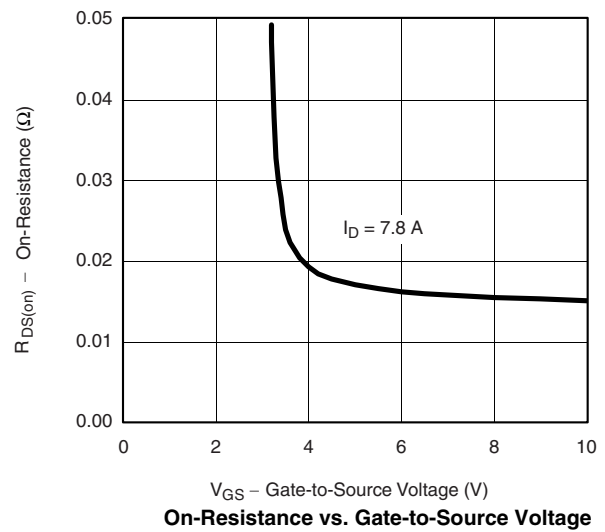
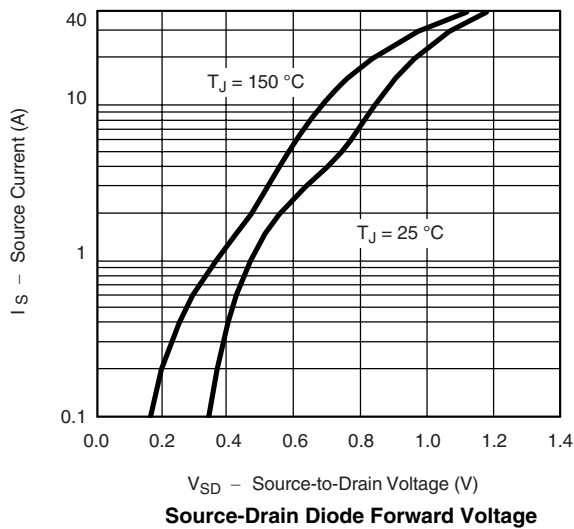
## CHANNEL-1 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



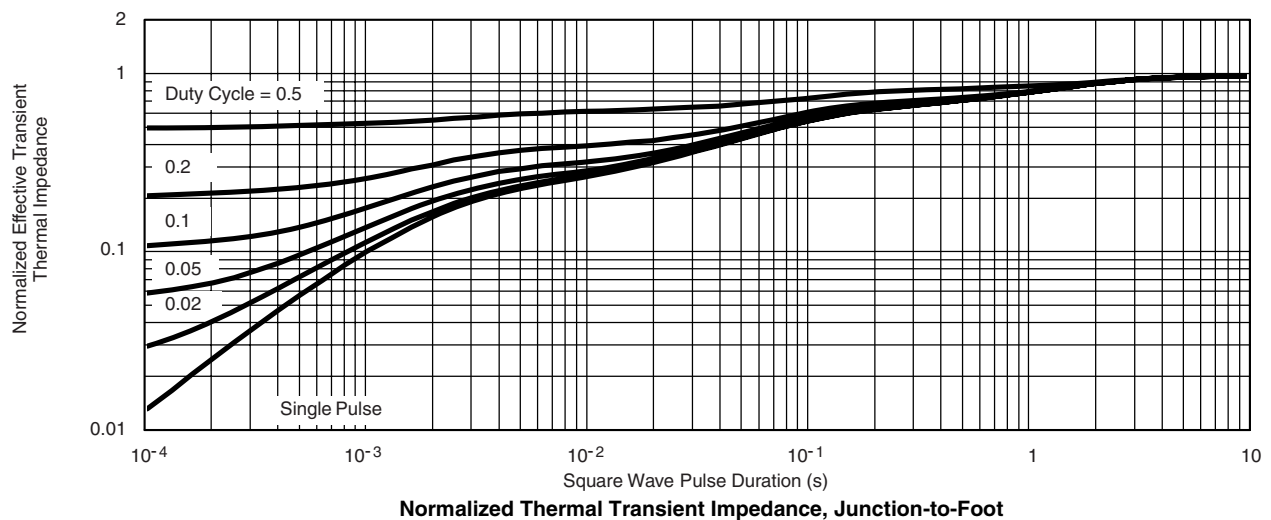
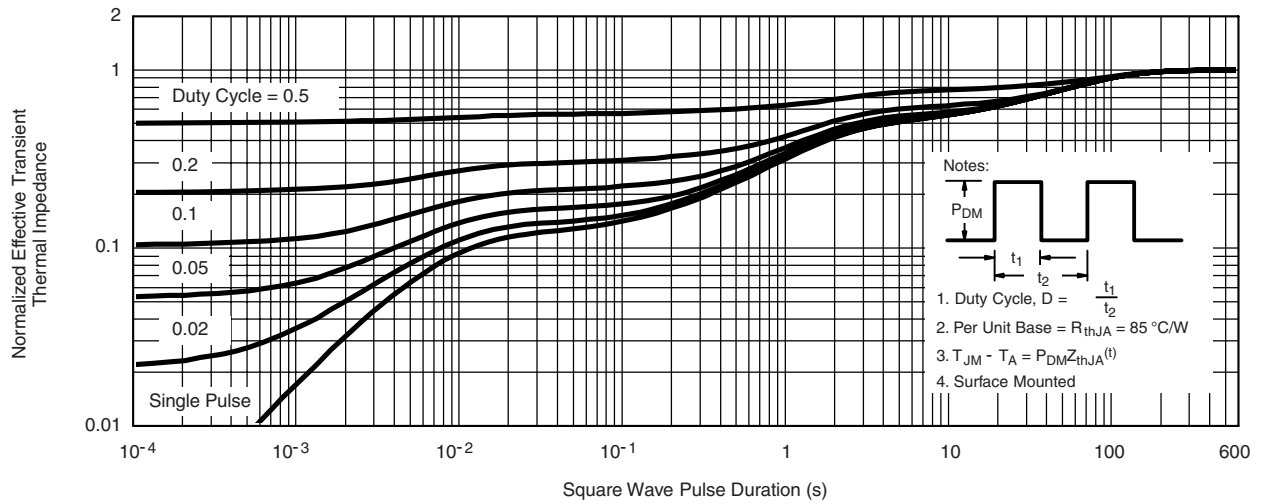
**CHANNEL-1 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

## CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



**CHANNEL-2 TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

## CHANNEL-2 TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

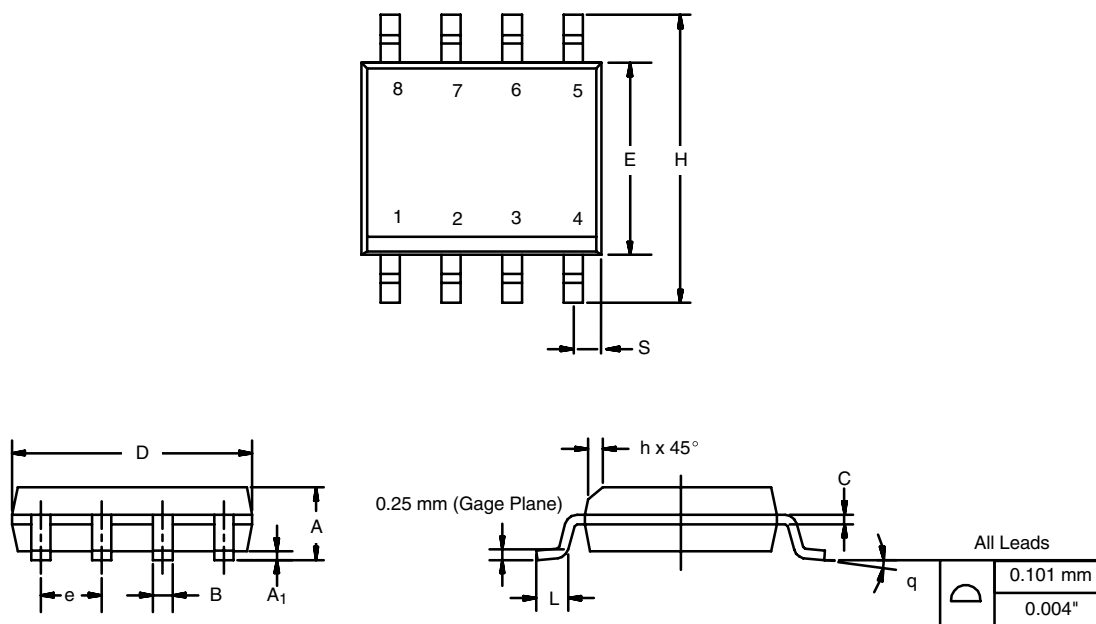


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## SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A <sub>1</sub>	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				

## RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads  
Dimensions in Inches/(mm)

[Return to Index](#)



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