

## 1. Pin Descriptions

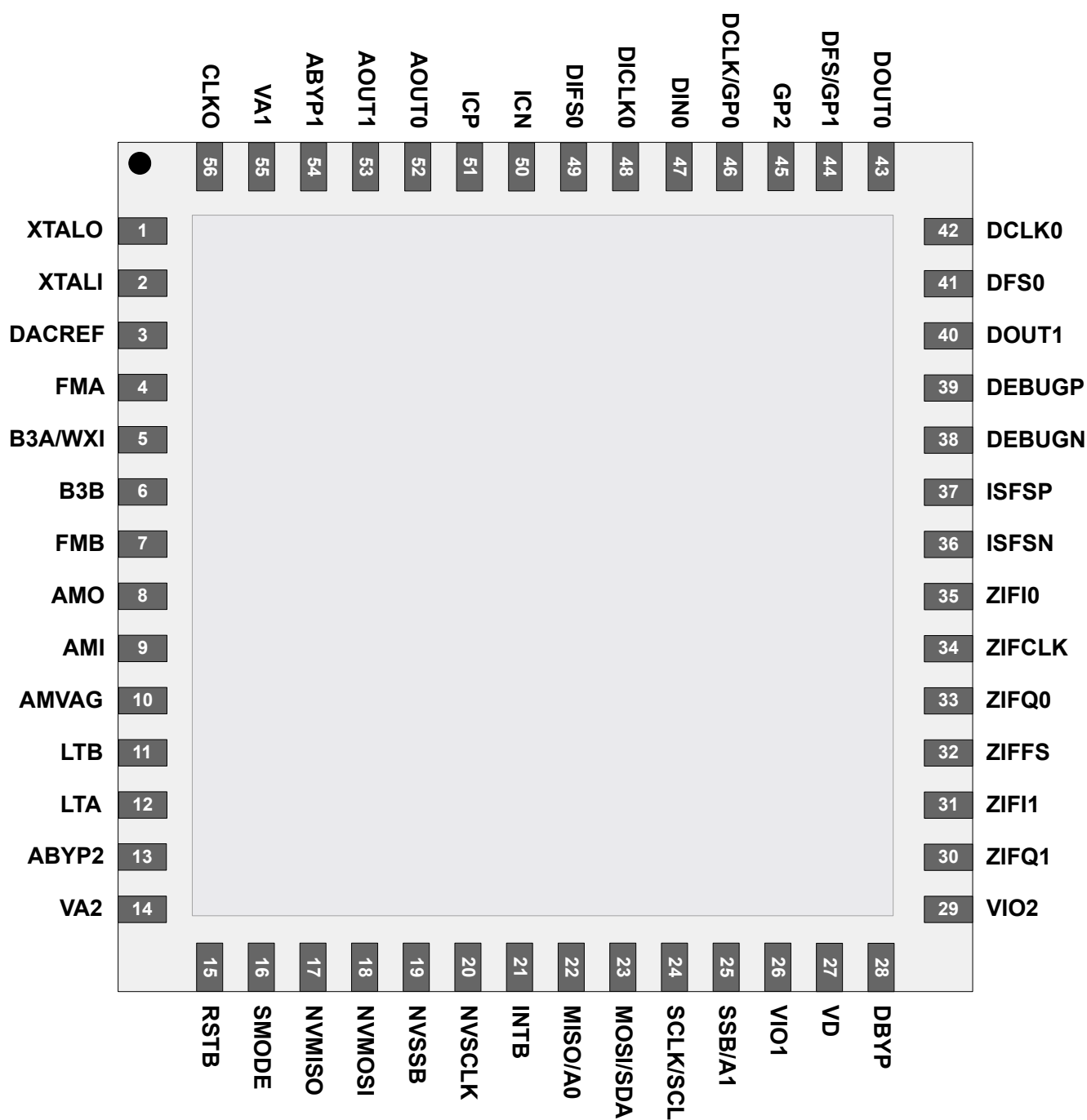


Figure 1.1. Si47961-62 Pinout Diagram

## 2. Package Outline

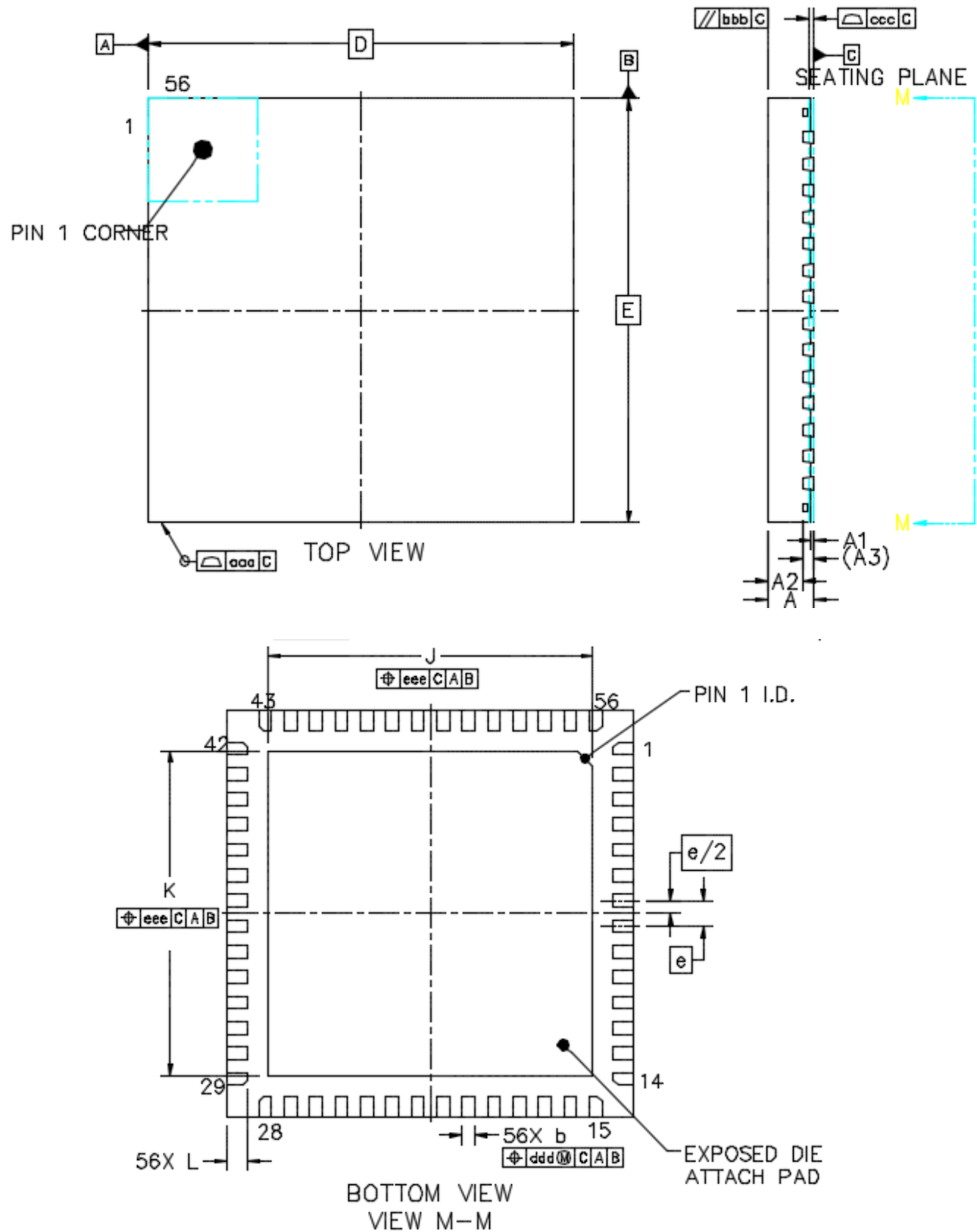
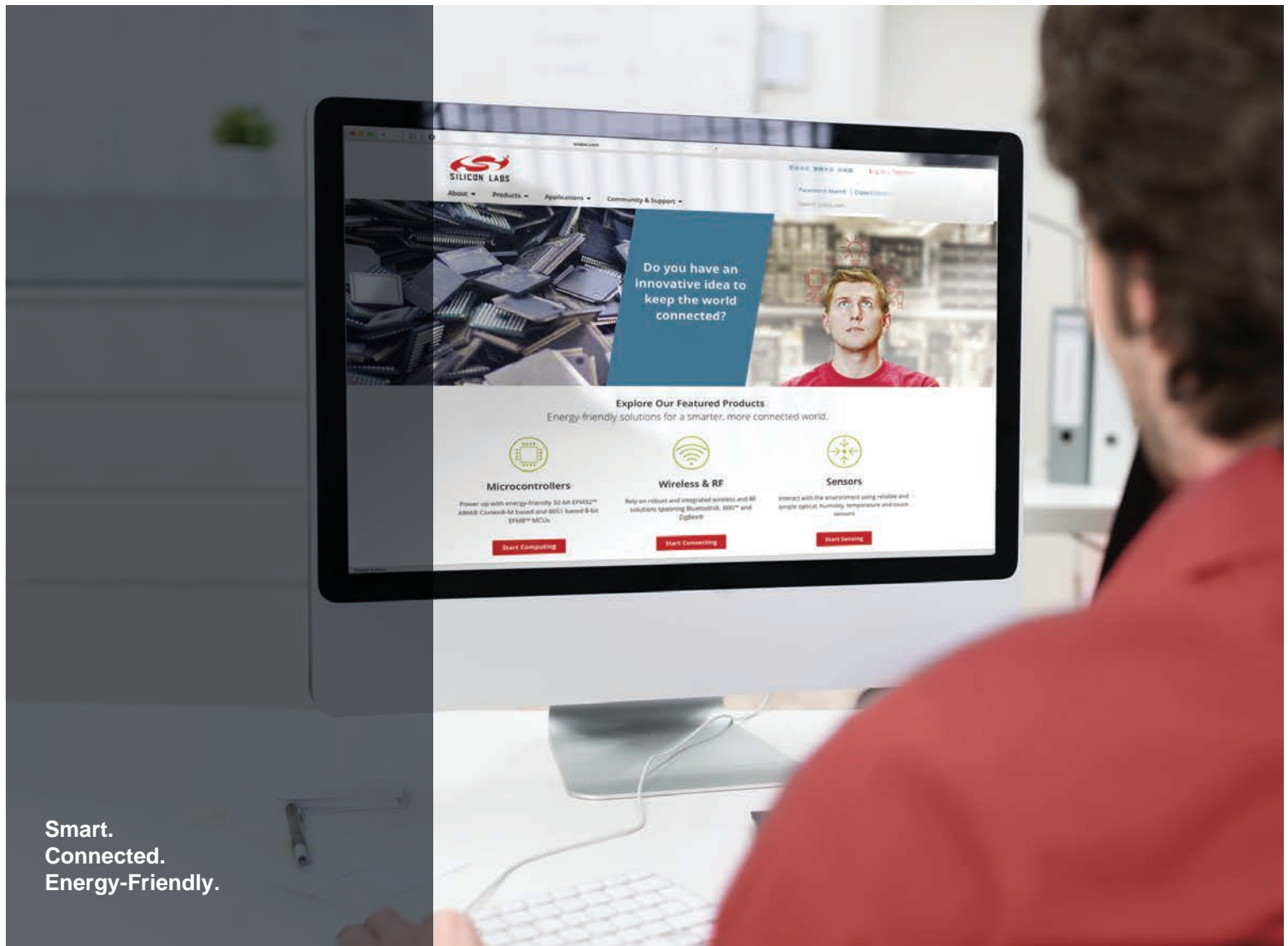


Figure 2.2. 56-Pin QFN

Table 2.1. Package Dimensions

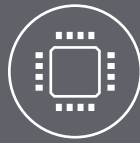
Dimensions	Min	Nom	Max
A	0.80	0.85	0.90
A1	0.00	0.035	0.05
b	0.20	0.25	0.30
A3	0.203 REF		
D	8.00 BSC		
e	0.5 BSC		
E	8.00 BSC.		
D2	6.30	6.40	6.50
E2	6.30	6.40	6.50
L	0.35	0.40	0.45
K	0.20	—	—
aaa	0.10		
bbb	0.10		
ccc	0.08		
ddd	0.10		
eee	0.10		
<b>Note:</b> 1. All dimensions shown are in millimeters (mm) unless otherwise noted. 2. Dimensioning and Tolerancing per ANSI Y14.5M-1994. 3. Recommended card reflow profile is per the JEDEC/IPC J-STD-020 specification for Small Body Components.			



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