



## **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Ambient Temperature with Power Applied	40°C to +85°C
Supply Voltage to Ground Potential	0.5V to +4.6V
DC Input Voltage	0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **DC Electrical Characteristics** (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ , $V_{CC} = 3.3\text{V} \pm 10\%$ )

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	<b>Typ.</b> <sup>(2)</sup>	Max.	Units
$V_{ m IH}$	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			17
$V_{\mathrm{IL}}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
$I_{IH}$	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
$I_{IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μΑ
I <sub>OZH</sub>	High Impedance Output Current	$0 \le I_N, Y_N \le V_{CC}$			±1	
$V_{IK}$	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18mA$			-1.2	V
R <sub>ON</sub>	Switch On-Resistance <sup>(3)</sup>	$V_{CC} = Min., V_{IN} = 0.0V,$ $I_{ON} = 48mA \text{ or } 64mA$		5	8	Ω
		$V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15mA$		10	17	

### Notes:

- . For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at  $V_{CC} = 3.3V$ ,  $T_A = 25$ °C ambient and maximum loading.
- 3. Measured by the voltage drop between I and Y pin at indicated current through the switch. On-Resistance is determined by the lower of the voltages on the two (I,Y) pins.

### Capacitance ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Cupacitance (IA 25 C, I I MILE)				
Parameters <sup>(1)</sup>	Description	<b>Test Conditions</b>	Тур.	Units
$C_{IN}$	Input Capacitance		3.0	
$C_{OFF}$	I <sub>A</sub> /I <sub>B</sub> Capacitance, Switch Off	$V_{IN} = 0V$	8.0	pF
C <sub>ON</sub>	I <sub>A</sub> /I <sub>B</sub> Capacitance, Switch On		36.0	

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#### **Notes:**

1. This parameter is determined by device characterization but is not production tested.





**Power Supply Characteristics** 

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	<b>Typ.</b> <sup>(2)</sup>	Max.	Units
$I_{CC}$	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND$ or $V_{CC}$		0.1	3.0	•
$\Delta I_{CC}$	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{IN} = 3.0V^{(3)}$			750	μΑ

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at  $V_{CC} = 3.3V$ ,  $+25^{\circ}C$  ambient.
- 3. Per TTL driven input (control inputs only); I and Y pins do not contribute to I<sub>CC</sub>.

**Switching Characteristics over Operating Range** 

Parameters	Description	Test Conditions	Com.		T.L *4
			Min.	Max.	Units
$t_{IY}$	Propagation Delay <sup>(1,2)</sup> In to Yn			0.25	
$t_{SY}$	Bus Select Time, Sn to Yn	$C_{\rm L} = 50 \rm pF$	1	4.0	
$t_{PZH}$	Bus Enable Time, E to Yn	$C_{L} = 50 pF$ $R_{L} = 500 \Omega$	1	3.8	ns
t <sub>PHZ</sub> t <sub>PLZ</sub>	Bus Disable Time, En to Y		1	5.2	

#### Notes:

- 1. This parameter is guaranteed but not tested on Propagation Delays.
- 2. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

## **Applications Information**

### **Logic Inputs**

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail<sup>®</sup> minimizes power consumption.

### **Power-Supply Sequencing and Hot Plug Information**

Proper power-supply sequencing is recommended for all CMOS devices. Always apply  $V_{CC}$  and GND before applying signals to input/output or control pins.

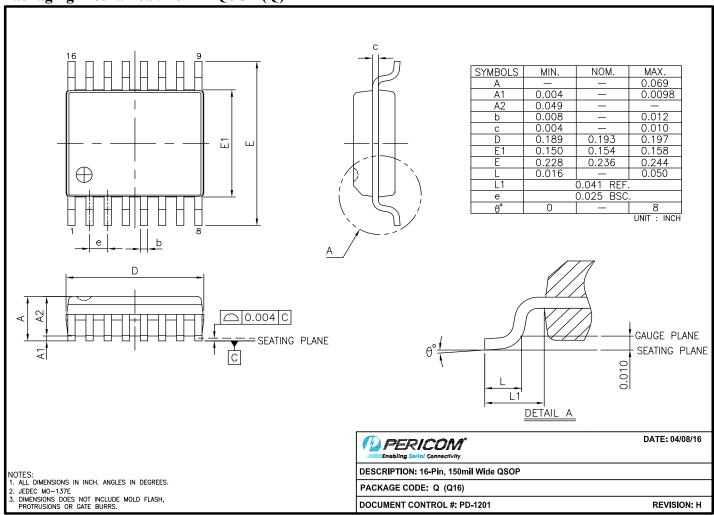
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Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.





# Packaging Mechanical: 16-Pin QSOP (Q)



16-0056

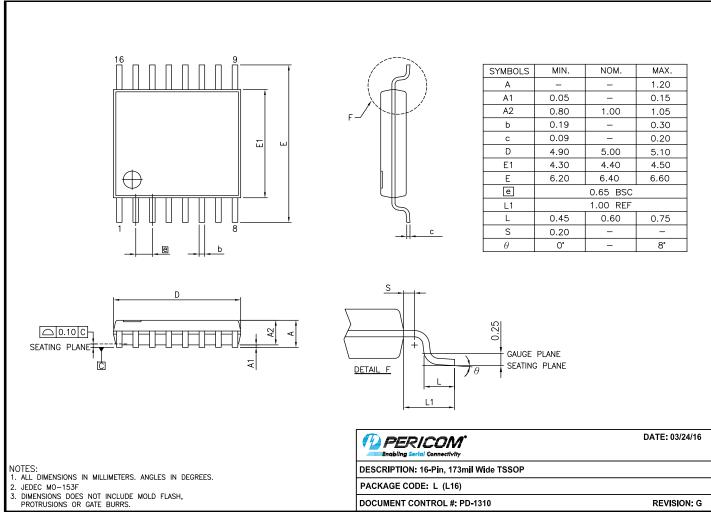
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# Packaging Mechanical: 16-Pin TSSOP (L)



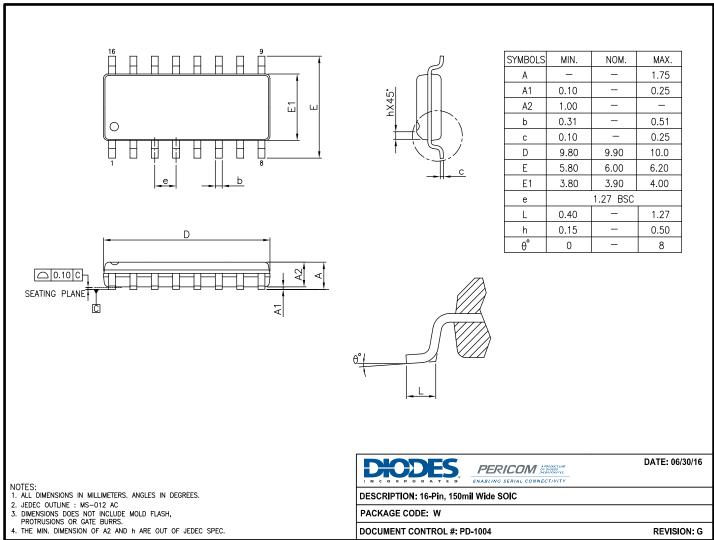
16-0061

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# Packaging Mechanical: 16-Pin SOIC (W)



16-0145

Note: For latest package info, please check: http://www.pericom.com/support/packaging/packaging-mechanicals-and-thermal-characteristics/

## **Ordering Information**

Ordering Code	Package Code	Package Desciption
PI3B3253QE	Q	16-pin, 150mil Wide (QSOP)
PI3B3253QEX	Q	16-pin, 150mil Wide (QSOP), Tape & Reel
PI3B3253LE	L	16-pin, 173mil Wide (TSSOP)
PI3B3253LEX	L	16-pin, 173mil Wide (TSSOP), Tape & Reel
PI3B3253WE	W	16-pin, 150mil Wide (SOIC)
PI3B3253WEX	W	16-pin, 150mil Wide (SOIC), Tape & Reel

## Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel





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