

## PolySwitch® PTC Devices

**Overcurrent Protection Device** 

PRODUCT: nanoSMD400LR-2

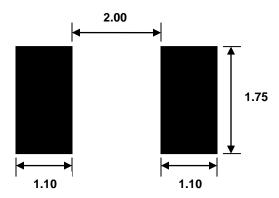
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**REV LETTER: A** 

REV DATE: MARCH 30, 2017

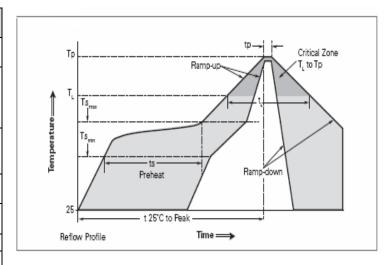
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#### Recommended pad layout (mm.)



### Recommended reflow profile

Profile Feature	Pb-Free Assembly
Average ramp up rate (Ts <sub>max</sub> to Tp)	3°C/s max.
Preheat • Temperature min. (Ts <sub>min</sub> ) • Temperature max. (Ts <sub>max</sub> ) • Time (ts <sub>min</sub> to ts <sub>max</sub> )	150°C 200°C 60-120s
Time maintained above: • Temperature (T <sub>L</sub> ) • Time (t <sub>L</sub> )	217°C 60-150s
Peak/Classification temperature (Tp)	260°C
Time within 5°C of actual peak temperature (tp)	30s max.
Ramp down rate	2°C/s max.
Time 25°C to peak temperature	8 mins max.



Note: All temperatures refer to top side of the package, measured on the package body surface.

#### Solder reflow recommendation

- Recommended reflow methods: IR, hot air and Nitrogen
- Recommended maximum solder paste thickness: 0.25mm
- Recommended minimum stencil thickness: 0.1mm
- Devices can be cleaned using standard methods and aqueous solvents.
- LF believes the optimum conditions for forming acceptable solder fillets occur when a reasonable amount of solder paste is placed underneath each device's termination. As such, we request that customers comply with our recommended solder pad layouts.
- Customer should validate that the solder paste amount and reflow recommendations meet its application.
- LF requests that customer board layouts refrain from placing raised features (e.g. vias, nomenclature, traces, etc.) underneath PolySwitch devices. It is possible that raised features could negatively impact solderability performance of our devices.



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