

LVDS/ 3.3V or 2.5V/ 7.0×5.0mm



RoHS Compliant

Features

- Miniature ceramic package
- Highly reliable with seam welding
- LVDS output
- Supply voltage $V_{CC}=3.3V$
- $\pm 25 \times 10^{-6}$ available

Table 1

Freq. Code	Tol. $\times 10^{-6}$	Operating Temperature Range (°C)	Note
0	± 50	0 to +70	Standard specifications
S	± 30		
U	± 25		
F	± 100	-40 to +85	With only certain frequencies
G	± 50		
6	± 50	-40 to +105	

How to Order

KC7050P 125.000 L 3 0 J 00
 ① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (7.0×5.0mm SMD)
- ② Output Frequency
- ③ Output Type (LVDS)
- ④ Supply Voltage (2 : 2.5V or 3 : 3.3V)
- ⑤ Frequency Tolerance (See Table 1)
- ⑥ Symmetry/ INH Function
(45/ 55%, Stand-by)
J : Low Phase Noise
E : Standard
- ⑦ Customer Special Model Suffix
(STD Specification is "00")

Packaging (Tape & Reel 1000 pcs./ reel)

Specifications

Item	Symbol	Specifications		Units	Conditions
		KC7050P-L2	KC7050P-L3		
Output Frequency Range ^{Note1}	fo	25 to 175		MHz	
Frequency Tolerance	f_tol	±50/ −40 to +105°C		ppm	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration
		±100/ −40 to +85°C			
		±50/ −40 to +85°C			
		±50/ 0 to +70°C			
		±30/ 0 to +70°C			
		±25/ 0 to +70°C			
Storage Temperature Range	T_stg	−55 to +125		°C	
Operating Temperature Range	T_use	0 to +70		°C	Standard Specifications Extend (Option)
		−40 to +105			
Max. Supply Voltage	—	−0.5 to +5.0		V	
Supply Voltage	V _{CC}	+2.375 to +2.625	+2.97 to +3.63	V	
Current Consumption	I _{CC}	70 max.		mA	
Stand-by Current	I_std	30 max.		μA	
Symmetry	SYM	50±5		%	100ohm @crossing point
Rise/ Fall Time (20% V _{CC} to 80% V _{CC} Maximum Loaded)	tr/ tf	0.6 max.		ns	100ohm
Low Level Output Voltage ^{Note2}	V _{OL}	0.9 min. Typ.:1.1		V	
High Level Output Voltage ^{Note2}	V _{OH}	1.6 max. Typ.:1.43		V	
Differential Output Voltage ^{Note2}	V _{OD}	247 to 454 Typ.:330		mV	
Differential Output Voltage Error ^{Note2}	dV _{OD}	50 max.		mV	dV _{OD} = V _{OD1} −V _{OD2}
Offset Voltage	V _{OS}	1.125 to 1.375		V	
Offset Voltage Error	dV _{OS}	50 max.		mV	dV _{OS} = V _{OS1} −V _{OS2}
Output Load	R _L	100		ohm	LVDS Output
Input Voltage Range	V _{IN}	0 to V _{CC}		V	
Low Level Input Voltage	V _{IL}	30% V _{CC} max.		V	
High Level Input Voltage	V _{IH}	70% V _{CC} min.		V	
Disable Time	t _{dis}	200 max.		ns	
Enable Time	t _{ena}	10 max.		ms	
Start-up Time	t _{str}	10 max.		ms	@Minimum operating voltage to be 0 sec.
Deterministic Jitter	DJ	2 max.		ps	
1 Sigma Jitter	J _{Sigma}	4 max.		ps	Measured with Wavecrest SIA-3000
Peak to Peak Jitter	J _{PK-PK}	30 max.		ps	

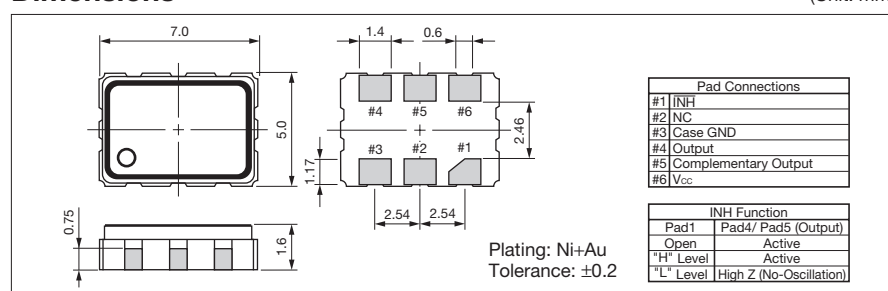
Note : All electrical characteristics are defined at the maximum load and operating temperature range.

Note1: Please contact us for inquiry about operating temperature range, available frequencies and other conditions.

Note2: DC characteristic

Dimensions

(Unit: mm)



Recommended Land Pattern

(Unit: mm)

