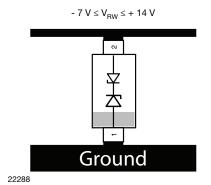
# Vishay Semiconductors

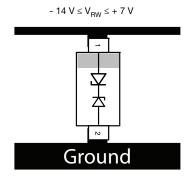
## Bidirectional Asymmetrical (BiAs) Single Line ESD-Protection Diode in SOD923



#### **CUT THE SPIKES WITH VCUT0714A-02Z**

The VCUT0714A-02Z is a bidirectional but asymmetrical (BiAs) ESD-protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0714A-02Z offers a high isolation (low leakage current, small capacitance) within the specified working range of - 7 V to + 14 V or - 14 V and + 7 V. Due to the short leads and small package size of the tiny SOD-923 package the line inductance is very low, so that fast transients like an ESD-strike can be clamped with minimal over- or undershoots.





ELECTRICAL CHARACTERISTICS VCUT0714A-02Z									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse working voltage	at I = 1 μA	$V_{RWM}$	14	-	-	V			
Reverse current	at V = 14 V	I <sub>R</sub>	-	-	0.1	μΑ			
Reverse breakdown voltage	at I = 1 mA	$V_{BR}$	14.5	-	-	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	-	27	V			
	at I <sub>PP</sub> = I <sub>PPM</sub> = 2 A		-	-	30	V			
Capacitance	at V = 0 V; f = 1 MHz	- C <sub>D</sub>	-	8	8.5	pF			
	at V = 7 V; f = 1 MHz		-	4	-	pF			

### Note

Ratings at 25 °C, ambient temperature unless otherwise specified. Measured from pin 2 to pin 1.

ELECTRICAL CHARACTERISTICS VCUT0714A-02Z									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse working voltage	at I = 1 μA	$V_{RWM}$	7	-	-	V			
Reverse current	at V = 7 V	I <sub>R</sub>	-	-	0.1	μΑ			
Reverse breakdown voltage	at I = 1 mA	$V_{BR}$	7.3	-	-	V			
Reverse clamping voltage	at I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	=.	13	V			
	at I <sub>PP</sub> = I <sub>PPM</sub> = 5 A		-	-	17	V			
Capacitance	at V = 0 V; f = 1 MHz	- C <sub>D</sub>	-	8	8.5	pF			
	at V = 3.5 V; f = 1 MHz		=	6.4	-	pF			

#### Note

Ratings at 25 °C, ambient temperature unless otherwise specified. Measured from pin 1 to pin 2.

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For technical questions, contact: <a href="mailto:ESDprotection@vishay.com">ESDprotection@vishay.com</a>

Document Number: 81627



## Bidirectional Asymmetrical (BiAs) Single Line ESD-Protection Diode in SOD923

# Vishay Semiconductors

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

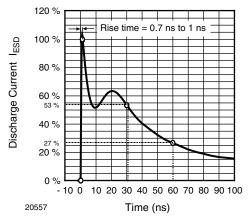


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega/150$  pF)

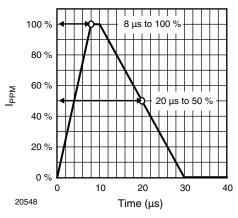


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

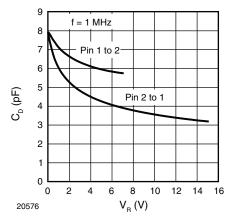


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

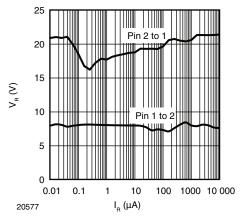


Fig. 4 - Typical Reverse Voltage  $V_R$  vs. Reverse Current  $I_R$ 

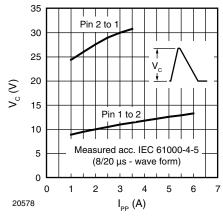


Fig. 5 - Typical Peak Clamping Voltage  $V_{\rm C}$  vs. Peak Pulse Current  $I_{\rm PP}$ 

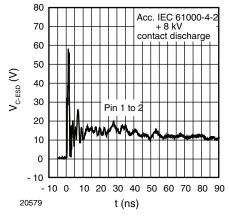


Fig. 6 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

# Vishay Semiconductors

## Bidirectional Asymmetrical (BiAs) Single Line ESD-Protection Diode in SOD923



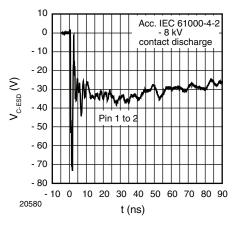


Fig. 7 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

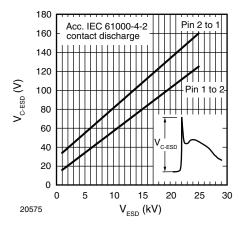
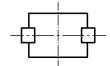
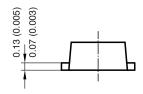
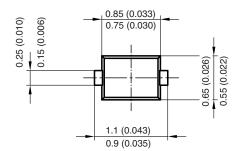


Fig. 8 - Typical Peak Clamping Voltage at ESD Contact Discharge (acc. IEC 61000-4-2)

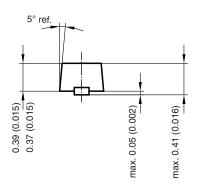
### PACKAGE DIMENSIONS in millimeters (inches): SOD-923



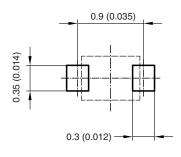




Document no.: S8-V-3880.05-001 (4) Rev. 1 - Date: 05.July.2006



Foot print recommendation:



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