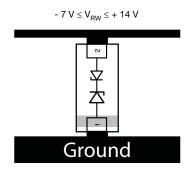
# VCUT0714A-HD1, VCUT0714AHD1

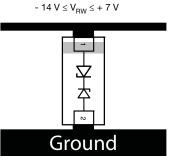


### **Vishay Semiconductors**

#### **CUT THE SPIKES**

The VCUT0714Ax is a bidirectional but asymmetrical (BiAs) ESD protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0714Ax offers a high isolation (low leakage current, small capacitance) within the specified working range of -7 V to +14 V or -14 V and +7 V. Due to the short leads and small package size of the tiny LLP1006 package the line inductance is very low, so that fast transients like an ESD strike can be clamped with minimal over- or undershoots.





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<b>ELECTRICAL CHARACTERISTICS</b> (pin 2 to pin 1) (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	14	V		
Reverse voltage	At I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	14	-	-	V		
Reverse current	At V <sub>RWM</sub> = 14 V	I <sub>R</sub>	-	-	0.1	μA		
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	V <sub>BR</sub>	14.5	-	-	V		
Reverse clamping voltage	At I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	-	27	V		
	At $I_{PP} = I_{PPM} = 2 A$	V <sub>C</sub>	-	-	30	V		
Capacitance	At $V_R = 0$ V; f = 1 MHz	CD	-	8	8.5	pF		
	At V <sub>R</sub> = 7 V; f = 1 MHz	CD	-	4	-	pF		

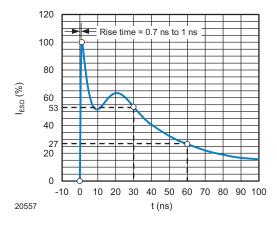
ELECTRICAL CHARACTERISTICS (pin 1 to pin 2) (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	1	lines			
Reverse stand-off voltage	Max. reverse working voltage	V <sub>RWM</sub>	-	-	7	V			
Reverse voltage	At I <sub>R</sub> = 0.1 μA	V <sub>R</sub>	7	-	-	V			
Reverse current	At V <sub>RWM</sub> = 7 V	I <sub>R</sub>	-	-	0.1	μA			
Reverse breakdown voltage	At I <sub>R</sub> = 1 mA	V <sub>BR</sub>	7.3	-	-	V			
Reverse clamping voltage	At I <sub>PP</sub> = 1 A	V <sub>C</sub>	-	-	13	V			
	At I <sub>PP</sub> = I <sub>PPM</sub> = 5 A	V <sub>C</sub>	-	-	17	V			
Capacitance	At V = 0 V; f = 1 MHz	CD	-	8	8.5	pF			
	At V = 3.5 V; f = 1 MHz	CD	-	6.4	-	pF			

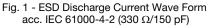


## VCUT0714A-HD1, VCUT0714AHD1

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### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)





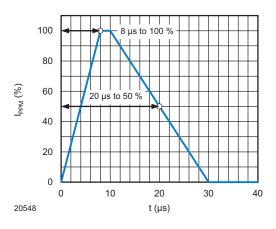


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

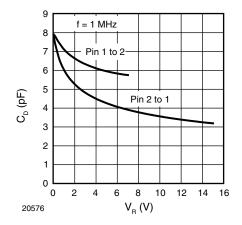


Fig. 3 - Typical Capacitance  $C_D$  vs. Reverse Voltage  $V_R$ 

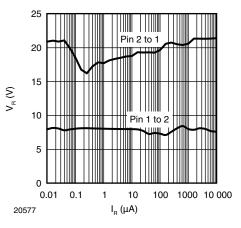


Fig. 4 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

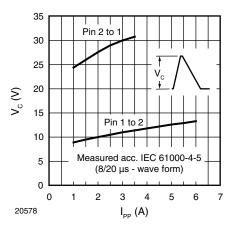


Fig. 5 - Typical Peak Clamping Voltage V\_C vs. Peak Pulse Current  $I_{PP}$ 

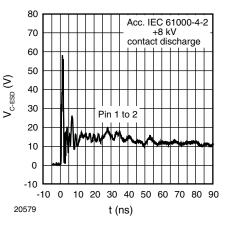
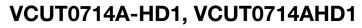


Fig. 6 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

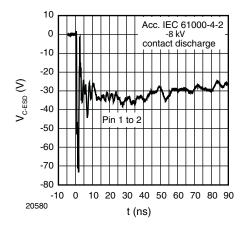
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Fig. 7 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

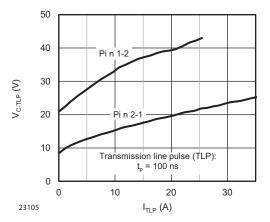
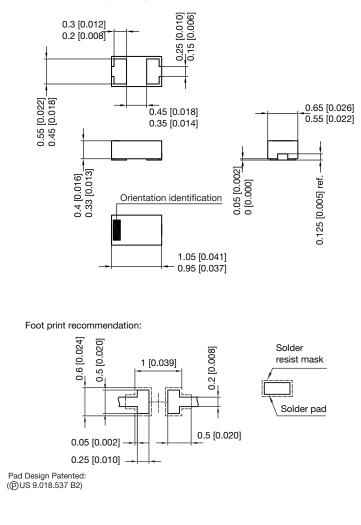


Fig. 8 - Typical Peak Clamping Voltage vs. Peak Pulse Current

#### PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L



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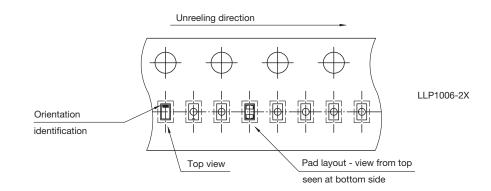
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# VCUT0714A-HD1, VCUT0714AHD1

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