Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC			SYMBOL	RATING	UNIT	
LED	Forward Current		lF	50	mA	
	Forward Current Derati (Ta ≥ 53°C)	ng	ΔI _F / °C	-0.7	mA / °C	
	Peak Forward Current (100μs pulse, 100pps)		IFP	1	Α	
	Reverse Voltage		VR	5	V	
	Power Dissipation		PD	100	mW	
	Power Dissipation Derating (Ta ≥ 25°C)		ΔP _D / °C	-1.0	mW / °C	
	Junction Temperature		Tj	125	°C	
	Off-State Output Termin	Off-State Output Terminal Voltage		400	V((
	On-Stage RMS	Ta = 25°C	l=	100	mA.	
DETECTOR	Current	Ta = 70°C	I _{T(RMS)}	50	mA	
	On-State Current Derating (Ta ≥ 25°C)		ΔI _T / °C	-1.1	mA / °C	
	Peak On-Stage Current (100μs pulse, 120pps)	t	ITP	2	\bigcirc A	
	Peak Nonrepetitive Sur Current (P _W = 10ms)	ge	I _{TSM}	1(2	, A	
	Power Dissipation		PD	(300)	mW	
	Power Dissipation Dera (Ta ≥ 25°C)	ating	ΔP _D / °C	-4.0	mW / °C	
	Junction Temperature		Tj (115	~c /	
Stora	age Temperature Range		T _{stg}	-55 to 150/	°C	
Operating Temperature Range			Topr	−40 to 100	√ °C)	
Lead Soldering Temperature (10 s)			(T _{sol})	260	°C\	
Total Package Power Dissipation			PT	330	mW	
Total Package Power Dissipation Derating (Ta ≥ 25°C)			ΔPτ / °C	-4.4	mW / °C	
	tion Voltage 60 s., R.H. ≤ 60 %)	(Note 1)	BVs	5000	Vrms	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: Pins 1, 2 and 3 shorted together and pins 4 and 6 shorted together.

Recommended Operating Conditions

CHARACTERISTIC	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	VAC	_	_	120	Vac
Forward Current	l _F *	15	20	25	mA
Peak On-Stage Current	ITP	_	_	1	Α
Operating Temperature	Topr	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

*: In the case of TLP3042

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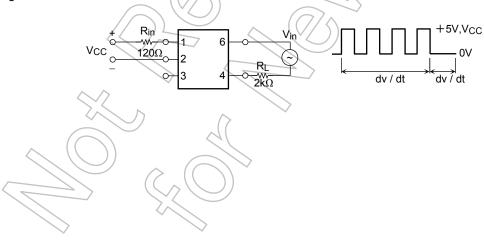
Individual Electrical Characteristics (Ta = 25°C)

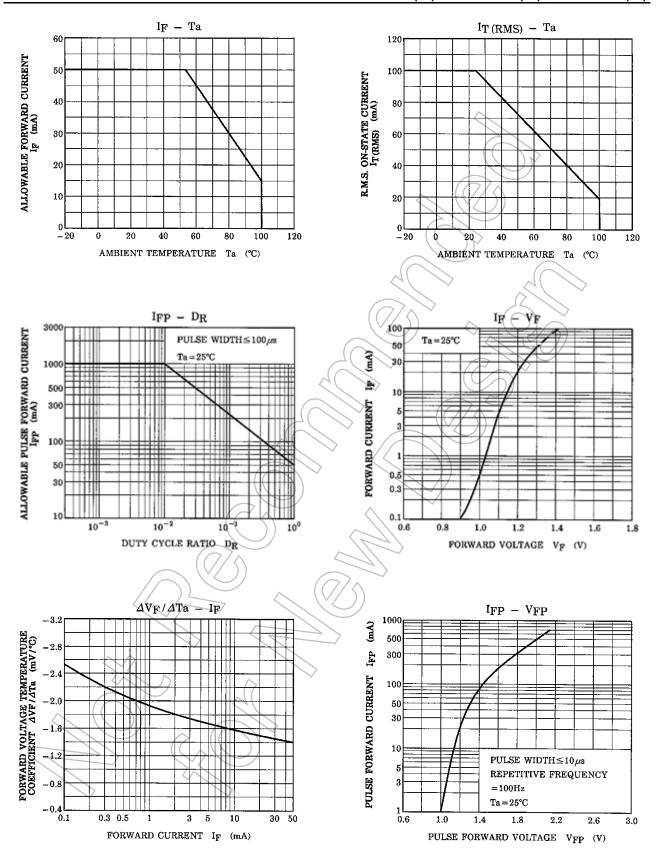
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	VF	I _F = 10 mA	1.0	1.15	1.3	V
	Reverse Current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	Ст	V = 0 V, f = 1 MHz	//	10	_	pF
DETECTOR	Peak Off-State Current	IDRM	V _{DRM} = 400 V		10	100	nA
	Peak On-Stage Voltage	V _{TM}	I _{TM} = 100 mA		1.7	3.0	V
	Holding Current	lΗ	(7)	7(0.6	-	mA
	Critical Rate of Rise of Off- State Voltage	dv / dt	V _{in} = 120 Vrms, Ta = 85 °C (Fig.1)	200	500	1	V / μs
	Critical Rate of Rise of Commutating Voltage	dv / dt(c)	V _{in} = 30 Vrms, IT = 15 mA (Fig.1)	-	0.2	-	V / μs

Coupled Electrical Characteristics (Ta = 25°C)

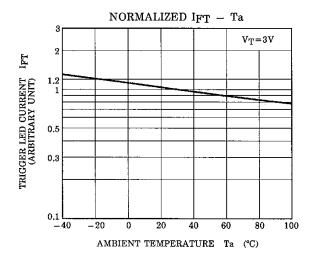
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
	TLP3041(S)					15	
Trigger LED Current	TLP3042(S)	lfT	VT = 3 V		5	10	mA
	TLP3043(S)			2	_	5	
Inhibit Voltage		VIH	IF = Rated IFT	_		40	V
Leakage in Inhibited State		III	I _F = Rated I _{FT} V _T = Rated V _{DRM}		100	300	μΑ
Capacitance Input to Output		CS	V _S = 0 V, f = 1 MHz	_	0.8	_	pF
Isolation Resistance		Rs	V _S = 500V, R.H. ≤ 60 %)	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation Voltage		BVs	AC, 60 s	5000		_	Vrms

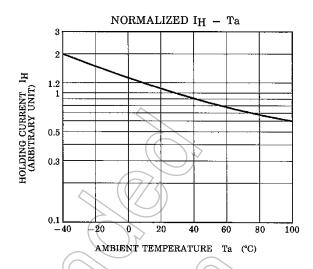
Fig. 1 dv / dt test circuit

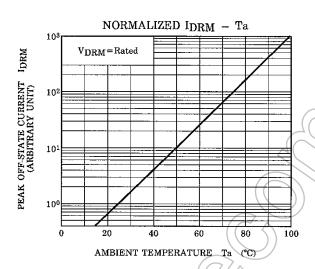


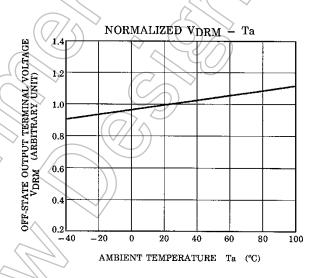


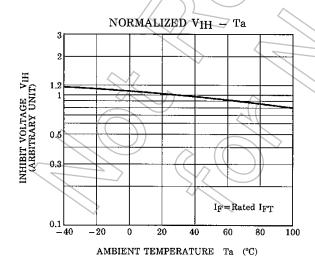
NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

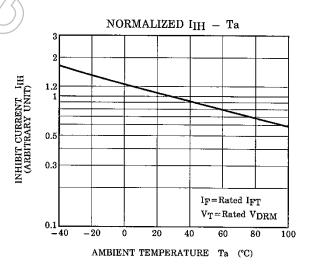












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