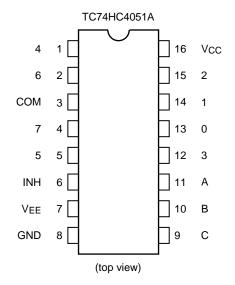
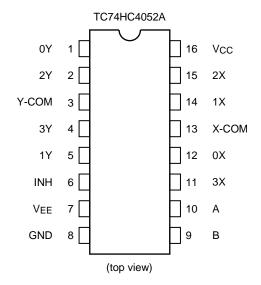
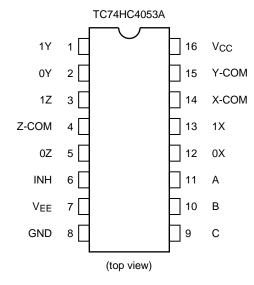


### **Pin Assignment**

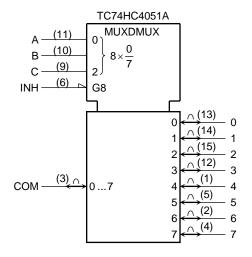


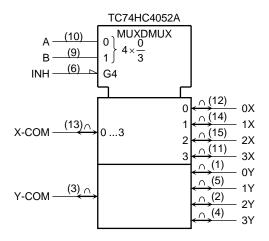


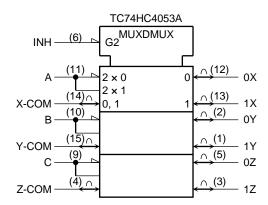




### **IEC Logic Symbol**







### **Truth Table**

	Contro	I Inputs		"ON" Channel				
Inhibit	C*	В	Α	HC4051A	HC4052A	HC4053A		
L	L	L	L	0	0X, 0Y	0X, 0Y, 0Z		
L	L	L	Н	1	1X, 1Y	1X, 0Y, 0Z		
L	L	Н	L	2	2X, 2Y	0X, 1Y, 0Z		
L	L	Н	Н	3	3X, 3Y	1X, 1Y, 0Z		
L	Н	L	L	4	_	0X, 0Y, 1Z		
L	Η	L	Η	5	_	1X, 0Y, 1Z		
L	Н	Н	L	6	_	0X, 1Y, 1Z		
L	Н	Н	Н	7	_	1X, 1Y, 1Z		
Н	Х	Х	Х	None	None	None		

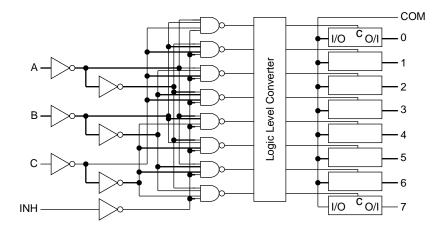
X: Don't care

\*: Except HC4052A

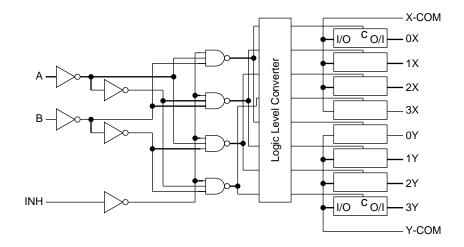


## **System Diagram**

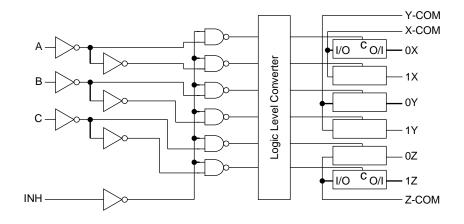
### TC74HC4051A



### TC74HC4052A



### TC74HC4053A





### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	-0.5 to 7	V
Supply voltage range	VCC-VEE	-0.5 to 13	V
Control input voltage	VIN	-0.5 to V <sub>CC</sub> + 0.5	V
Switch I/O voltage	VI/O	VEE - 0.5 to VCC + 0.5	V
Control input diode current	lcĸ	±20	mA
I/O diode current	liok	±20	mA
Switch through current	ΙΤ	±25	mA
DC V <sub>CC</sub> or ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP/TSSOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

### **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	2 to 6	V
Supply voltage range	VEE	-6 to 0	V
Supply voltage range	VCC-VEE	2 to 12	V
Control input voltage	VIN	0 to VCC	V
Switch I/O voltage	VI/O	VEE to VCC	V
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 (Vcc = 2.0 V)	
Control input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 (V <sub>CC</sub> = 4.5 V)	ns
		0 to 400 (V <sub>CC</sub> = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused control inputs must be tied to either VCC or GND.



## **Electrical Characteristics**

### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
	-,		VEE (V)	Vcc (V)	Min	Тур.	Max	Min	Max		
				2.0	1.50	_	_	1.50	_		
High-level control input voltage	VIHC	_		4.5	3.15	_	_	3.15	_	V	
				6.0	4.20	_	_	4.20	_		
				2.0	-	_	0.50	_	0.50		
Low-level control input voltage	$V_{ILC}$	_		4.5	_	_	1.35	_	1.35	V	
Ü				6.0	1	_	1.80	_	1.80		
		VIN = VILC or VIHC	GND	4.5	_	85	180	_	225		
		$V_{I/O} = V_{CC}$ to $V_{EE}$	-4.5	4.5	_	55	120	_	150		
		$I_{I/O} \leq 2 \; mA$	-6.0	6.0	1	50	100	_	125		
ON resistance	Ron	$V_{IN} = V_{ILC}$ or $V_{IHC}$ $V_{I/O} = V_{CC}$ or $V_{EE}$ $I_{I/O} \le 2$ mA	GND	2.0	_	150	_	_	_	Ω	
			GND	4.5	_	70	150	_	190		
			-4.5	4.5	_	50	100	_	125		
		11/0 = 2 111/4	-6.0	6.0	1	45	80	_	100		
Difference of ON		VIN = VILC or VIHC	GND	4.5	_	10	30	_	35		
resistance between	ΔR <sub>ON</sub>	$V_{I/O} = V_{CC}$ to $V_{EE}$	-4.5	4.5	_	5	12	_	15	Ω	
switches		$I_{I/O} \leq 2 \; mA$	-6.0	6.0	1	5	10	_	12		
Input/output leakage		Vos = Vcc or GND	GND	6.0		±60		±600			
current	IOFF	V <sub>IS</sub> = GND or V <sub>CC</sub>	-6.0	6.0			±100	_	±1000	nA	
(switch off)		VIN = VILC or VIHC	-0.0	0.0			±100	_	±1000		
Switch input leakage	I <sub>IZ</sub>	Vos = Vcc or GND	GND	6.0	_		±60	_	±600		
current		VIN = VILC or VIHC	-6.0	6.0	_		±100	_	±1000	nA	
(switch on, output open)			OND	0.0			0.4		4.0		
Control input current	I <sub>IN</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND	GND	6.0	_	_	±0.1	_	±1.0	μΑ	
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND	GND	6.0	_	_	4.0	_	40.0	μΑ	
			-6.0	6.0	_	_	8.0	_	80.0		



### AC Characteristics (CL = 50 pF, input: tr = tf = 6 ns, GND = 0 V)

Phase difference between input and output  Phase difference between input and output  All types  Al	Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
Phase difference between input and output  All types  A		Cymbol			VEE (V)	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic
Dutput enable time    April					GND	2.0	_	25	60	_	75	
Output enable time    1	Phase difference		A II 4		GND	4.5	_	6	12	_	15	
Output enable time    1pZL   4051A   (Note 1)   GND   2.0   -     64   225   -     280	output	ΦΙ/Ο	All types		GND	6.0	_	5	10	_	13	ns
Output enable time    1-pz	•				-4.5	4.5		4	_	_	_	
Output enable time					GND	2.0	-	64	225	-	280	
Output enable time    Total			4054 4	(Note 1)	GND	4.5	_	18	45	_	56	
Output enable time    t_{pZL} t_{pZH}			4051A	(Note 1)	GND	6.0	_	15	38	_	48	
Output enable time    1pZL					-4.5	4.5		18	_	_	_	
Output enable time    1pZH   4052A   (Note 1)   GND   6.0   -   15   38   -   48   18   -   -   -   -					GND	2.0		64	225	_	280	
1p2H	Output anabla tima	tpZL	40504	(Note 1)	GND	4.5	_	18	45	_	56	20
Output disable time    Application   Applica	Output enable time	tpZH	4052A	(Note 1)	GND	6.0	_	15	38	_	48	ns
Automatic properties of the pr					-4.5	4.5		18	_	_	_	
Output disable time    Automatical Control input capacitance   CIN   All types					GND	2.0	-	50	225	-	280	
Output disable time    International Control input capacitance   Cis   4051A			40504	(NInto 4)	GND	4.5	_	14	45	_	56	
Output disable time    t_{pLZ} t_{pHZ}			4053A	(Note 1)	GND	6.0	_	12	38	_	48	
Output disable time    International Control input capacitance   Control input capacitance   Cost   4051A   40					-4.5	4.5		14	_	_	_	
Output disable time    tplZ				(Note 1)	GND	2.0	_	100	250	_	315	
Output disable time    tpLZ			4051A		GND	4.5	_	33	50	_	63	ns
Output disable time    tplZ					GND	6.0	_	28	43	_	54	
Output disable time    tplZ					-4.5	4.5		29	_	_	_	
Output disable time  tpHZ  4052A (Note 1) GND 6.0 - 28 43 - 54  4053A (Note 1) GND 2.0 - 95 225 - 280  GND 4.5 - 30 45 - 56  GND 6.0 - 26 38 - 48  -4.5 4.5 - 26  Control input capacitance  CIN All types 5 10 - 10 pF  COMMON terminal capacitance  CIS 4052A 4053A  COS			4052A	(Note 1)	GND	2.0	_	100	250	_	315	
Control input capacitance	Outroot disable ties				GND	4.5	_	33	50	_	63	
A053A	Output disable time				GND	6.0	_	28	43	_	54	
A053A					-4.5	4.5		29	_	_	_	
A053A			40504	(Nata 4)	GND	2.0	_	95	225	_	280	
Control input capacitance					GND	4.5	_	30	45	_	56	
Control input capacitance  CIN  All types  ———————————————————————————————————			4053A	(Note 1)	GND	6.0	_	26	38	_	48	
Common terminal capacitance					-4.5	4.5		26	_	_	_	
COMMON terminal capacitance         CIS         4052A 4053A         -5.0         5.0         -         19         40         -         40         pF           SWITCH terminal capacitance         Cos         4051A         -5.0         5.0         -         7         15         -         15         pF           SWITCH terminal capacitance         Cos         4052A         -5.0         5.0         -         7         15         -         15         pF           Feedthrough capacitance         Clos         4051A         -         -         0.95         2         -         2         pF           Power dissipation capacitance         CPD         4052A         (Note 2)         GND         5.0         -         70         -         -         pF	Control input capacitance	CIN	All types		_	_	_	5	10	_	10	pF
Capacitance			4051A					36	70	_	70	
A053A		CIS	4052A		-5.0	5.0	_	19	40	_	40	pF
SWITCH terminal capacitance         Cos         4052A 4053A         -5.0         5.0         -         7         15         -         15         pF           Feedthrough capacitance         Clos         4051A 4052A 4053A         -5.0         5.0         -         0.95 2         2         -         2         pF           Power dissipation capacitance         CPD         4052A (Note 2) GND 5.0         -         70         -         -         pF	оприонально		4053A				_	11	20	_	20	
Cos 4052A -5.0 5.0 - 7 15 - 15 pr 4053A - 7 15 - 15 pr 4051A - 0.95 2 - 2 pr 4053A - 0.75 2 - 2 pr 4051A (Note 2) - 70 - 70 - 70 - 70 pr 4052A (Note 2) GND 5.0 - 71 - 70 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 - 75 pr 4052A (Note 2) GND 5.0 - 71 pr 4052A (Note 2) GND 5.0 pr 4052			4051A				_	7	15	_	15	
A053A		Cos	4052A		-5.0	5.0	_	7	15	_	15	pF
Feedthrough capacitance         Clos         4052A         -5.0         5.0         - 0.85         2         - 2         pF           4053A         4051A         (Note 2)         - 70         - 70         70         - Power dissipation capacitance	- Capacitatio		4053A				_	7	15	_	15	
Clos   4052A   -5.0   5.0   -   0.65   2   -   2   pr			4051A				_	0.95	2	_	2	
4053A     —     0.75     2     —     2       Power dissipation capacitance     CPD     4052A     (Note 2)     GND     5.0     —     71     —     —     pF		Cios	4052A		-5.0	5.0	_	0.85	2	_	2	pF
Power dissipation capacitance CPD 4052A (Note 2) GND 5.0 — 71 — — PF	- Sapasitarios		4053A				_	0.75	2	_	2	
capacitance CPD 4052A (Note 2) GND 5.0 — 71 — — — PF			4051A	(Note 2)			_	70	_	_	_	
	Power dissipation	CPD	4052A		GND	5.0	_	71	_	_	_	pF
	- сарасканс <del>с</del>		4053A	(Note 2)			_	67	_	_	_	

Note 1:  $RL = 1 k\Omega$ 

Note 2: CPD is defined as the value of the internal equivalent capacitance of IC which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

ICC (opr) =  $CPD \cdot VCC \cdot fIN + ICC$ 



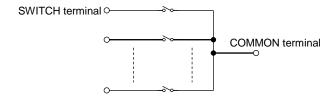
### Analog Switch Characteristics (GND = 0 V, Ta = 25°C) (Note 1)

		Test C	Condition				
Characteristics	Symbol		VEE (V)	Vcc (V)	Тур.	Unit	
		$R_L = 10 \text{ k}\Omega,$	$V_{IN} = 4.0 V_{p-p}$	-2.25	2.25	0.025	
Sine wave distortion (T.H.D)		C <sub>L</sub> = 50 pF	$V_{IN} = 8.0 V_{p-p}$	-4.5	4.5	0.020	%
		f <sub>IN</sub> = 1 kHz	$V_{IN} = 11.0 V_{p-p}$	-6.0	6.0	0.018	
			All (Note 2)			120	
			4051A (Note 3)	-2.25	2.25	45	
			4052A (Note 3)	-2.25	2.25	70	
		Advet for walter and a shipping	4053A (Note 3)			95	
		Adjust f <sub>IN</sub> voltage to obtain 0dBm at V <sub>OS</sub>	All (Note 2)	-4.5	4.5	190	MHz
Frequency response		dB meter reads -3dB $R_L = 50 \Omega$ , $C_L = 10 pF$ $I_{IN} = 1 MHz$ , sine wave	4051A (Note 3)			70	
(switch on)	f <sub>max</sub>		4052A (Note 3)			110	
			4053A (Note 3)			150	
			All (Note 2)		6.0	200	
			4051A (Note 3)	-6.0		85	
			4052A (Note 3)			140	
			4053A (Note 3)			190	
		V <sub>IN</sub> is centered at (V <sub>CC</sub> - V <sub>EE</sub>	-2.25	2.25	-50		
Feed through attenuation		Adjust input for 0dBm		-2.25 -4.5	4.5	-50 -50	dB
(switch off)		$R_L = 600 \Omega, C_L = 50 pF$	- <del>4</del> .5	6.0	-50	uБ	
		f <sub>IN</sub> = 1 MHz, sine wave	-0.0	0.0	-30		
		$R_1 = 600 \Omega$ , $C_1 = 50 pF$		-2.25	2.25	60	
Crosstalk (control input to signal output)		$R_L = 600 \Omega$ , $G_L = 50 \text{ pr}$ $f_{IN} = 1 \text{ MHz}$ , square wave $(t_r = t_f = 6 \text{ ns})$			4.5	140	mV
		IIIV = 1 IVII 12, Square wave (	-6.0	6.0	200		
		Adjust V <sub>IN</sub> to obtain 0dBm at input			2.25	-50	
Crosstalk (between any switches)		$R_L = 600 \Omega$ , $C_L = 50 pF$		-4.5	4.5	-50	dB
		f <sub>IN</sub> = 1 MHz, sine wave		-6.0	6.0	-50	

Note 1: These characteristics are determined by design of devices.

Note 2: Input COMMON terminal, and measured at SWITCH terminal.

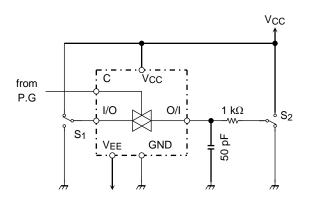
Note 3: Input SWITCH terminal, and measured at COMMON terminal.

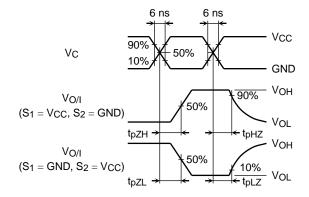




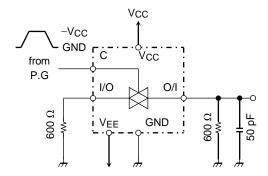
## **Switching Characteristics Test Circuits**

### 1. tpLZ, tpHZ, tpZL, tpZH

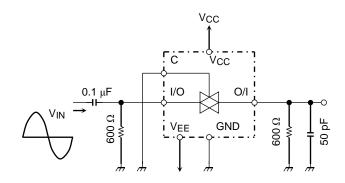




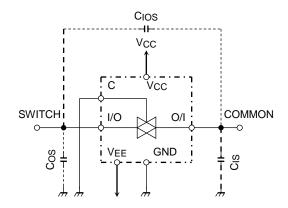
### 2. Cross Talk (control input-switch output) fin = 1 MHz duty = 50% tr = tf = 6 ns



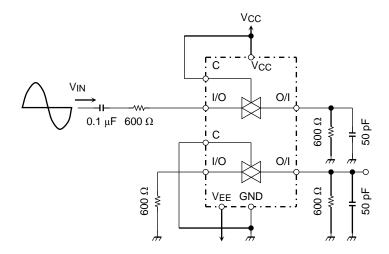
### 3. Feedthrough Attenuation



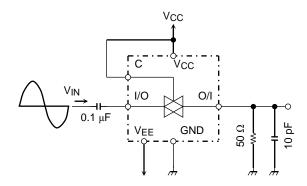
### 4. Cios, Cis, Cos



### 5. Cross Talk (between any two switches)



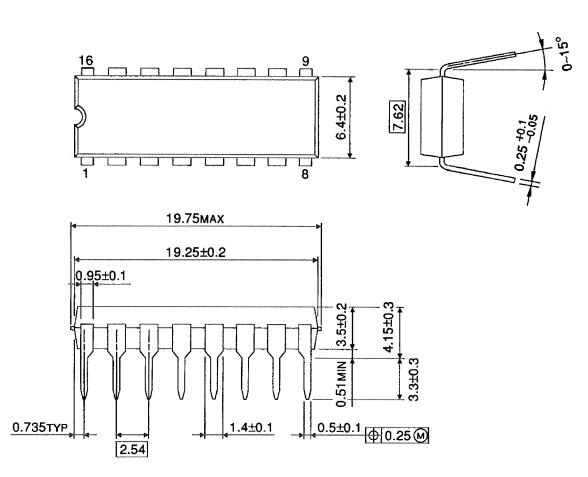
### 6. Frequency Response (switch on)





# **Package Dimensions**

DIP16-P-300-2.54A Unit: mm

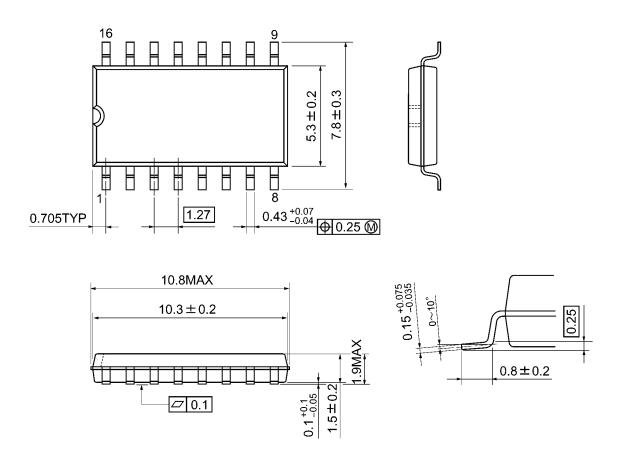


Weight: 1.00 g (typ.)



## **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



12

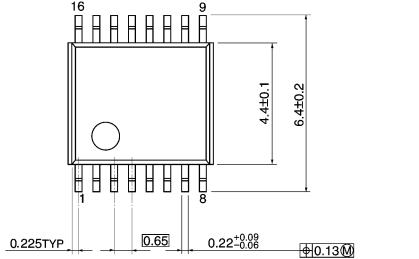
Weight: 0.18 g (typ.)

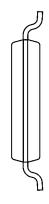


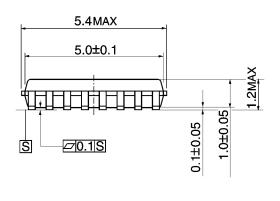
# **Package Dimensions**

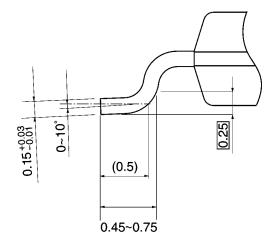
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)



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