Vishay Siliconix



Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static	<u></u>		l .	l	l.	I.
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu A$	60			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.0	2.0	3.0	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}$			50	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175^{\circ}\text{C}$			150	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	15			Α
Drain-Source On-State Resistance ^b	r _{DS(on)}	$V_{GS} = 10 \ V, I_D = 10 A$		0.050	0.065	Ω
		$V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}, T_J = 125^{\circ}\text{C}$			0.12	
		V_{GS} = 10 V, I_{D} = 10 A, T_{J} = 175 $^{\circ}$ C			0.15	
		$V_{GS} = 4.5 \text{ V}, I_D = 5 \text{ A}$		0.065	0.090	
Forward Transconductanceb	9fs	V _{DS} = 15 V, I _D = 10 A		11		S
Dynamic	<u> </u>		- 1	•	ľ	•
Input Capacitance	C _{iss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		524		pF
Output Capacitance	C _{oss}			98		
Reverse Transfer Capacitance	C _{rss}			28		
Total Gate Charge ^c	Qg	$V_{DS} = 30 \text{ V}, \ \ V_{GS} = 10 \text{ V}, \ I_D = 15 \text{ A}$		12	20	nC
Gate-Source Charge ^c	Q _{gs}			2		
Gate-Drain Charge ^c	Q _{gd}			3.5		
Turn-On Delay Time ^c	t _{d(on)}	$V_{DD} = 30 \text{ V, } R_L = 2 \Omega$ $I_D \cong 15 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 2.5 \Omega$		7	20	- ns
Rise Time ^c	t _r			8	25	
Turn-Off Delay Time ^c	t _{d(off)}			15	40	
Fall Time ^c	t _f			7	20	
Source-Drain Diode Ratings ar	nd Characteristi	cs (T _C = 25°C)	<u>.</u>			
Pulsed Current	I _{SM}				30	Α
Diode Forward Voltage	V _{SD}	I _F = 15 A, V _{GS} = 0 V		0.9	1.2	V
Reverse Recovery Time	t _{rr}	I _F = 15 A, di/dt = 100 A/μs		29	60	ns

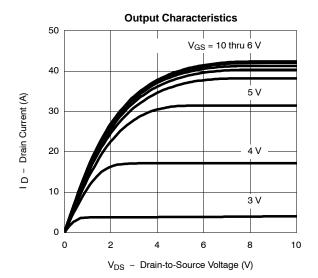
- For design aid only; not subject to production testing. Pulse test; pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$. Independent of operating temperature.

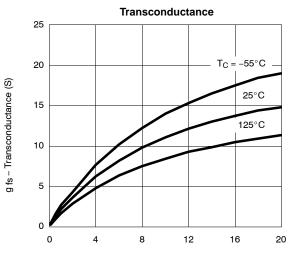
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

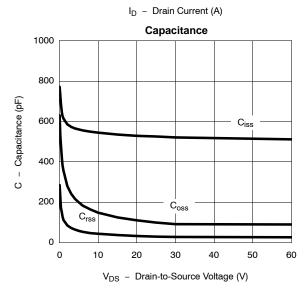


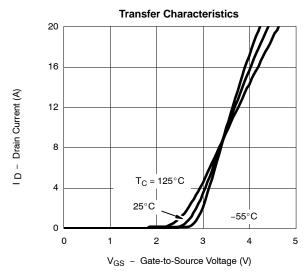


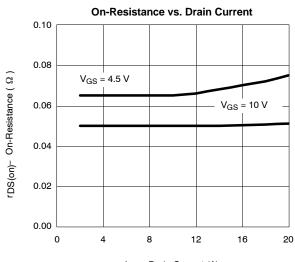
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

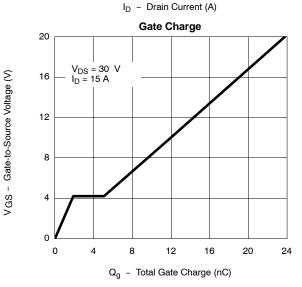








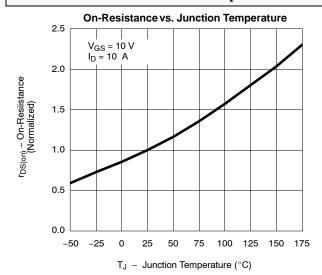


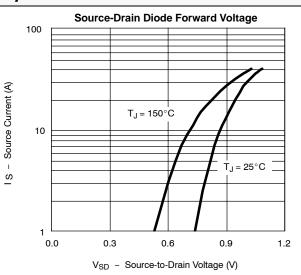


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TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

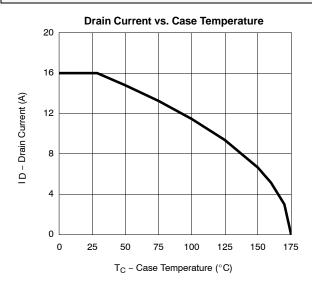


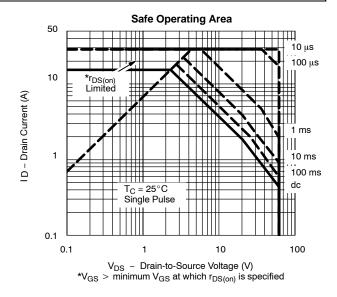


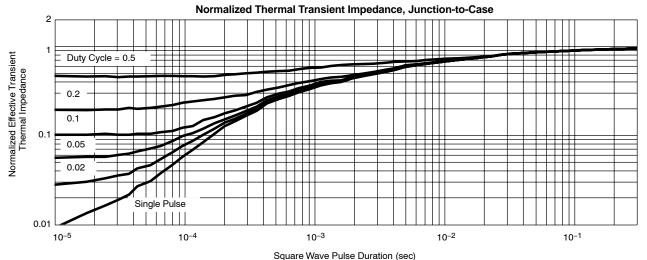


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THERMAL RATINGS







Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?71087.



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