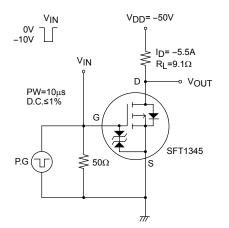
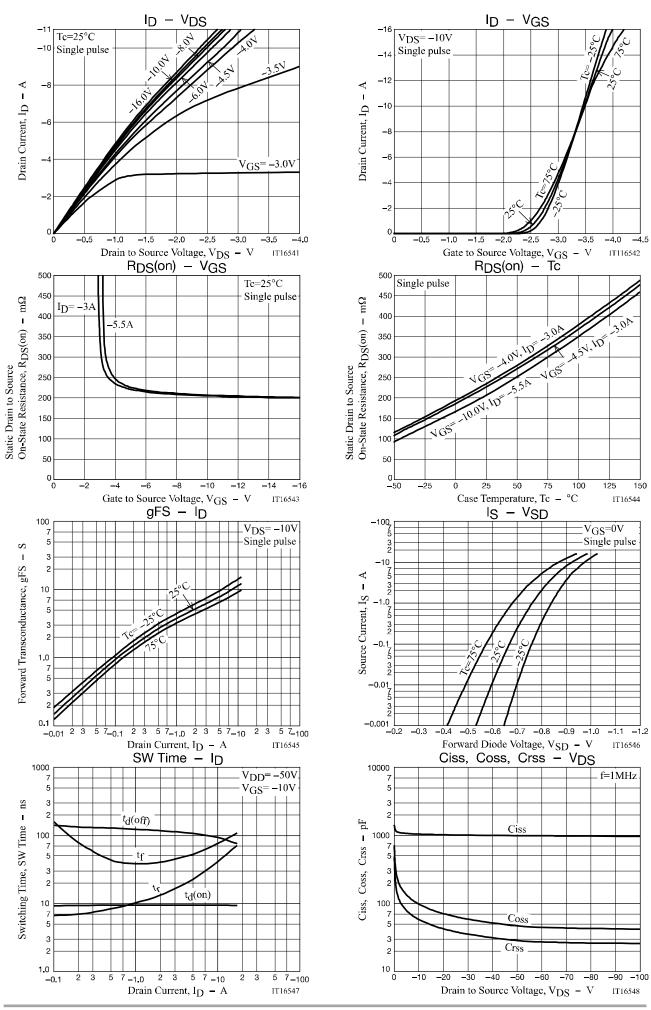
Parameter	Symbol	Conditions	Value			Unit
Faranielei		Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	ID=-1mA, VGS=0V	-100			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V			-1	μA
Gate to Source Leakage Current	IGSS	V <sub>GS</sub> =±16V, V <sub>DS</sub> =0V			±10	μA
Gate Threshold Voltage	VGS(th)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA -1.2			-2.6	V
Forward Transconductance	9FS	V <sub>DS</sub> =-10V, I <sub>D</sub> =-5.5A		8.5		S
Static Drain to Source On-State Resistance	R <sub>DS</sub> (on)1	ID=-5.5A, VGS=-10V		210	275	mΩ
	R <sub>DS</sub> (on)2	ID=-3A, VGS=-4.5V		225	315	mΩ
	R <sub>DS</sub> (on)3	ID=-3A, VGS=-4V		235	330	mΩ
Input Capacitance	Ciss			1020		pF
Output Capacitance	Coss	V <sub>DS</sub> =–20V, f=1MHz		72		pF
Reverse Transfer Capacitance	Crss			43		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			9.5		ns
Rise Time	tr			25		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See specified Test Circuit		105		ns
Fall Time	tf			55		ns
Total Gate Charge	Qg			21		nC
Gate to Source Charge	Qgs	V <sub>DS</sub> =–50V, V <sub>GS</sub> =–10V, I <sub>D</sub> =–11A		3.6		nC
Gate to Drain "Miller" Charge	Qgd			4.5		nC
Forward Diode Voltage	V <sub>SD</sub>	IS=-11A, VGS=0V		-0.93	-1.5	V

## **ELECTRICAL CHARACTERISTICS** at Ta = 25°C (Note 3)

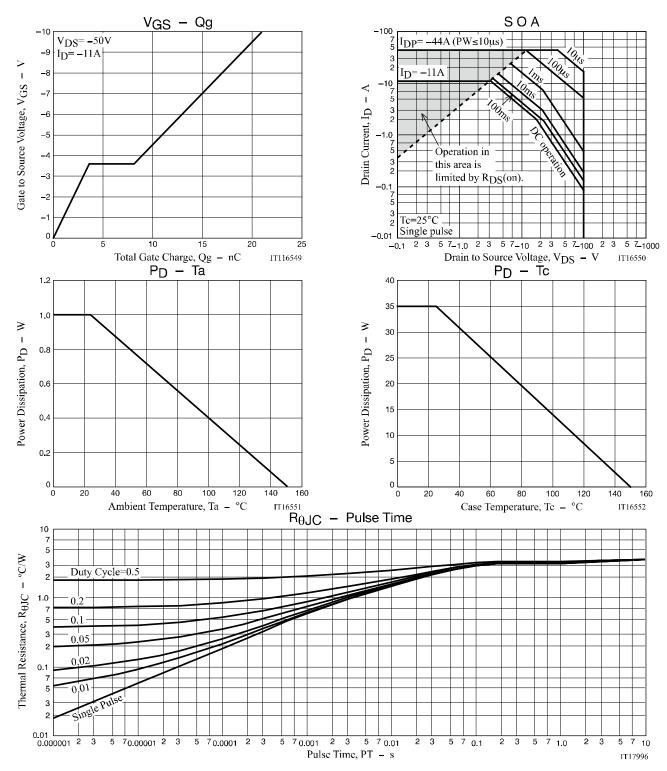
Note 3 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

### Switching Time Test Circuit



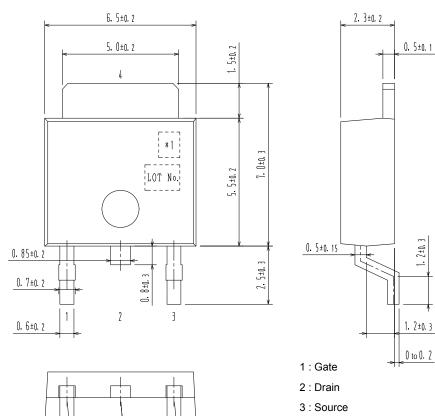


www.onsemi.com 3 SFT1345



#### PACKAGE DIMENSIONS unit : mm

DPAK / TP-FA CASE 369AH ISSUE O



2. 3±0. 2

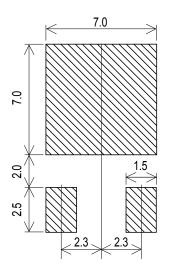
4 : Drain

Pin 2 is idle pin with electrical designation only carried.

\*1:Lot indication

Recommended Soldering Footprint

2. 3±0.2

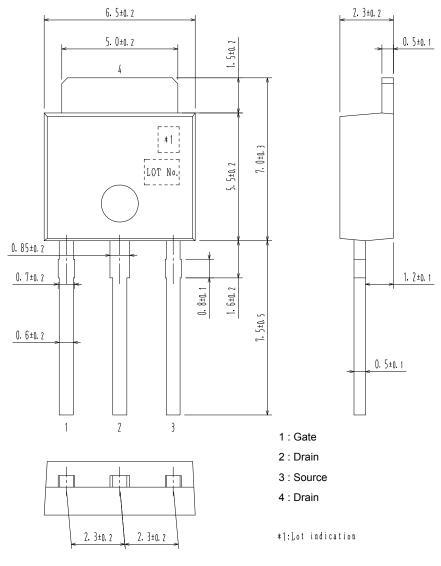


### PACKAGE DIMENSIONS

unit : mm

IPAK / TP CASE 369AJ

ISSUE O



#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping (Qty / Packing)
SFT1345-H	T1345	IPAK / TP (Pb-Free / Halogen Free)	500 / Bag
SFT1345-TL-H	T1345	DPAK / TP-FA (Pb-Free / Halogen Free)	700 / Tape & Reel

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

# Note on usage : Since the SFT1345 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

ON Semiconductor and the ON logo are registered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications in which the failure of the SCILLC product create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmativ