# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
A1	G	gate	1 2	D
A2	S	source	A ( ) ( )	
B1	D	drain		$G \xrightarrow{\Psi} V$
B2	S	source	В	Y TO
			Transparent top view WLCSP4 (OL- PMCM4401VPE)	S 017aaa259

# 6. Ordering information

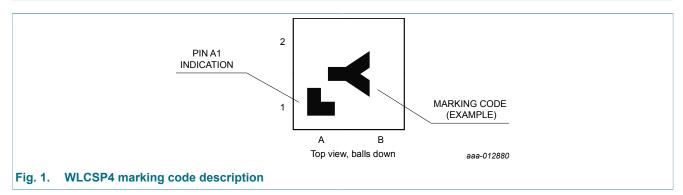
Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMCM4401VPE	WLCSP4	WLCSP4: wafer level chip-size package; 4 bumps (2 x 2)	OL-PMCM4401VPE			

# 7. Marking

Table 4. Marking codes

Type number	Marking code
PMCM4401VPE	Q



PMCM4401VPE

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# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-12	V
$V_{GS}$	gate-source voltage			-8	8	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C; t ≤ 5 s	[1]	-	-4.9	Α
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 25 °C	[1]	-	-3.9	Α
		V <sub>GS</sub> = -4.5 V; T <sub>amb</sub> = 100 °C	[1]	-	-2.5	Α
I <sub>DM</sub>	peak drain current	$T_{amb}$ = 25 °C; single pulse; $t_p \le 10 \mu s$		-	-16	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	400	mW
			[1]	-	1300	mW
		T <sub>sp</sub> = 25 °C		-	12500	mW
Tj	junction temperature			-55	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
Source-drai	n diode	1	ı	1	-1	J
I <sub>S</sub>	source current	T <sub>amb</sub> = 25 °C	[1]	-	-1.2	Α

<sup>[1]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.

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<sup>[2]</sup> Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

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12 V, P-channel Trench MOSFET

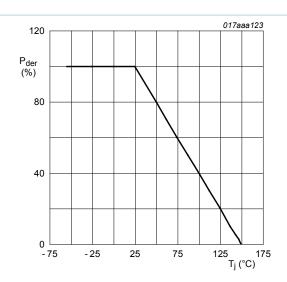


Fig. 2. MOSFET transistor: Normalized total power dissipation as a function of junction temperature

$$P_{\textit{der}} = \frac{P_{\textit{tot}}}{P_{\textit{tot}(25^{\circ}\textit{C})}} \times \textbf{100 \%}$$

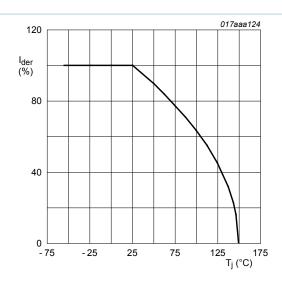


Fig. 3. **MOSFET transistor: Normalized continuous** drain current as a function of junction temperature

$$I_{der} = \frac{I_D}{I_{D(25^{\circ}C)}} \times 100 \%$$

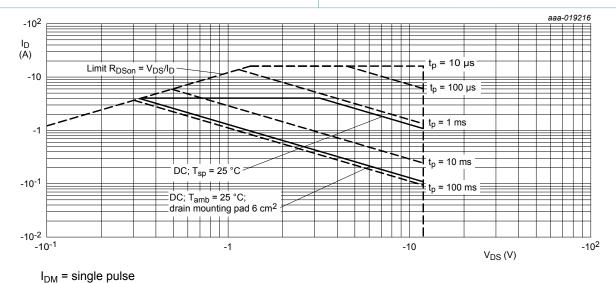


Fig. 4. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drainsource voltage

#### Thermal characteristics 9.

Table 6. Thermal characteristics

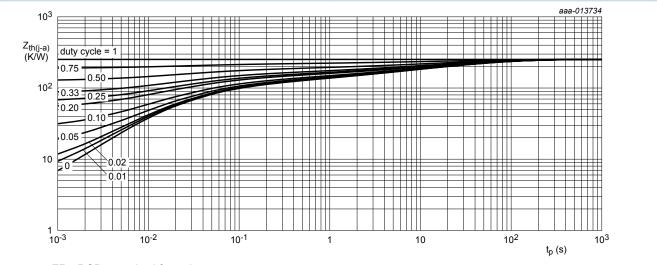
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	250	300	K/W
			[2]	-	70	85	K/W
			<u>[3]</u>	-	85	100	K/W

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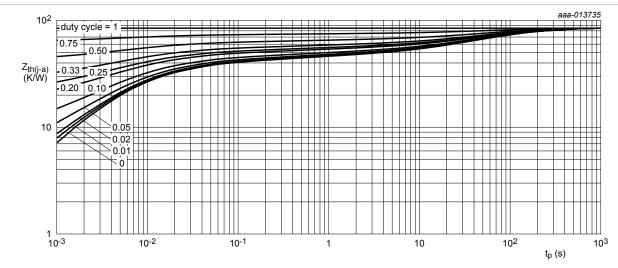
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
		in free air; t ≤ 5 s	[3]	-	50	60	K/W
R <sub>th(j-sp)</sub>	thermal resistance from junction to solder point			-	5	10	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain, 4-layer, 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm<sup>2</sup>.



FR4 PCB, standard footprint

Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for drain 6 cm<sup>2</sup>

Fig. 6. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

# 10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics		'			
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D$ = -250 $\mu$ A; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-12	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = -250 \ \mu A; \ V_{DS} = V_{GS}; \ T_j = 25 \ ^{\circ}C$	-0.4	-0.6	-0.9	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = -12 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μΑ
I <sub>GSS</sub>	gate leakage current	$V_{GS}$ = -8 V; $V_{DS}$ = 0 V; $T_j$ = 25 °C	-	-	-10	μΑ
		V <sub>GS</sub> = 8 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	10	μA
		V <sub>GS</sub> = -4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-1	μA
		V <sub>GS</sub> = 4.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μA
		V <sub>GS</sub> = -2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-200	nA
		V <sub>GS</sub> = 2.5 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	200	nA
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = -4.5 \text{ V}; I_D = -3 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	55	65	mΩ
		$V_{GS}$ = -4.5 V; $I_D$ = -3 A; $T_j$ = 150 °C	-	73	86	mΩ
		$V_{GS}$ = -2.5 V; $I_D$ = -2 A; $T_j$ = 25 °C	-	77	96	mΩ
		$V_{GS}$ = -1.8 V; $I_D$ = -0.1 A; $T_j$ = 25 °C	-	110	160	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS} = -6 \text{ V}; I_D = -3 \text{ A}; T_j = 25 \text{ °C}$	-	13.6	-	S
$R_G$	gate resistance	f = 1 MHz	-	5.5	-	Ω
Dynamic ch	naracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS}$ = -6 V; $I_{D}$ = -3 A; $V_{GS}$ = -4.5 V;	-	6.8	10	nC
$Q_{GS}$	gate-source charge	T <sub>j</sub> = 25 °C	-	0.8	-	nC
$Q_{GD}$	gate-drain charge		-	2.2	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = -6 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	415	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	195	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	165	-	pF
t <sub>d(on)</sub>	turn-on delay time	$V_{DS}$ = -6 V; $I_{D}$ = -3.5 A; $V_{GS}$ = -4.5 V;	-	4.8	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega$ ; $T_j = 25 °C$	-	24.7	-	ns
t <sub>d(off)</sub>	turn-off delay time		-	25.1	-	ns
t <sub>f</sub>	fall time		-	16.5	-	ns
Source-drai	in diode		I	1	1	
V <sub>SD</sub>	source-drain voltage	$I_S$ = -1.2 A; $V_{GS}$ = 0 V; $T_j$ = 25 °C	-	-0.8	-1.2	V
		The state of the s		1		

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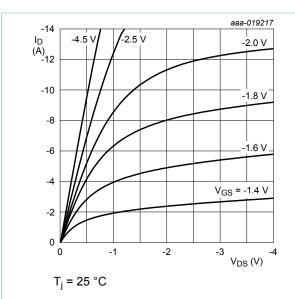
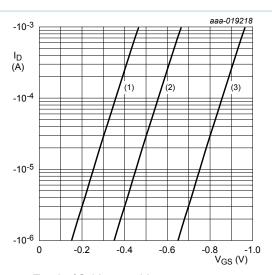


Fig. 7. Output characteristics: drain current as a function of drain-source voltage; typical values



 $T_i$  = 25 °C;  $V_{DS}$  = -5 V

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 8. Sub-threshold drain current as a function of gate-source voltage

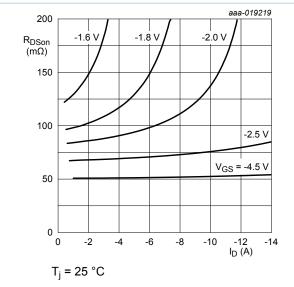


Fig. 9. Drain-source on-state resistance as a function of drain current; typical values

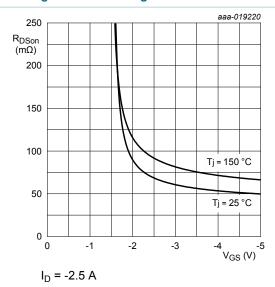


Fig. 10. Drain-source on-state resistance as a function of gate-source voltage; typical values

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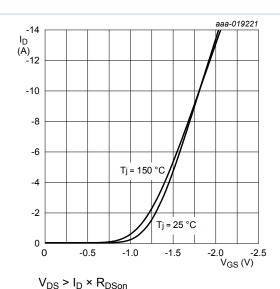


Fig. 11. Transfer characteristics: drain current as a function of gate-source voltage; typical values

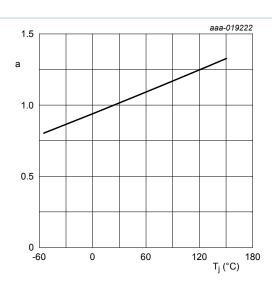
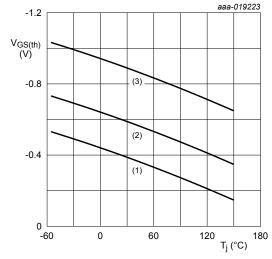


Fig. 12. Normalized drain-source on-state resistance as a function of junction temperature; typical values

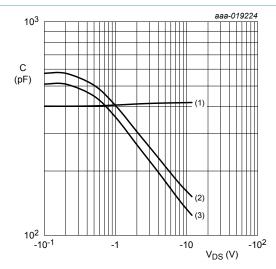
$$a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$$



 $I_D = -0.25 \text{ mA}; V_{DS} = V_{GS}$ 

- (1) minimum values
- (2) typical values
- (3) maximum values

Fig. 13. Gate-source threshold voltage as a function of junction temperature



 $f = 1 MHz; V_{GS} = 0 V$ 

- (1) C<sub>iss</sub>
- (2) C<sub>oss</sub>
- (3) C<sub>rss</sub>

Fig. 14. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

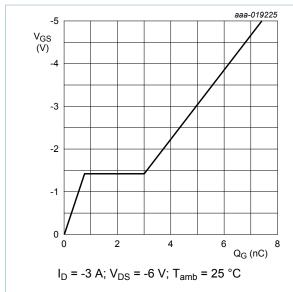


Fig. 15. Gate-source voltage as a function of gate charge; typical values

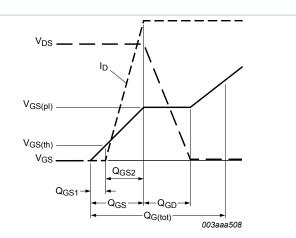


Fig. 16. MOSFET transistor: Gate charge waveform definitions

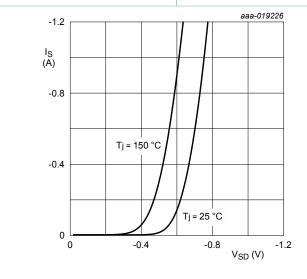
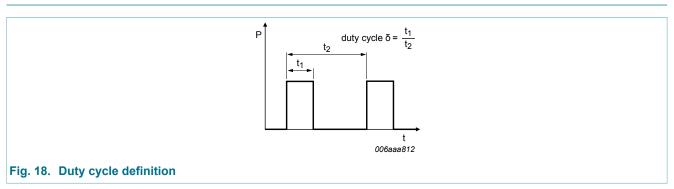


Fig. 17. Source current as a function of source-drain voltage; typical values

### 11. Test information

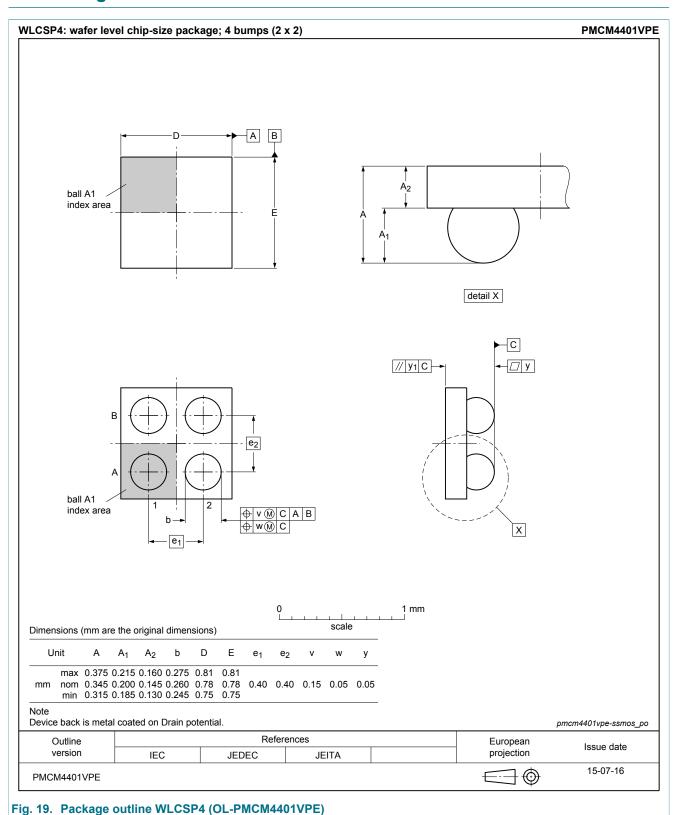
 $V_{GS} = 0 V$ 



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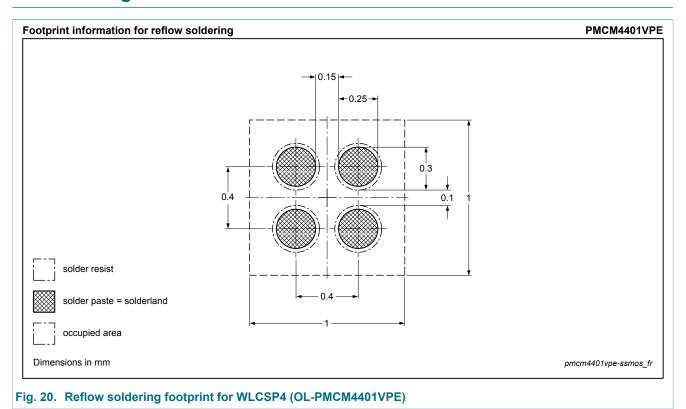
# 12. Package outline



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# 13. Soldering



# 14. Revision history

### Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PMCM4401VPE v.1	20150729	Product data sheet	-	-

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### 15. Legal information

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
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Product [short] data sheet	Production	This document contains the product specification.

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