

# MAX9644/MAX9645/ MAX9646

## nanoPower Comparators with Precision Reference in 4-Bump UCSP

### Absolute Maximum Ratings

V<sub>CC</sub>, REF, IN to GND.....-0.3V to +6V  
 OUT to GND (MAX9644/MAX9645).....-0.3V to +6V  
 OUT to GND (MAX9646 only).....-0.3V to + (V<sub>CC</sub> + 0.3V)  
 Output Short-Circuit Current Duration..... 10s  
 Input Current into Any Terminal..... ±20mA  
 Continuous Power Dissipation  
 4-Bump UCSP (derate 3.0mW/°C above +70°C).....238mW

5-Pin SOT23 (derate 3.9mW/°C above +70°C).....312mW  
 Operating Temperature Range.....-40°C to +85°C  
 Junction Temperature.....+150°C  
 Storage Temperature Range.....-65°C to +150°C  
 Bump Temperature (soldering) Reflow.....+235°C  
 Lead Temperature (soldering, 10s).....+300°C  
 Soldering Temperature (reflow).....+260°C

Stresses beyond those listed under ?Absolute Maximum Ratings? may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### Electrical Characteristics

(V<sub>CC</sub> = 3.3V, R<sub>PULLUP</sub> = 10kΩ to V<sub>PULLUP</sub> = 3.3V for MAX9644/MAX9645, T<sub>A</sub> = -40°C to +85°C. Typical values at T<sub>A</sub> = +25°C, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
<b>DC CHARACTERISTICS</b>						
Input Voltage Range	V <sub>IN</sub>	Guaranteed by I <sub>IN</sub> test	-0.3		+5.5	V
Input Bias Current	I <sub>IN</sub>	V <sub>IN</sub> = 0.2V to 5.5V (Note 2)		0.06	15	nA
Input Leakage Current	I <sub>IN_SHDN</sub>	V <sub>CC</sub> = 0V, V <sub>IN</sub> = 5.5V (Note 2)		< 0.1	15	nA
Output Voltage Low	V <sub>OL</sub>	I <sub>SINK</sub> = 50μA, V <sub>CC</sub> = 1.0V		0.03	0.2	V
		I <sub>SINK</sub> = 200μA, V <sub>CC</sub> = 1.2V		0.08	0.20	
		I <sub>SINK</sub> = 500μA, V <sub>CC</sub> = 1.8V		0.13	0.23	
		I <sub>SINK</sub> = 0.75mA, V <sub>CC</sub> = 3.3V		0.14	0.3	
		I <sub>SINK</sub> = 1.2mA, V <sub>CC</sub> = 5.5V		0.19	0.5	
Output Voltage High (MAX9646 Only)	V <sub>OH</sub>	I <sub>SOURCE</sub> = 15μA, V <sub>CC</sub> = 1.0V		V <sub>CC</sub> - 0.08V	V <sub>CC</sub> - 0.2V	V
		I <sub>SOURCE</sub> = 40μA, V <sub>CC</sub> = 1.2V		V <sub>CC</sub> - 0.08V	V <sub>CC</sub> - 0.20V	
		I <sub>SOURCE</sub> = 180μA, V <sub>CC</sub> = 1.8V		V <sub>CC</sub> - 0.15V	V <sub>CC</sub> - 0.23V	
		I <sub>SOURCE</sub> = 0.3mA, V <sub>CC</sub> = 3.3V		V <sub>CC</sub> - 0.13V	V <sub>CC</sub> - 0.3V	
		I <sub>SOURCE</sub> = 0.75mA, V <sub>CC</sub> = 5.5V		V <sub>CC</sub> - 0.24V	V <sub>CC</sub> - 0.5V	
Output Leakage Current (MAX9644/MAX9645 Only)	I <sub>OUT_LEAKAGE</sub>	OUT = high, V <sub>PULLUP</sub> = 5.5V (Note 2)		< 0.1	15	nA
<b>AC CHARACTERISTICS</b>						
Propagation Delay	t <sub>PD</sub>	V <sub>OVERDRIVE</sub> = ±100mV (Note 3)		15		μs
Fall Time	t <sub>F</sub>	C <sub>L</sub> = 10pF		14		ns
Rise Time	t <sub>R</sub>	C <sub>L</sub> = 10pF, MAX9646 only		30		ns
<b>REFERENCE VOLTAGE</b>						
Input Threshold (Note 4)	V <sub>REF</sub>	MAX964_EBS+		200		mV
		MAX964_EUK+		199		

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Electrical Characteristics (continued)

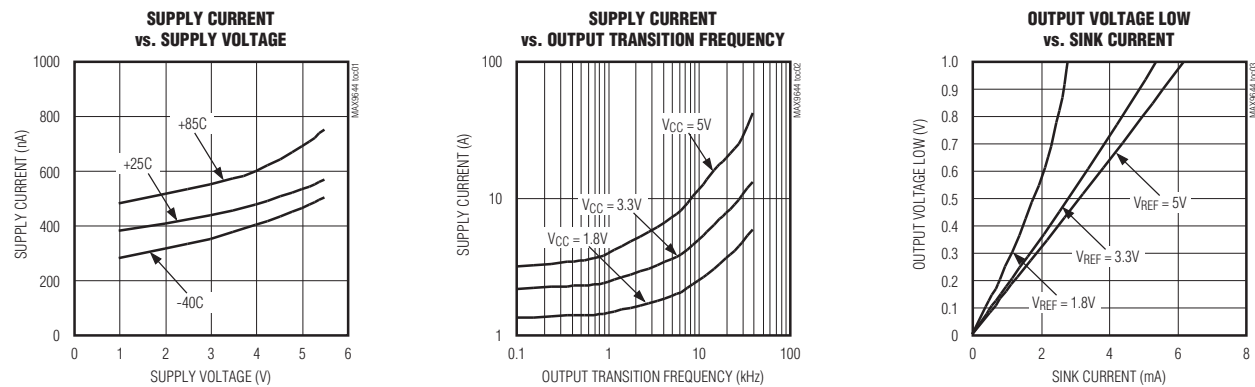
(V<sub>CC</sub> = 3.3V, R<sub>PULLUP</sub> = 10kΩ to V<sub>PULLUP</sub> = 3.3V for MAX9644/MAX9645, T<sub>A</sub> = -40°C to +85°C. Typical values at T<sub>A</sub> = +25°C, unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Threshold Error (Note 4)	Delta-V <sub>REF</sub>	T <sub>A</sub> = +25°C	-1		+1	%
		T <sub>A</sub> = -40°C to +85°C	-3.5		+3.5	
Input Threshold Hysteresis	V <sub>HYS</sub>	T <sub>A</sub> = -40°C to +85°C (Note 5)		±0.9		mV
REF Tempco	V <sub>REF_TEMP</sub> CO	(Note 6)		6		µV/°C
Power-Supply Rejection Ratio	PSRR	V <sub>CC</sub> = 1.0V to 5.5V	40	53		dB
POWER SUPPLY						
Supply Voltage	V <sub>CC</sub>	Guaranteed by V <sub>OL</sub> /V <sub>OH</sub> tests	1.0		5.5	V
Supply Current	I <sub>CC</sub>	V <sub>CC</sub> = 1.0V		0.4	0.7	µA
		V <sub>CC</sub> = 5.5V		0.6	1.1	
Power-Up Time	t <sub>ON</sub>			3		ms

- Note 1:** All devices are 100% production tested at T<sub>A</sub> = +25°C. Temperature limits are guaranteed by design.
- Note 2:** Too small to be measured in an ATE test environment. Only gross test to catch failures is implemented.
- Note 3:** Overdrive is defined as the voltage above or below the switching points.
- Note 4:** Guaranteed by ATE and/or bench characterization over temperature. V<sub>REF</sub> is the average of the trip points.
- Note 5:** Hysteresis is half the input voltage difference between the two switching points.
- Note 6:** Includes reference error along with comparator offset voltage error.

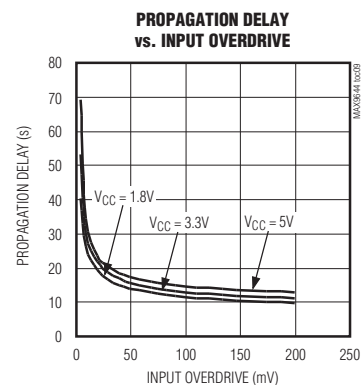
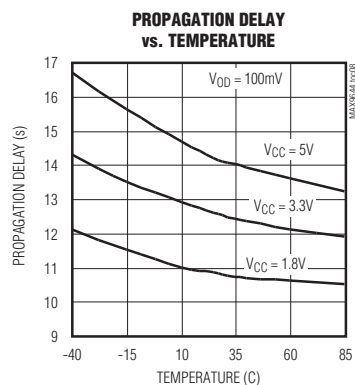
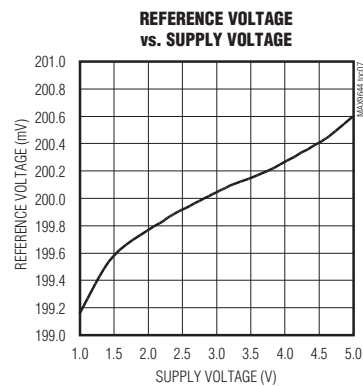
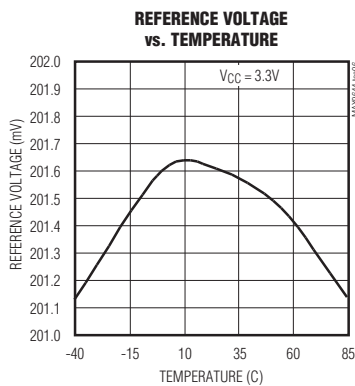
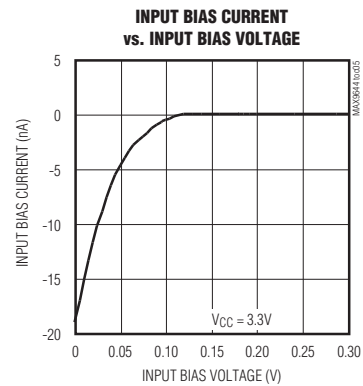
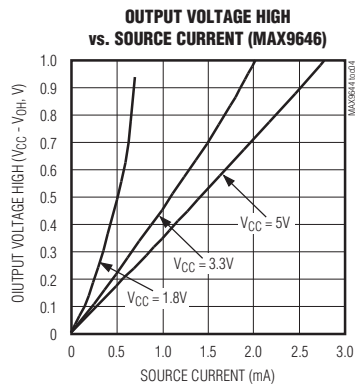
Typical Operating Characteristics

(V<sub>CC</sub> = 3.3V, V<sub>REF</sub> = 1.8V, R<sub>PULLUP</sub> = 10kΩ to V<sub>PULLUP</sub> = 3.3V for MAX9644/MAX9645, V<sub>GND</sub> = 0V, T<sub>A</sub> = +25°C, unless otherwise noted.)



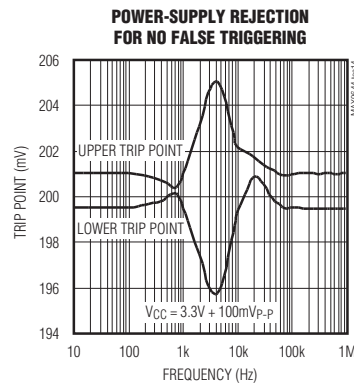
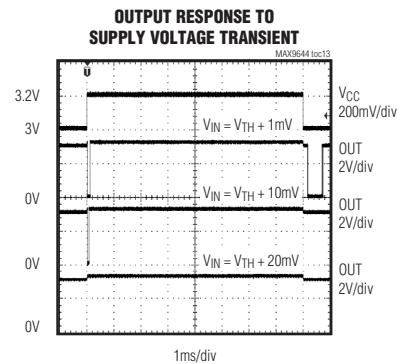
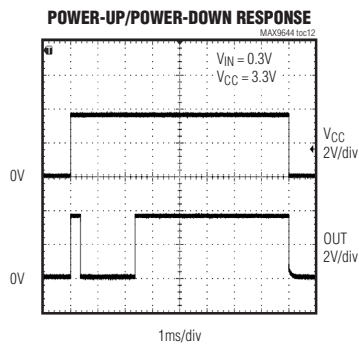
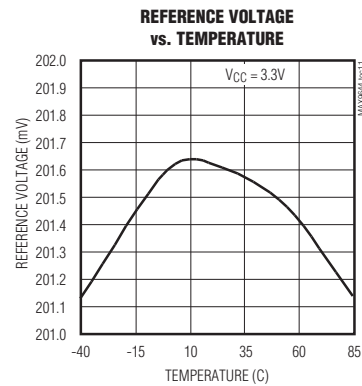
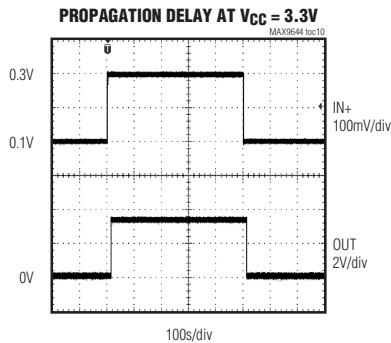
## Typical Operating Characteristics (continued)

( $V_{CC} = 3.3V$ ,  $V_{REF} = 1.8V$ ,  $R_{PULLUP} = 10k\Omega$  to  $V_{PULLUP} = 3.3V$  for MAX9644/MAX9645,  $V_{GND} = 0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



## Typical Operating Characteristics (continued)

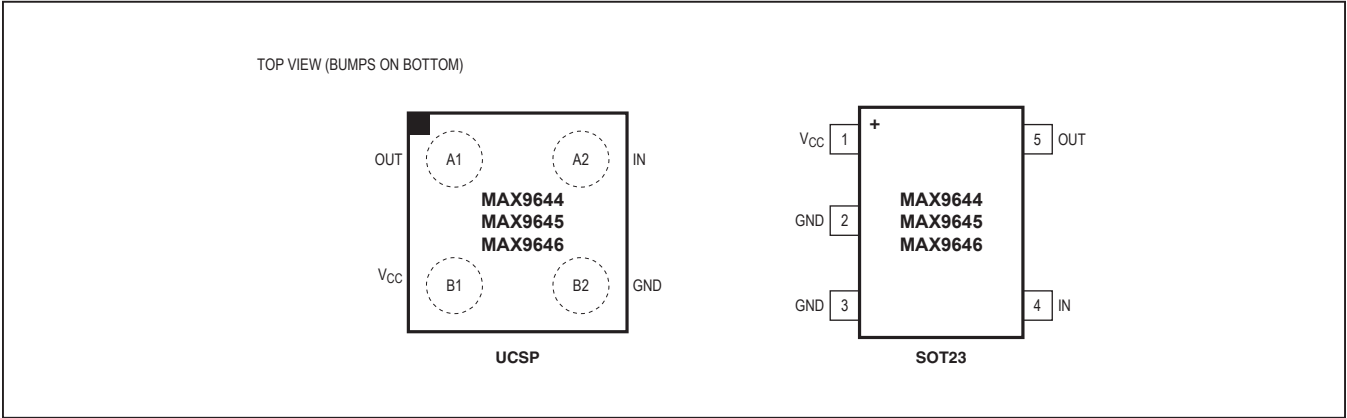
( $V_{CC} = 3.3V$ ,  $V_{REF} = 1.8V$ ,  $R_{PULLUP} = 10k\Omega$  to  $V_{PULLUP} = 3.3V$  for MAX9644/MAX9645,  $V_{GND} = 0V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



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Pin Configuration



Pin Description

PIN		NAME	FUNCTION
UCSP	SOT23		
A1	5	OUT	Comparator Output. The MAX9644/MAX9645 have open-drain outputs. The MAX9646 has a push-pull output.
A2	4	IN	Comparator Input. The MAX9644/MAX9646 have noninverting inputs. The MAX9645 has inverting inputs.
B1	1	V <sub>CC</sub>	Power-Supply Voltage. Bypass to ground with a 0.1μF bypass capacitor.
B2	2, 3	GND	Ground

Detailed Description

The MAX9644/MAX9645/MAX9646 are extremely small comparators ideal for compact, low-current, and low-voltage applications.

The ICs consume only 400nA (typ). The low-voltage operating capability of the operating current makes these devices extremely attractive to long-life battery-operated devices—these applications can now use a single digital power-supply rail to power the new generation of microcontrollers (which can be down to 0.9V). All parts are available in a tiny 4-bump UCSP, which is only 0.6mm tall and occupies a 1mm x 1mm footprint and a 5-pin SOT23.

Input Stage Circuitry

Noninverting inputs are available on the MAX9644/MAX9646 and inverting inputs are available on the MAX9645.

The MAX9644/MAX9645/MAX9646 incorporate an innovative input stage architecture that allows their input voltage to exceed  $V_{CC}$  by several volts (limited only by the *Absolute Maximum Ratings*). This is unlike traditional comparators that have an input ESD diode clamp between the input and  $V_{CC}$ , limiting this maximum overvoltage to about 0.3V. The ICs architecture maintains a high input impedance to input signals even when the device power-supply voltage is completely turned off ( $V_{CC}$  or REF taken to 0V). This greatly benefits flexible power-saving schemes to be easily implemented in advanced battery-operated devices. On-chip filtering provides immunity from any RF noise being picked up by input traces. These devices feature an internal temperature-compensated, low-power 0.2V reference voltage

Output Stage Structure

The MAX9644/MAX9645 have open-drain outputs that allow them to interface to logic circuitry running from supply voltages other than the one supplied to the part. These devices require an external pullup resistor or current source for proper operation. Many microcontroller digital inputs ports can be readily programmed to include these.

The MAX9646 has a push-pull output stage that can both sink and source current, eliminating the need for an external pullup resistor. In this case, the MAX9646 uses the microcontroller's power supply as  $V_{CC}$

Applications Information

Bypassing REF/ $V_{CC}$

Place a 0.1μF capacitor between REF or  $V_{CC}$  and GND as close as possible to the device. During a switching event, all comparators draw a current spike from their power-supply rails. This current spike is minimized by the use of an internal break-before-make design.

Hysteresis Operation

The ICs feature internal hysteresis for noise immunity and glitch-free operation. If additional hysteresis is needed, an external positive feedback network can be easily implemented on the MAX9644 and MAX9646 noninverting input devices. Additional external hysteresis is not possible on the MAX9645 because the noninverting input of the comparator is not externally accessible.

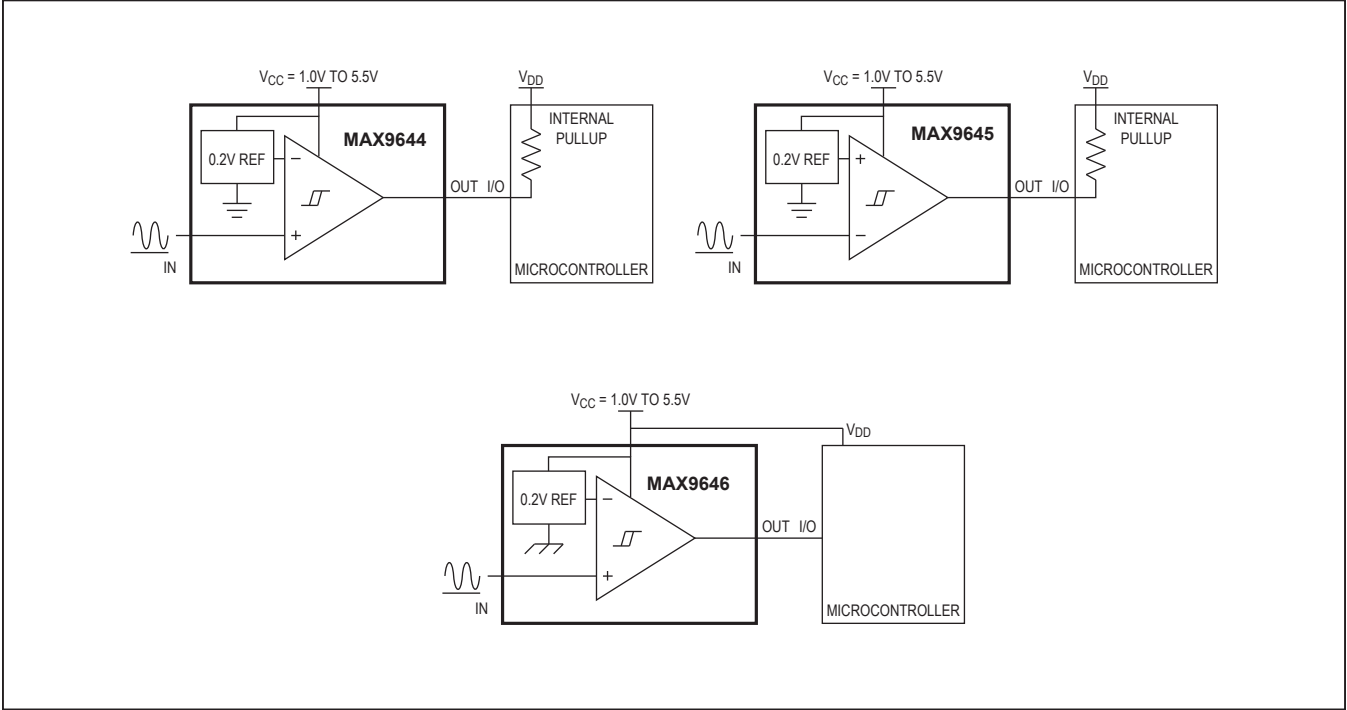
Table 1. How Devices Behave Under Various Input Voltage Conditions

PART	INPUT VOLTAGE CONDITIONS	ACTION AT OUTPUT
MAX9644	$V_{IN} > 0.2V$	External pullup resistor pulls output high.
	$V_{IN} < 0.2V$	Output asserts low.
MAX9645	$V_{IN} > 0.2V$	Output asserts low.
	$V_{IN} < 0.2V$	External pullup resistor pulls output high.
MAX9646	$V_{IN} > 0.2V$	Output asserts high.
	$V_{IN} < 0.2V$	Output asserts low.

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Typical Operating Circuits



Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns (footprints), go to [www.maximintegrated.com/packages](http://www.maximintegrated.com/packages). Note that a “+”, “#”, or “-” in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.	LAND PATTERN NO.
4 UCSP	B4+1	<a href="#">21-0789</a>	Refer to <a href="#">Application Note 1891</a>
5 SOT23	U5+2	<a href="#">21-0057</a>	<a href="#">90-0174</a>

## Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	3/14	Initial release	—
1	10/11	Updated <i>Features</i> section	1
2	3/17	Updated title to include “nanoPower”	1–11
3	11/19	Updated <i>Absolute Maximum Ratings</i> and <i>Package Information</i>	2, 8

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