

ABSOLUTE MAXIMUM RATINGS

(Note 1)

Supply Voltage (V_{CC})	6V
V^+	13.2V
V^-	-13.2V
Input Voltage	
Driver	V^- to V^+
Receiver	-30V to 30V
Output Voltage	
Driver	($V^+ - 30V$) to ($V^- + 30V$)
Receiver	-0.3V to ($V_{CC} + 0.3V$)
Short-Circuit Duration	
V^+	30 sec
V^-	30 sec
Driver Output	Indefinite
Receiver Output	Indefinite
Operating Temperature Range	
LT1381C	0°C to 70°C
LT1381I	-40°C to 85°C
Storage Temperature Range	-65°C to 150°C
Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION

TOP VIEW		ORDER PART NUMBER
		LT1381CN LT1381CS LT1381IS
N PACKAGE 16-LEAD PLASTIC DIP	S PACKAGE 16-LEAD PLASTIC SOIC	
$T_{JMAX} = 125^{\circ}\text{C}$, $\theta_{JA} = 90^{\circ}\text{C/W}$, $\theta_{JC} = 46^{\circ}\text{C/W}$ (N) $T_{JMAX} = 125^{\circ}\text{C}$, $\theta_{JA} = 110^{\circ}\text{C/W}$, $\theta_{JC} = 34^{\circ}\text{C/W}$ (S)		

Consult factory for Military grade parts.

ELECTRICAL CHARACTERISTICS (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Power Supply Generator					
V^+ Output			7.9		V
V^- Output			-7.0		V
Supply Current (V_{CC})	(Note 3), $T_A = 25^{\circ}\text{C}$		8	14 16	mA mA
Supply Rise Time	$C1 = C2 = C3 = C4 = 0.1\mu\text{F}$		0.2		ms
Oscillator Frequency			130		kHz
Driver					
Output Voltage Swing	Load = 3k to GND	Positive Negative	5.0 -6.3	7.5 -5.0	V V
Logic Input Voltage Level	Input Low Level ($V_{OUT} = \text{High}$) Input High Level ($V_{OUT} = \text{Low}$)		2.0 1.4	1.4 0.8	V V
Logic Input Current	$0.8V \leq V_{IN} \leq 2.0V$		5	20	μA
Output Short-Circuit Current	$V_{OUT} = 0V$		9	17	mA
Output Leakage Current	Power Off $V_{OUT} = \pm 15V$		10	100	μA
Data Rate	$R_L = 3k$, $C_L = 2500\text{pF}$ $R_L = 3k$, $C_L = 1000\text{pF}$		120 250		kBaud kBaud
Slew Rate	$R_L = 3k$, $C_L = 51\text{pF}$ $R_L = 3k$, $C_L = 2500\text{pF}$		4 6	15 30	V/ μs V/ μs
Propagation Delay	Output Transition t_{HL} High to Low (Note 4) Output Transition t_{LH} Low to High		0.6 0.5	1.3 1.3	μs μs

ELECTRICAL CHARACTERISTICS (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Receiver					
Input Voltage Thresholds	Input Low Threshold ($V_{OUT} = \text{High}$) Input High Threshold ($V_{OUT} = \text{Low}$)	0.8	1.3 1.7	2.4	V V
Hysteresis		● 0.1	0.4	1.0	V
Input Resistance	(Note 6)	3	5	7	k Ω
Output Voltage	Output Low, $I_{OUT} = -1.6\text{mA}$ Output High, $I_{OUT} = 160\mu\text{A}$ ($V_{CC} = 5\text{V}$)	● 3.5	0.2 4.2	0.4	V V
Output Short-Circuit Current	Sinking Current, $V_{OUT} = V_{CC}$ Sourcing Current, $V_{OUT} = 0\text{V}$	10	-20 20	-10	mA mA
Propagation Delay	Output Transition t_{HL} High-to-Low (Note 5) Output Transition t_{LH} Low-to-High		250 350	600 600	ns ns

The ● denotes specifications which apply over the full operating temperature range.

Note 1: Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

Note 2: Testing done at $V_{CC} = 5\text{V}$, unless otherwise specified.

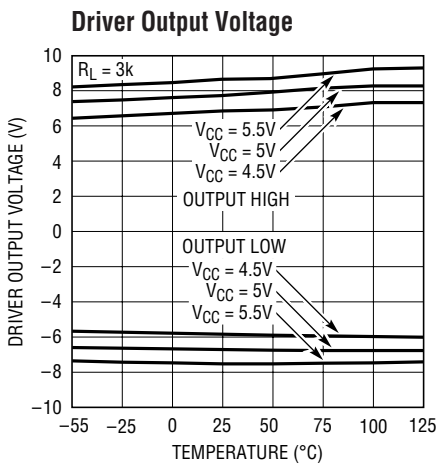
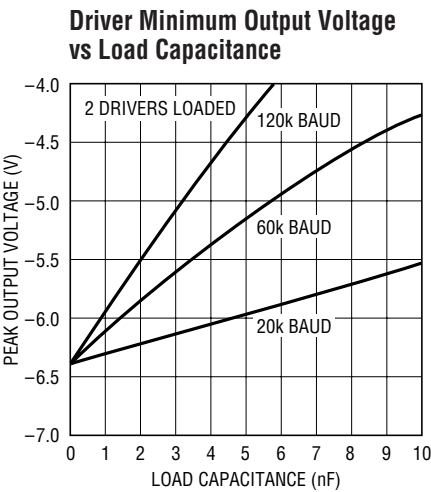
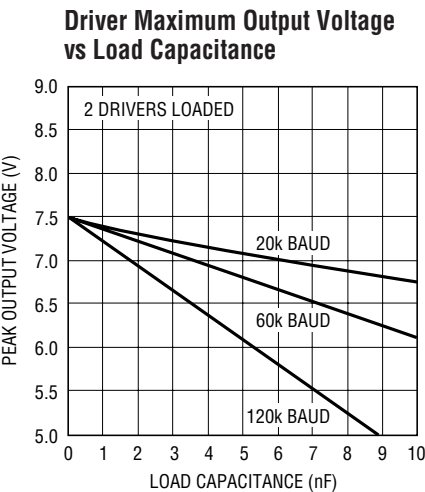
Note 3: Supply current is measured as the average over several charge pump cycles. $C^+ = C^- = C1 = C2 = 0.1\mu\text{F}$. All outputs are open, with all driver inputs tied high.

Note 4: For driver delay measurements, $R_L = 3\text{k}$ and $C_L = 51\text{pF}$. Trigger points are set between the driver's input logic threshold and the output transition to the zero crossing ($t_{HL} = 1.4\text{V}$ to 0V and $t_{LH} = 1.4\text{V}$ to 0V).

Note 5: For receiver delay measurements, $C_L = 51\text{pF}$. Trigger points are set between the receiver's input logic threshold and the output transition to standard TTL/CMOS logic threshold ($t_{HL} = 1.3\text{V}$ to 2.4V and $t_{LH} = 1.7\text{V}$ to 0.8V).

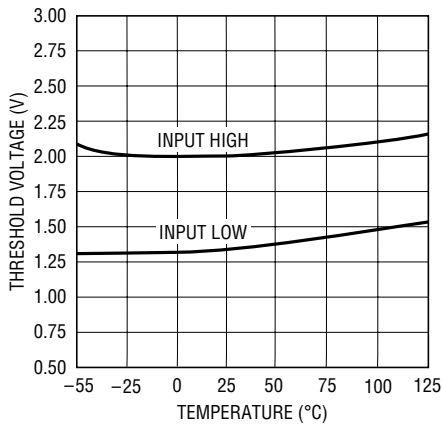
Note 6: Tested at $V_{IN} = \pm 10\text{V}$.

TYPICAL PERFORMANCE CHARACTERISTICS

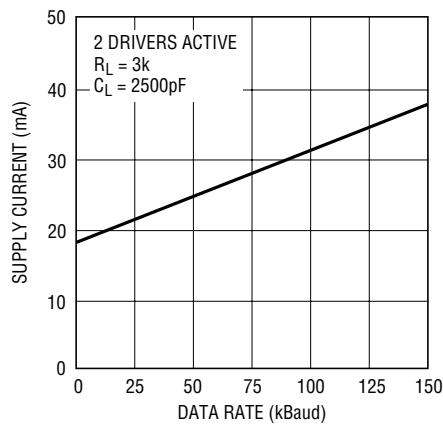


TYPICAL PERFORMANCE CHARACTERISTICS

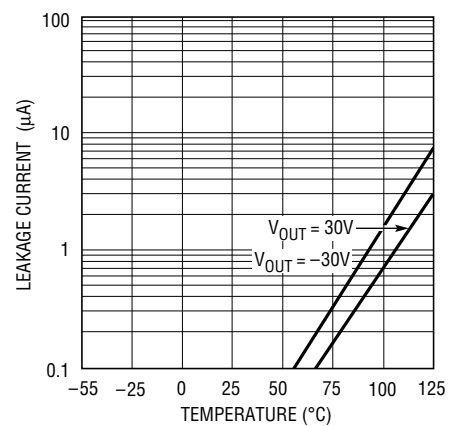
Receiver Input Threshold



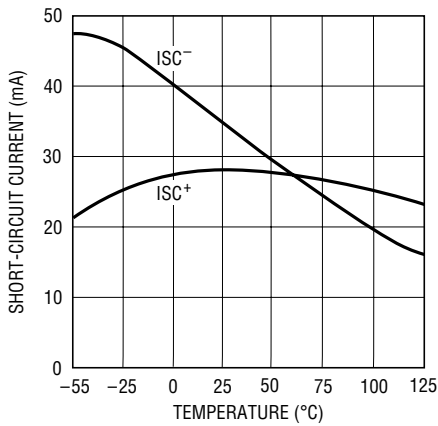
Supply Current vs Data Rate



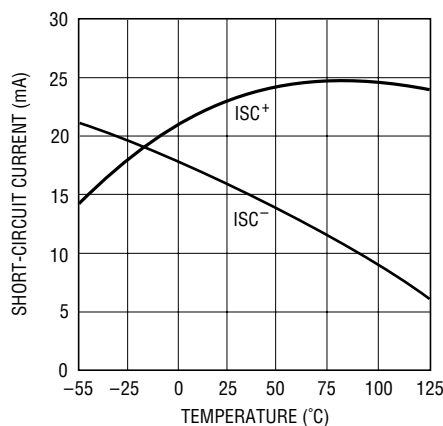
Driver Leakage in Shutdown



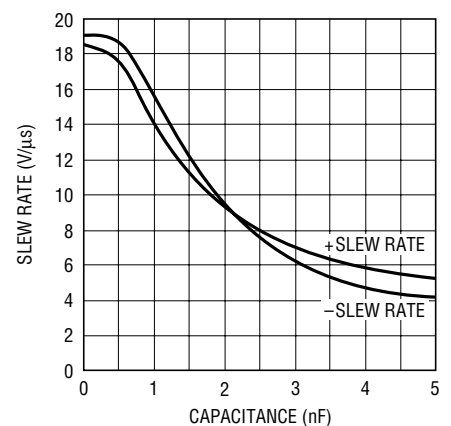
Receiver Short-Circuit Current



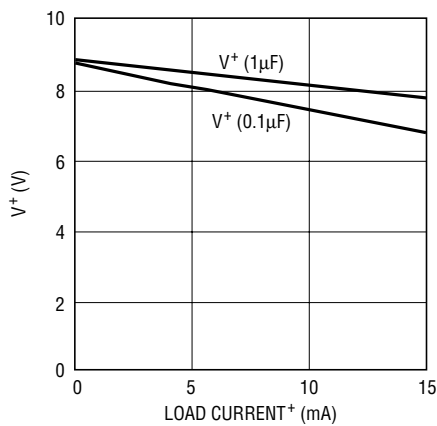
Driver Short-Circuit Current



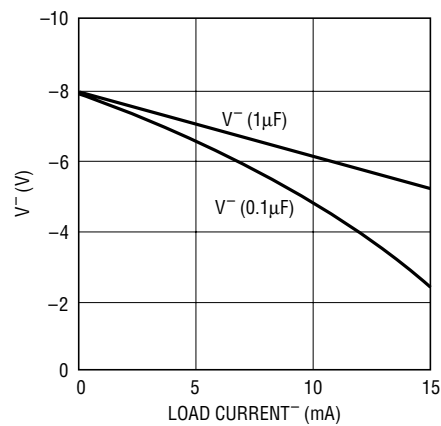
Slew Rate vs Load Capacitance



V+ Compliance Curve



V- Compliance Curve



PIN FUNCTIONS

C1⁺, C1⁻, C2⁺, C2⁻ (Pins 1, 3, 4, 5): Commutating Capacitor Inputs. These pins require two external capacitors $C \geq 0.1\mu\text{F}$: one from C1⁺ to C1⁻ and another from C2⁺ to C2⁻. C1 may be deleted if a separate 12V supply is available and connected to pin C1⁺.

V⁺ (Pin 2): Positive Supply Output (RS232 Drivers). $V^+ \approx 2V_{CC} - 2.1\text{V}$. This pin requires an external charge storage capacitor $C \geq 0.1\mu\text{F}$, tied to ground or V_{CC} . Larger value capacitors may be used to reduce supply ripple. With multiple transceivers, the V⁺ and V⁻ pins may be paralleled into common capacitors.

V⁻ (Pin 6): Negative Supply Output (RS232 Drivers). $V^- \approx -(2V_{CC} - 3\text{V})$. This pin requires an external charge storage capacitor $C \geq 0.1\mu\text{F}$. Larger value capacitors may be used to reduce supply ripple. With multiple transceivers, the V⁺ and V⁻ pins may be paralleled into common capacitors.

TR2 OUT, TR1 OUT (Pin 7, 14): Driver Outputs at RS232 Voltage Levels. Driver output swing meets RS232 levels for loads up to 3k. Slew rates are controlled for lightly loaded lines. Output current capability is sufficient for load conditions up to 2500pF. Outputs are in a high impedance state when $V_{CC} = 0\text{V}$. Outputs are fully short-circuit protected from $V^- + 25\text{V}$ to $V^+ - 25\text{V}$. Applying

higher voltages will not damage the device if the overdrive is moderately current limited. Short circuits on one output can load the power supply generator and may disrupt the signal levels of the other outputs. The driver outputs are protected against ESD to $\pm 10\text{kV}$ for human body model discharges.

REC2 IN, REC1 IN (Pins 8, 13): Receiver Inputs. These pins accept RS232 level signals ($\pm 30\text{V}$) into a protected 5k terminating resistor. The receiver inputs are protected against ESD to $\pm 10\text{kV}$ for human body model discharges. Each receiver provides 0.4V of hysteresis for noise immunity. Open receiver inputs assume a logic low state.

REC2 OUT, REC1 OUT (Pins 9, 12): Receiver Outputs with TTL/CMOS Voltage Levels. Outputs are fully short-circuit protected to ground or V_{CC} with the power ON or OFF.

TR2 IN, TR1 IN (Pins 10, 11): RS232 Driver Input Pins. These inputs are TTL/CMOS compatible. Inputs should not be allowed to float. Tie unused inputs to V_{CC} .

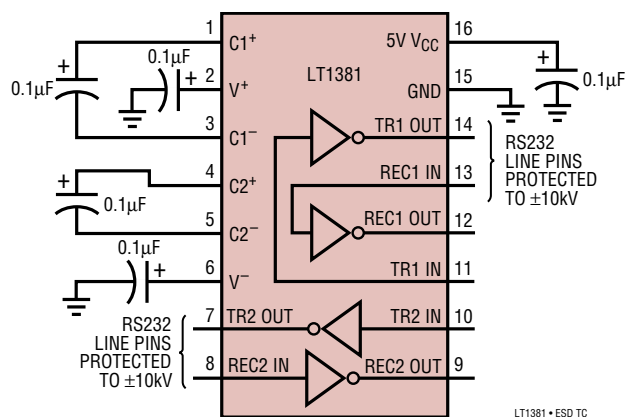
GND (Pin 15): Ground Pin.

V_{CC} (Pin 16): 5V Input Supply Pin. This pin should be decoupled with a 0.1μF ceramic capacitor close to the package pin. Insufficient supply bypassing can result in low output drive levels and erratic charge pump operation.

ESD PROTECTION

The RS232 line inputs of the LT1381 have on-chip protection from ESD transients up to $\pm 10\text{kV}$. The protection structures act to divert the static discharge safely to system ground. In order for the ESD protection to function effectively, the power supply and ground pins of the circuit must be connected to ground through low impedances. The power supply decoupling capacitors and charge pump storage capacitors provide this low impedance in normal application of the circuit. The only constraint is that low ESR capacitors must be used for bypassing and charge storage. ESD testing must be done with pins V_{CC} , V⁺, V⁻ and GND shorted to ground or connected with low ESR capacitors.

ESD Test Circuit



The schematic diagram illustrates a floating RS232 interface circuit. It is divided into three main sections by a dashed line representing an isolation barrier.

- Power Section (Top):** A 5V input from a system (5V ±10%) is filtered by a 100μF capacitor. This feeds an LT1111 voltage regulator, which is configured with a 47Ω resistor at its I_{LIM} pin, its SW1 pin connected to the input, and its SW2 pin connected to ground. The output of the LT1111 is connected to a CTX20-1 1:1 transformer. The secondary of the transformer is connected to a 1N5818 diode, which is in series with a 1Ω resistor and a 100μF capacitor. This is followed by a 0.1μF capacitor to ground and a 10μF capacitor to the 5V output (5V ±10%).
- Isolation Section (Middle):** Two LTC1145 isolators are used. The top LTC1145 has its V_{CC} pin connected to the 5V supply, its IN pin connected to the ISOLATOR OUTPUT (DATA TO SYSTEM), and its OS pin connected to ground. The bottom LTC1145 has its IN pin connected to the ISOLATOR INPUT (DATA FROM SYSTEM), its OS pin connected to ground, and its V_{CC} pin connected to the output of the top LTC1145.
- RS232 Section (Right):** An LT1381 RS232 driver/receiver is shown. Its V_{CC} pin (pin 16) is connected to the 5V supply. Pins 1, 3, 4, 5, and 6 are connected to ground through 0.1μF capacitors. Pin 9 (R_X) is connected to the OS pin of the bottom LTC1145. Pin 11 (D_X) is connected to the OS pin of the top LTC1145. Pin 8 is labeled RS232 INPUT and pin 14 is labeled RS232 OUTPUT. Pins 2, 7, 10, 12, 13, and 15 are connected to ground.

Labels at the bottom indicate the connection points: ISOLATOR OUTPUT (DATA TO SYSTEM), ISOLATOR INPUT (DATA FROM SYSTEM), ISOLATION BARRIER, FLOATING GROUND, and SYSTEM GROUND.

ISOLATOR INPUT

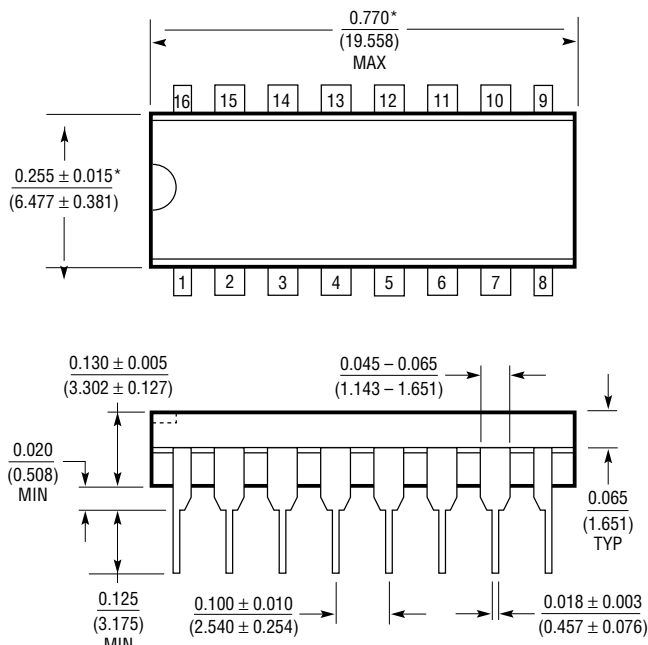
RS232 OUTPUT

50mV 50ms 20ms

PACKAGE DESCRIPTION

Dimensions in inches (millimeters) unless otherwise noted.

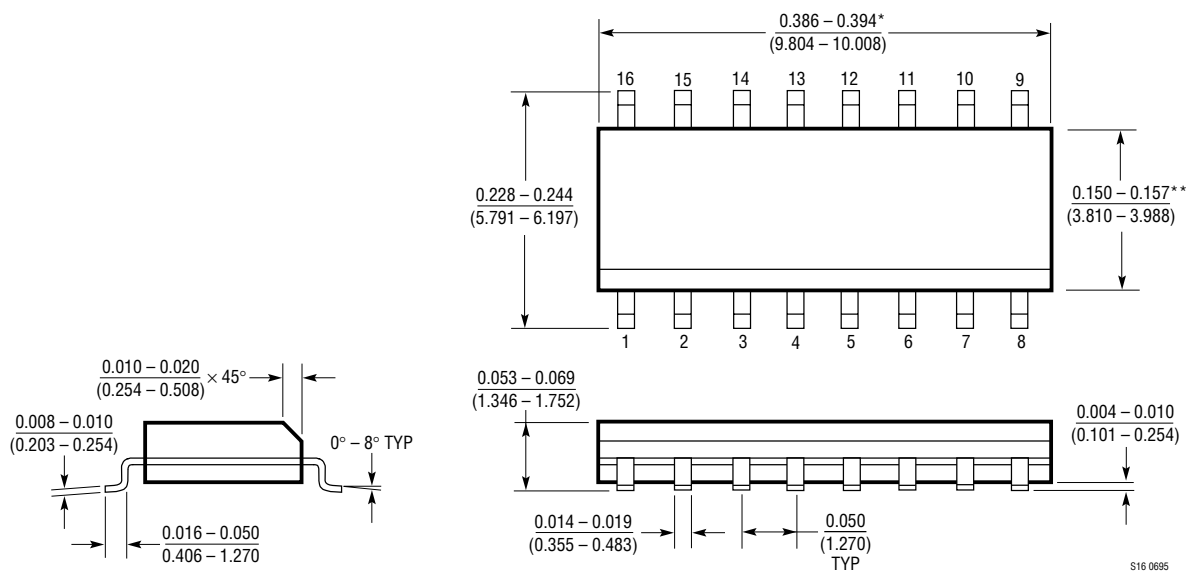
N Package 16-Lead PDIP (Narrow 0.300) (LTC DWG # 05-08-1510)



*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.010 INCH (0.254mm)

N16 1197

S Package 16-Lead Plastic Small Outline (Narrow 0.150) (LTC DWG # 05-08-1610)



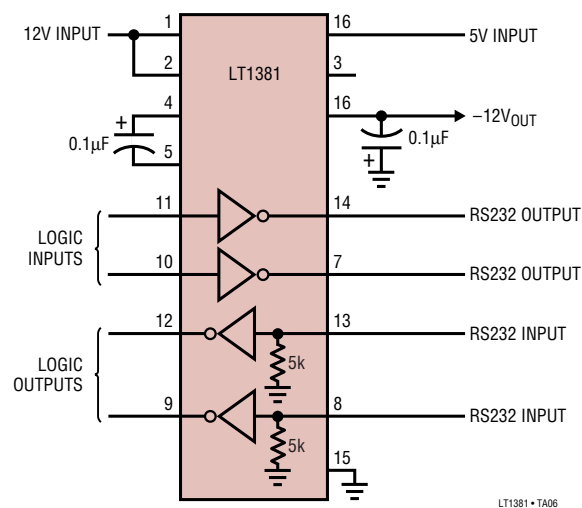
*DIMENSION DOES NOT INCLUDE MOLD FLASH. MOLD FLASH
SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

**DIMENSION DOES NOT INCLUDE INTERLEAD FLASH. INTERLEAD
FLASH SHALL NOT EXCEED 0.010" (0.254mm) PER SIDE

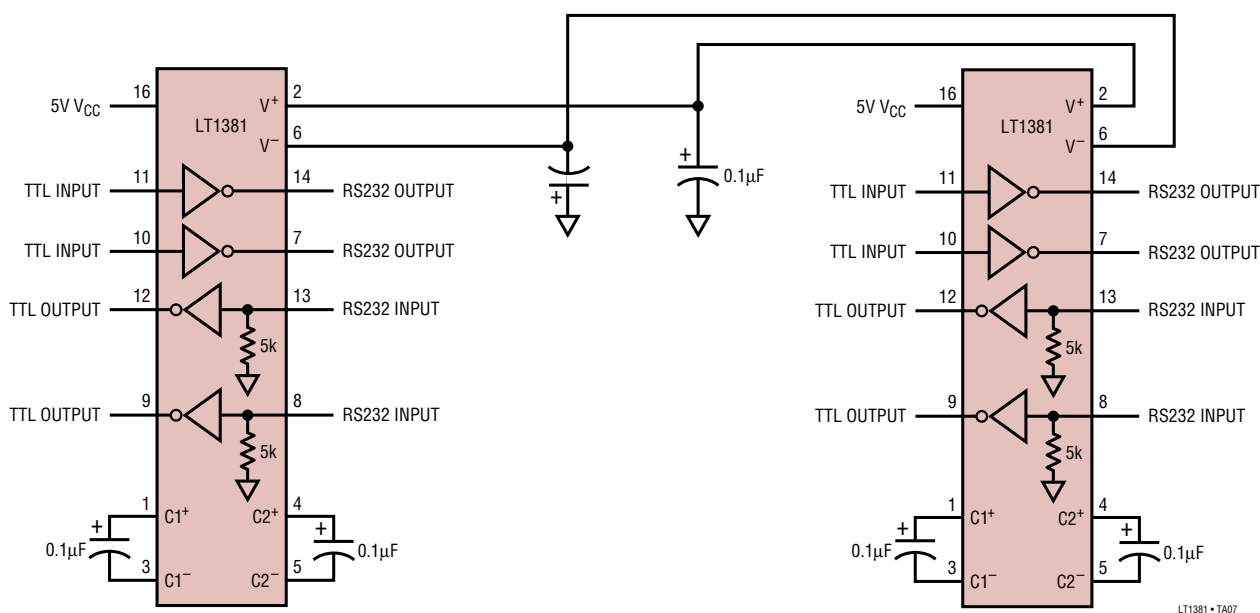
S16 0695

TYPICAL APPLICATIONS

Operation Using 5V and 12V Power Supplies



Sharing Capacitors



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LT1180A/LT1181A	5V 2-Driver/2-Receiver RS232 Transceivers	Pin Compatible with LT1280A/LT1280A
LT1280A/LT1281A	5V 2-Driver/2-Receiver RS232 Transceivers	Pin Compatible with LT1180A/LT1181A
LT1780/LT1781	5V 2-Driver/2-Receiver RS232 Transceivers	IEC 1000-4-2 Level 4 Compliance