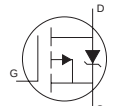


Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	-20	—	—	V	$V_{GS} = 0V$, $I_D = -250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	-0.009	—	V/°C	Reference to 25°C , $I_D = -1mA$ ②
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	0.050	0.065	Ω	$V_{GS} = -4.5V$, $I_D = -3.7A$ ②
		—	0.080	0.135		$V_{GS} = -2.5V$, $I_D = -3.1A$ ②
$V_{GS(th)}$	Gate Threshold Voltage	-0.40	-0.55	-1.2	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
g_{fs}	Forward Transconductance	6.0	—	—	S	$V_{DS} = -10V$, $I_D = -3.7A$ ②
I_{DSS}	Drain-to-Source Leakage Current	—	—	-1.0	μA	$V_{DS} = -20V$, $V_{GS} = 0V$
		—	—	-25		$V_{DS} = -20V$, $V_{GS} = 0V$, $T_J = 70^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	-100	nA	$V_{GS} = -12V$
	Gate-to-Source Reverse Leakage	—	—	100		$V_{GS} = 12V$
Q_g	Total Gate Charge	—	8.0	12	nC	$I_D = -3.7A$
Q_{gs}	Gate-to-Source Charge	—	1.2	1.8		$V_{DS} = -10V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	2.8	4.2		$V_{GS} = -5.0V$ ②
$t_{d(on)}$	Turn-On Delay Time	—	350	—	ns	$V_{DD} = -10V$
t_r	Rise Time	—	48	—		$I_D = -3.7A$
$t_{d(off)}$	Turn-Off Delay Time	—	588	—		$R_G = 89\Omega$
t_f	Fall Time	—	381	—		$R_D = 2.7\Omega$
C_{iss}	Input Capacitance	—	633	—	pF	$V_{GS} = 0V$
C_{oss}	Output Capacitance	—	145	—		$V_{DS} = -10V$
C_{rss}	Reverse Transfer Capacitance	—	110	—		$f = 1.0MHz$

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	-1.3	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	-22		
V_{SD}	Diode Forward Voltage	—	—	-1.2	V	$T_J = 25^\circ\text{C}$, $I_S = -1.0A$, $V_{GS} = 0V$ ②
t_{rr}	Reverse Recovery Time	—	29	43	ns	$T_J = 25^\circ\text{C}$, $I_F = -1.0A$
Q_{rr}	Reverse Recovery Charge	—	11	17	nC	$di/dt = -100A/\mu s$ ②

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- ② Pulse width $\leq 400\mu s$; duty cycle $\leq 2\%$.
- ③ Surface mounted on 1" square single layer 1oz. copper FR4 board, steady state.
- ④ Starting $T_J = 25^\circ\text{C}$, $L = 1.65mH$
 $R_G = 25\Omega$, $I_{AS} = -3.7A$.

** For recommended footprint and soldering techniques refer to application note #AN-994.

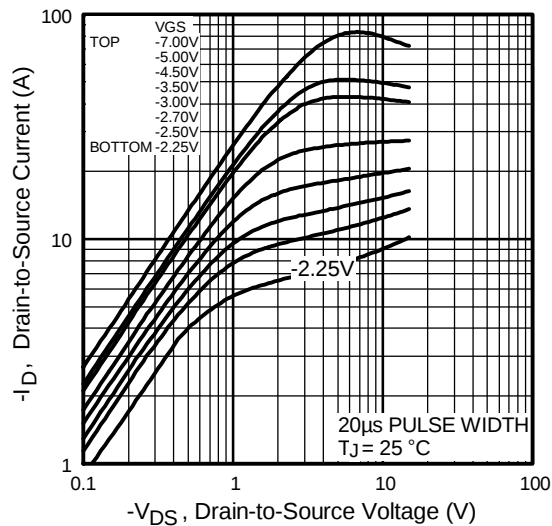


Fig 1. Typical Output Characteristics

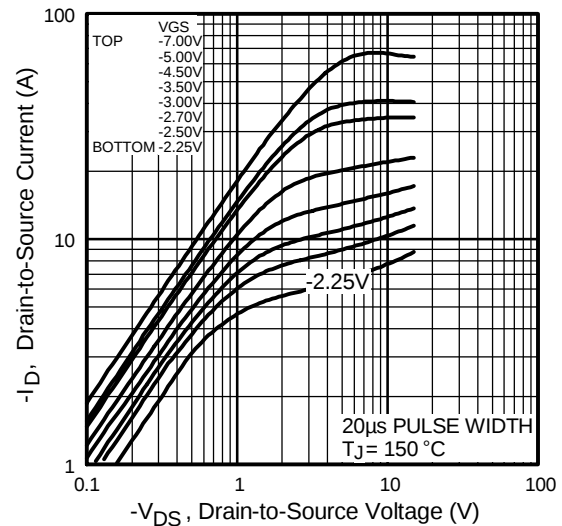


Fig 2. Typical Output Characteristics

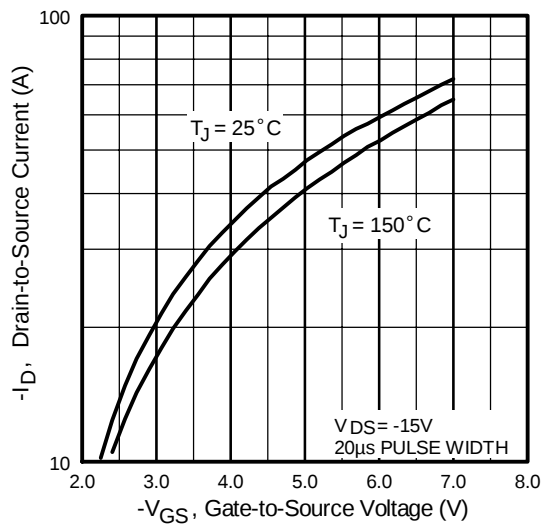


Fig 3. Typical Transfer Characteristics

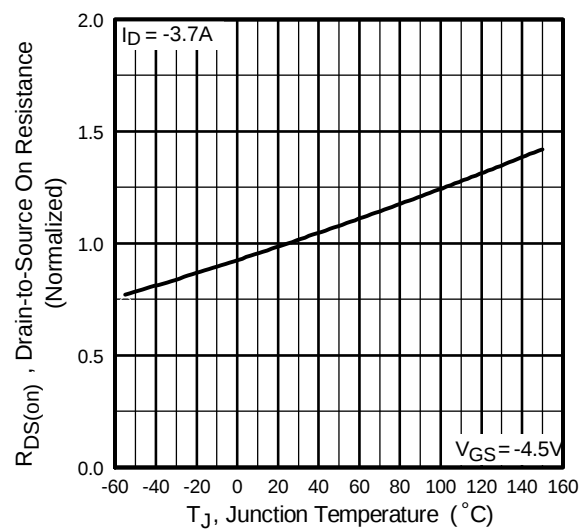


Fig 4. Normalized On-Resistance Vs. Temperature

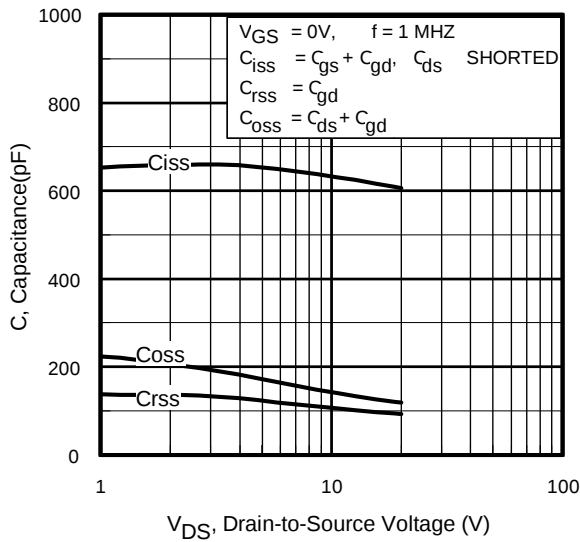


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

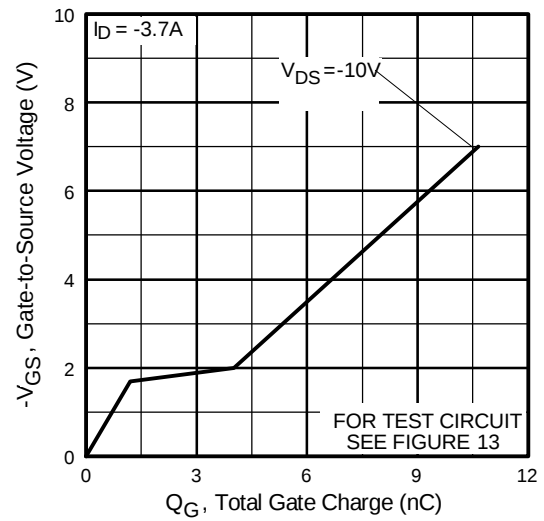


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

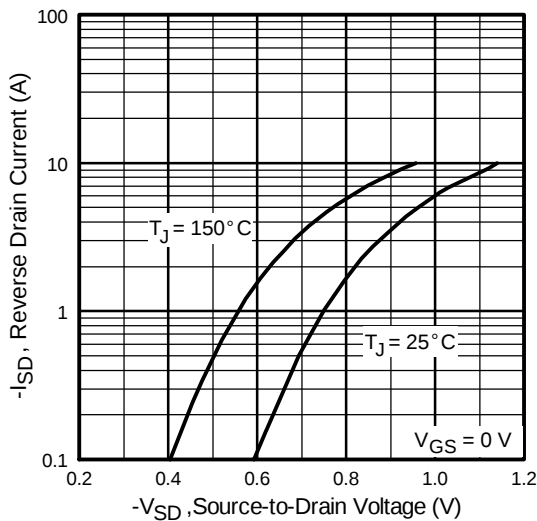


Fig 7. Typical Source-Drain Diode Forward Voltage

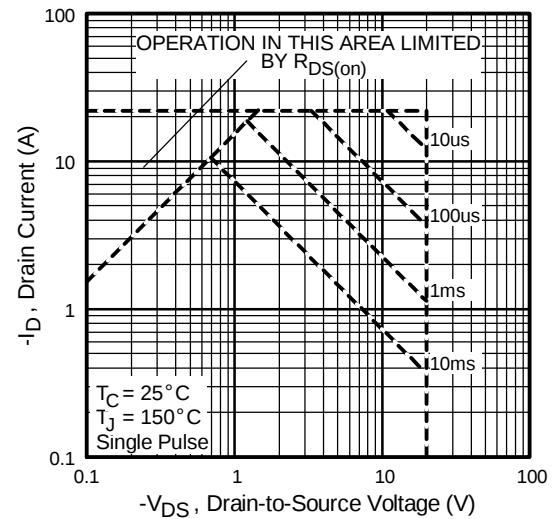


Fig 8. Maximum Safe Operating Area

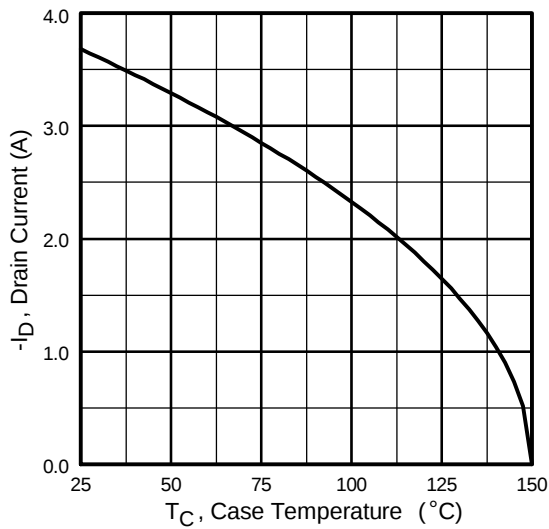


Fig 9. Maximum Drain Current Vs. Case Temperature

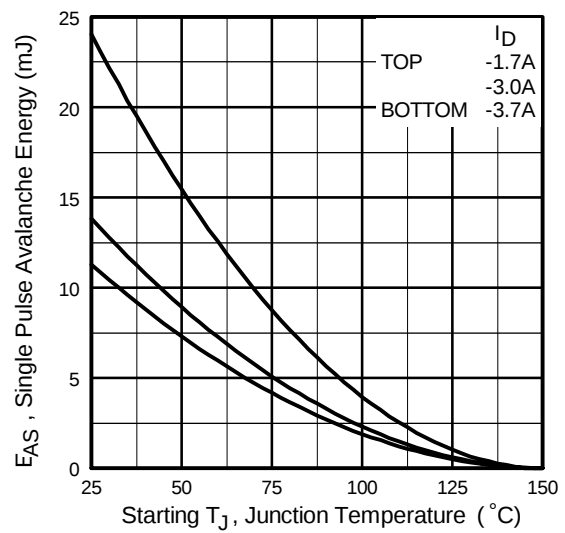


Fig 10. Maximum Avalanche Energy Vs. Drain Current

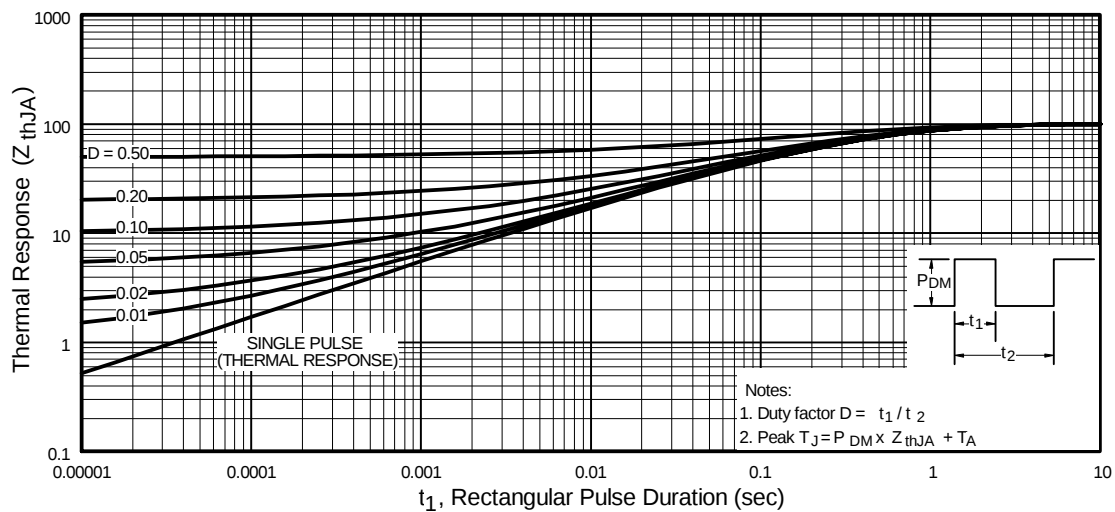


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

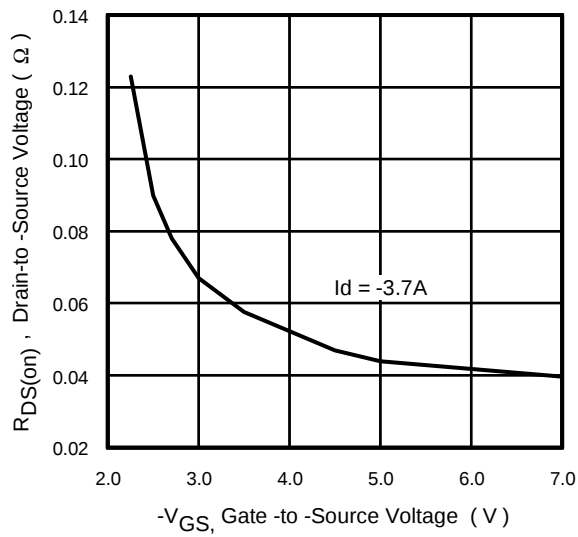


Fig 12. Typical On-Resistance Vs. Gate Voltage

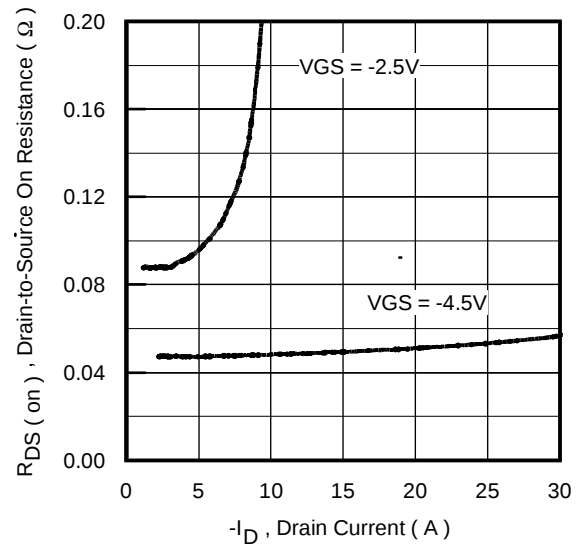
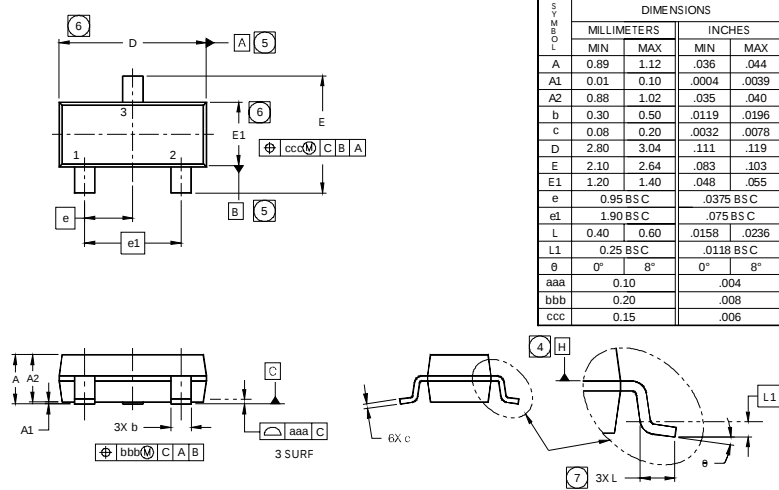


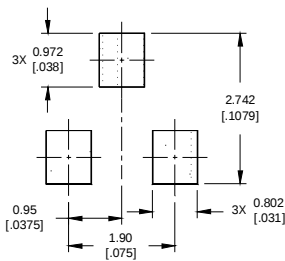
Fig 13. Typical On-Resistance Vs. Drain Current

Micro3 (SOT-23/TO-263AB) Package Outline

Dimensions are shown in millimeters (inches)



RECOMMENDED FOOTPRINT

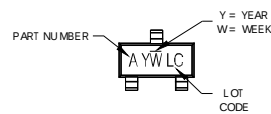


NOTES

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
2. DIMENSIONS ARE SHOWN IN MILLIMETERS AND INCHES.
3. CONTROLLING DIMENSION: MILLIMETER.
4. DATUM PLANE H IS LOCATED AT THE MOLD PARTING LINE.
5. DATUM A AND B TO BE DETERMINED AT DATUM PLANE H.
6. DIMENSIONS D AND E1 ARE MEASURED AT DATUM PLANE H.
7. DIMENSION L IS THE LEAD LENGTH FOR SOLDERING TO A SUBSTRATE.
8. OUTLINE CONFORMS TO JEDEC OUTLINE TO-236AB.

Micro3 (SOT-23/TO-236AB) Part Marking Information

W = (1-26) IF PRECEDED BY LAST DIGIT OF CALENDAR YEAR



PART NUMBER CODE REFERENCE:

A = IRLML2402
B = IRLML2803
C = IRLML6302
D = IRLML5103
E = IRLML6402
F = IRLML6401
G = IRLML2502
H = IRLML5203

Note: A line above the work week (as shown here) indicates Lead-Free.

YEAR	Y	WORK WEEK	W
2001	1	01	A
2002	2	02	B
2003	3	03	C
1994	4	04	D
1995	5		
1996	6		
1997	7		
1998	8		
1999	9		
2000	0	24	X
		25	Y
		26	Z

W = (27-52) IF PRECEDED BY A LETTER

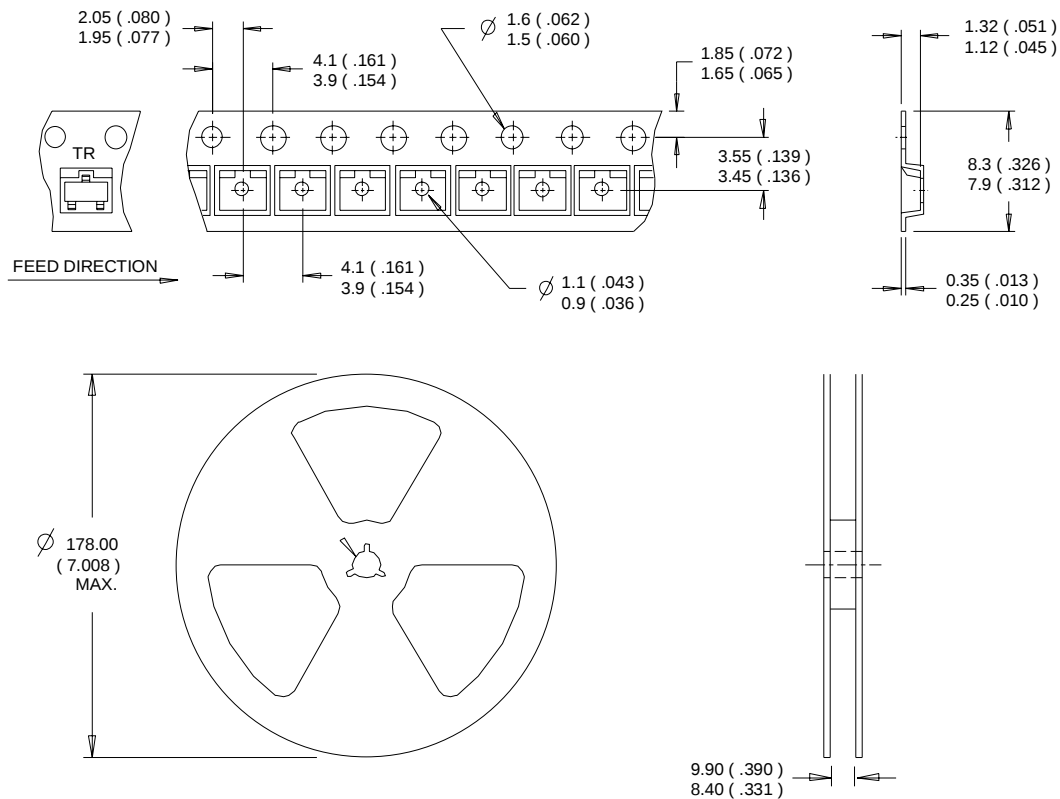
YEAR	Y	WORK WEEK	W
2001	A	27	A
2002	B	28	B
2003	C	29	C
1994	D	30	D
1995	E		
1996	F		
1997	G		
1998	H		
1999	J		
2000	K	50	X
		51	Y
		52	Z

IRLML6402

International
IR Rectifier

Micro3™(SOT-23/TO-263AB) Tape & Reel Information

Dimensions are shown in millimeters (inches)



- NOTES:
1. CONTROLLING DIMENSION : MILLIMETER.
 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.

Data and specifications subject to change without notice.

International
IR Rectifier

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TAC Fax: (310) 252-7903

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