



**GaAs MMIC I/Q MIXER
6 - 10 GHz**

Data taken as IRM with External IF Hybrid

Conversion Gain vs. Temperature*

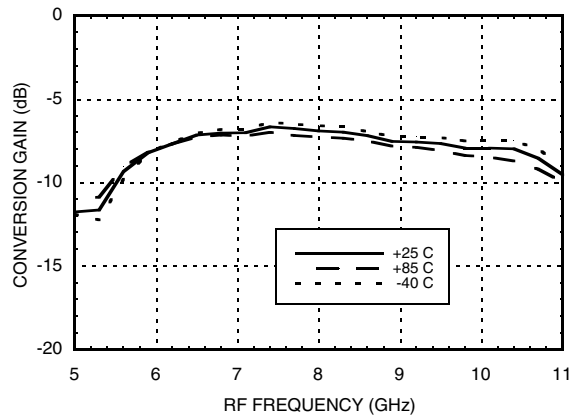
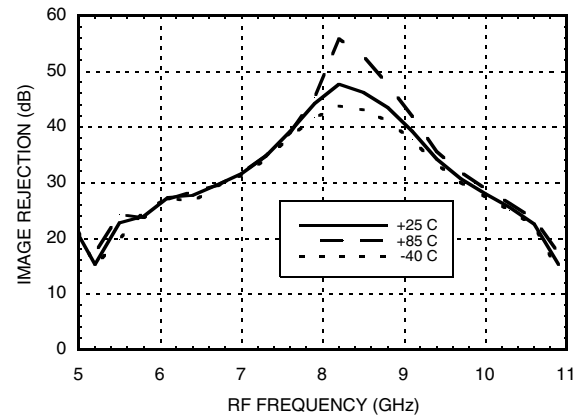
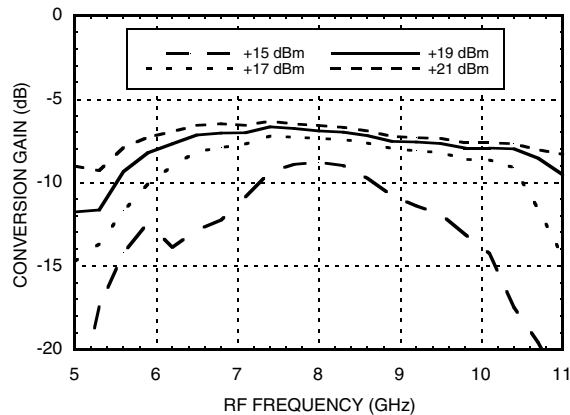


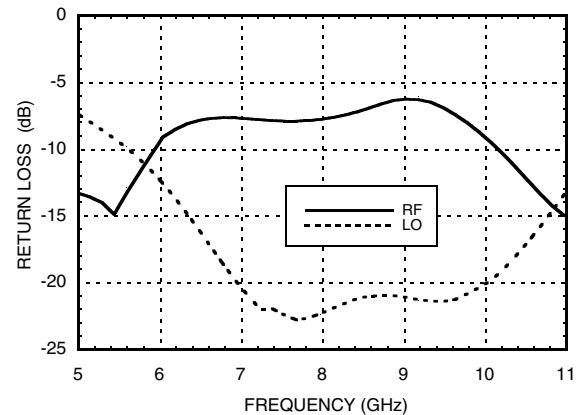
Image Rejection vs. Temperature



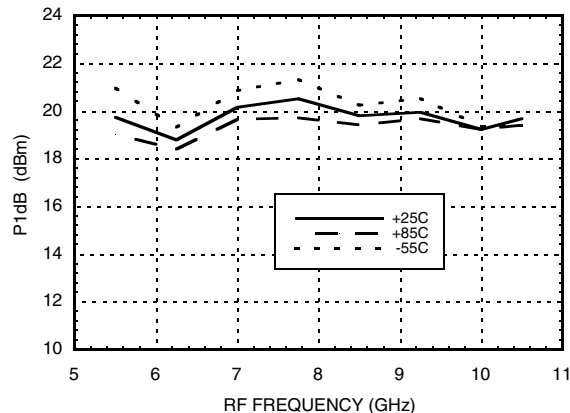
Conversion Gain vs. LO Drive



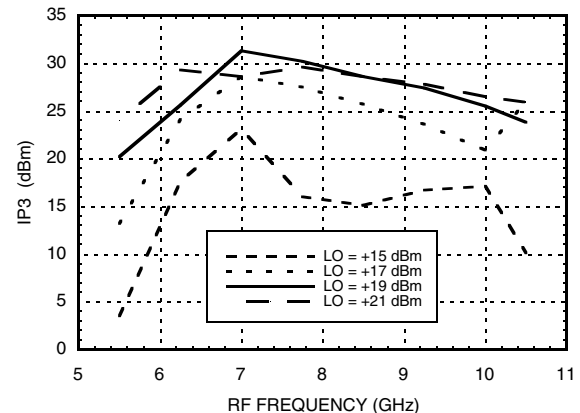
Return Loss



Input P1dB vs. Temperature



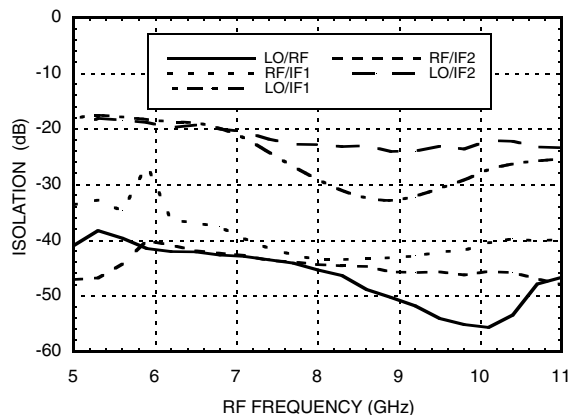
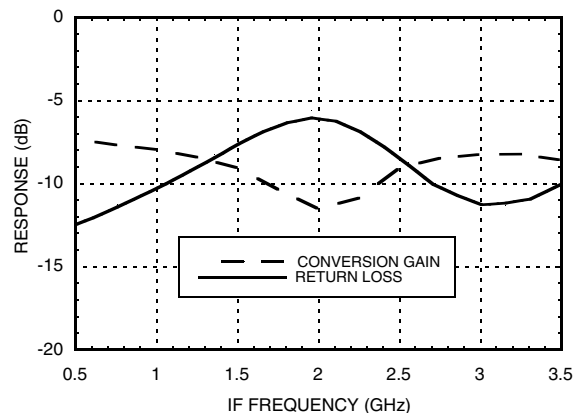
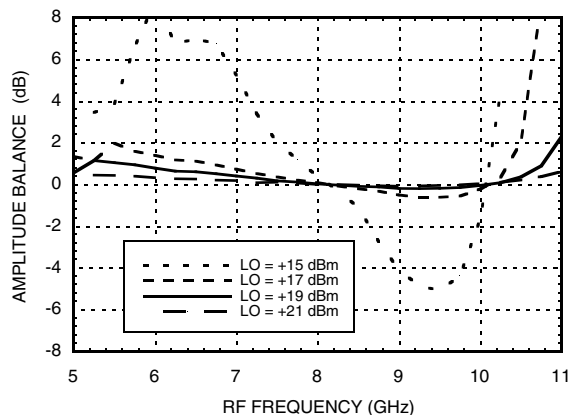
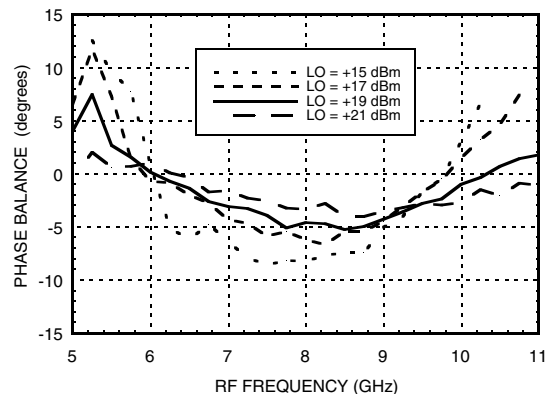
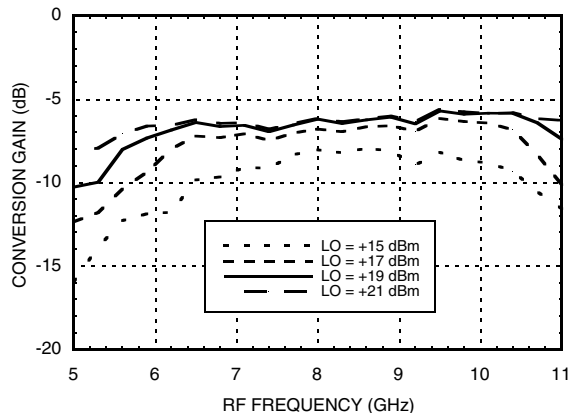
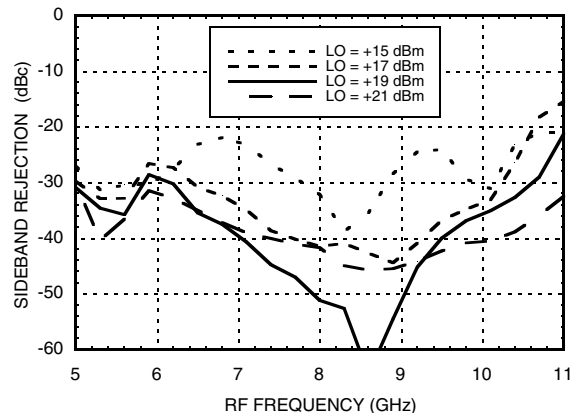
Input IP3 vs. LO Drive



* Conversion gain data taken with external IF hybrid

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**GaAs MMIC I/Q MIXER
6 - 10 GHz**
Quadrature Channel Data Taken Without IF Hybrid
Isolations

IF Bandwidth

Amplitude Balance vs. LO Drive

Phase Balance vs. LO Drive

Upconverter Performance Conversion Gain vs. LO Drive

Upconverter Performance Sideband Rejection vs. LO Drive


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Harmonics of LO

LO Freq. (GHz)	nLO Spur at RF Port			
	1	2	3	4
3.5	39	40	52	51
6.5	43	49	51	70
7.5	51	65	53	62
8.5	56	61	56	50
9.5	47	57	65	63
10.5	45	55	59	46

LO = +19 dBm
Values in dBc below input LO level measured at RF Port.

MxN Spurious Outputs

mRF	nLO				
	0	1	2	3	4
0	xx	10	29	18	51
1	33	0	46	77	68
2	99	71	75	70	99
3	97	101	100	86	101
4	99	98	98	102	107

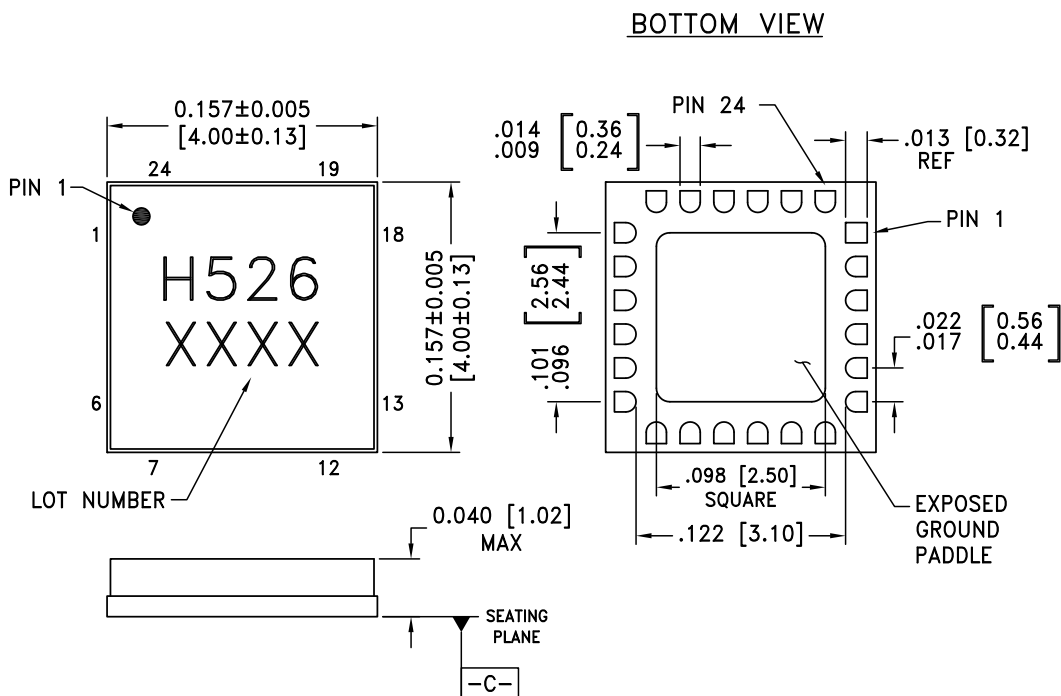
RF = 7.6 GHz @ -10 dBm
LO = 7.5 GHz @ +19 dBm
Data taken without IF hybrid
All values in dBc below IF power level

Absolute Maximum Ratings

RF / IF Input	+20 dBm
LO Drive	+27 dBm
Channel Temperature	150°C
Continuous Pdiss (T=85°C) (derate 7.8 mW/°C above 85°C)	507 mW
Thermal Resistance (R _{TH}) (junction to die bottom)	128 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 0, Passed 150V



**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**


GaAs MMIC I/Q MIXER
6 - 10 GHz
Outline Drawing

NOTES:

1. PACKAGE BODY MATERIAL: ALUMINA
2. LEAD AND GROUND PADDLE PLATING: 30 - 80 MICROINCHES GOLD OVER 50 MICROINCHES MINIMUM NICKLE
3. DIMENSIONS ARE IN INCHES [MILLIMETERS]
4. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
5. PACKAGE WARP SHALL NOT EXCEED 0.05mm DATUM
6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND

Package Information


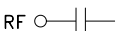
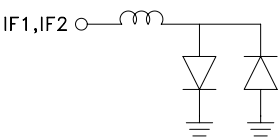
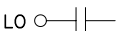
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC526LC4	Alumina, White	Gold over Nickel	MSL3 ^[1]	H526 XXXX

[1] Max peak reflow temperature of 260 °C

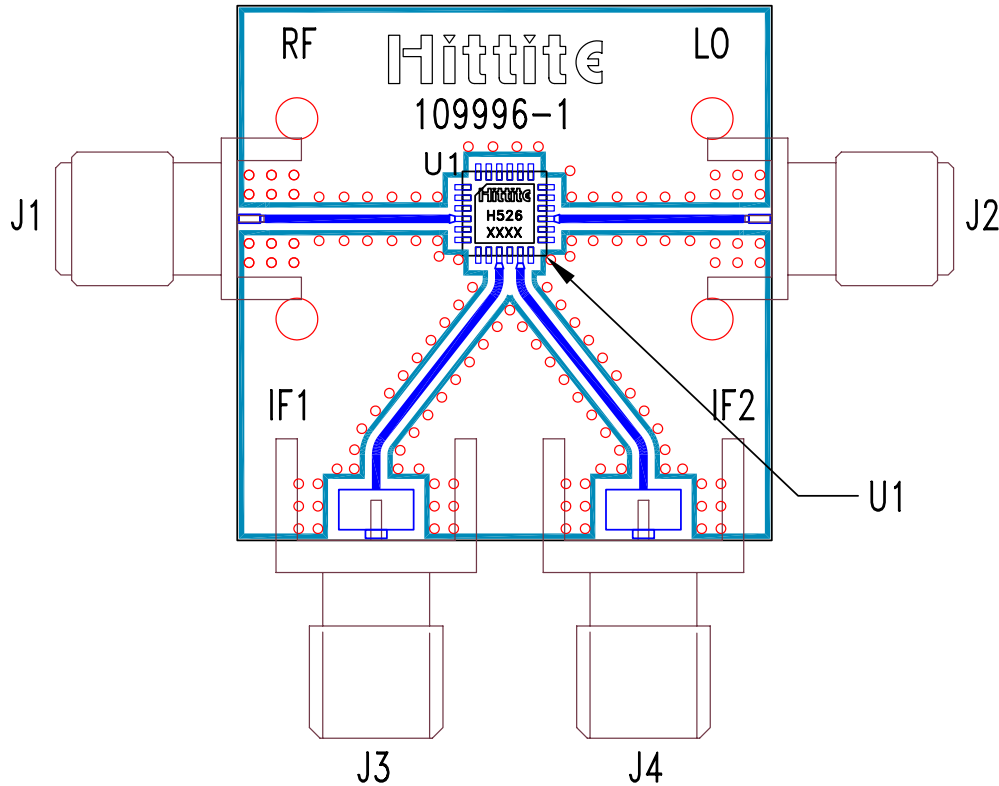
[2] 4-Digit lot number XXXX



Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 2, 6 - 8, 10, 13, 17 - 24	N/C	No connection required. These pins may be connected to RF/DC ground without affecting performance.	
3, 5, 12, 14, 16	GND	These pins and package bottom must be connected to RF/DC ground.	
4	RF	This pin is AC coupled and matched to 50 Ohms from 6 to 10 GHz.	
9	IF1	This pin is DC coupled. For applications not requiring operation to DC, this port should be DC blocked externally using a series capacitor whose value has been chosen to pass the necessary IF frequency range. For operation to DC, this pin must not source/sink more than 3mA of current or part non-function and possible part failure will result.	
11	IF2		
15	LO	This pin is AC coupled and matched to 50 Ohms from 6 to 10 GHz.	

Evaluation PCB



List of Materials for Evaluation PCB 109998 ^[1]

Item	Description
J1, J2	PCB Mount K RF Connector, SRI
J3 - J4	PCB Mount SMA Connector, Johnson
U1	HMC526LC4
PCB [2]	109996 Evaluation Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and exposed paddle should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hitrite upon request.

