Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Cha	aracteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA				V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu A$ , Referenced to 25°C		0.6		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V			10	μА
		V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125°C			100	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V			-100	nA
On Ch:	aracteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.5 A		2.8	3.6	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 1.5 A (Note 4)		2.6		S
C <sub>oss</sub>	Output Capacitance Reverse Transfer Capacitance	f = 1.0 MHz		50 5.5	65 7.5	pF pF
C <sub>rss</sub>	' '	1 – 1.0 WH 12			7.5	
Switch	ing Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 300 V, I <sub>D</sub> = 3.0 A,		10	30	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25 \Omega$		30	70	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	- 1.6		20	50	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4, 5)		30	70	ns
	T	1/ 4001/1 004		10	13	nC
Q <sub>g</sub>	Total Gate Charge	$V_{DS} = 480 \text{ V}, I_{D} = 3.0 \text{ A},$		10	10	IIC
	Gate-Source Charge	$V_{DS} = 480 \text{ V}, I_{D} = 3.0 \text{ A},$ $V_{GS} = 10 \text{ V}$		2.7		nC
Q <sub>g</sub> Q <sub>gs</sub>		_		_		
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V (Note 4, 5)		2.7		nC
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub>	Gate-Source Charge Gate-Drain Charge	V <sub>GS</sub> = 10 V (Note 4, 5)  nd Maximum Ratings		2.7		nC
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub> <b>Drain-S</b>	Gate-Source Charge Gate-Drain Charge Source Diode Characteristics ar	V <sub>GS</sub> = 10 V  (Note 4, 5)  nd Maximum Ratings ode Forward Current		2.7		nC nC
Q <sub>g</sub> Q <sub>gs</sub> Q <sub>gd</sub> Drain-S I <sub>S</sub> I <sub>SM</sub>	Gate-Source Charge Gate-Drain Charge  Source Diode Characteristics as Maximum Continuous Drain-Source Dio	V <sub>GS</sub> = 10 V  (Note 4, 5)  nd Maximum Ratings ode Forward Current  Forward Current		2.7	3.0	nC nC
$Q_g$ $Q_{gs}$ $Q_{gd}$ Drain-S	Gate-Source Charge Gate-Drain Charge  Source Diode Characteristics and Maximum Continuous Drain-Source Diode Maximum Pulsed Drain-Source Diode F	V <sub>GS</sub> = 10 V  (Note 4, 5)  nd Maximum Ratings ode Forward Current		2.7 4.9	3.0	nC nC

- **Notes:**1. Repetitive Rating : Pulse width limited by maximum junction temperature 2. L = 40mH, I $_{AS}$  = 3.0A, V $_{DD}$  = 50V, R $_{G}$  = 25  $\Omega$ . Starting T $_{J}$  = 25°C 3. I $_{SD}$  ≤ 3.0A, di/dt ≤ 200A/µs, V $_{DD}$  ≤ BV $_{DSS}$ . Starting T $_{J}$  = 25°C 4. Pulse Test : Pulse width ≤ 300µs, Duty cycle ≤ 2% 5. Essentially independent of operating temperature

©2000 Fairchild Semiconductor International Rev. A, April 2000

# **Typical Characteristics**

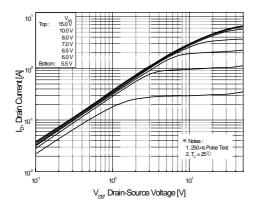


Figure 1. On-Region Characteristics

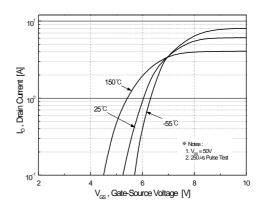


Figure 2. Transfer Characteristics

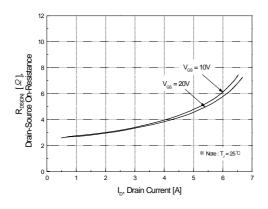


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

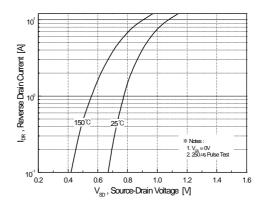


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

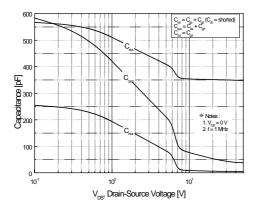


Figure 5. Capacitance Characteristics

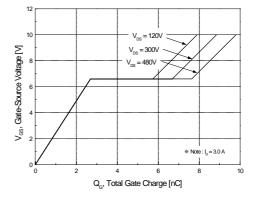
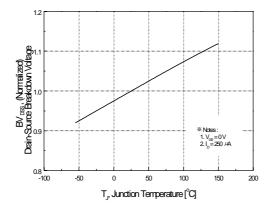


Figure 6. Gate Charge Characteristics

©2000 Fairchild Semiconductor International Rev. A, April 2000

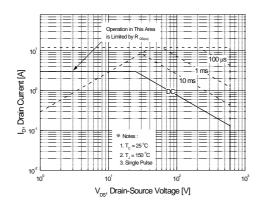
# Typical Characteristics (Continued)



30 25 (Sex) (Sex)

Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



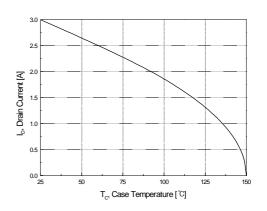


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

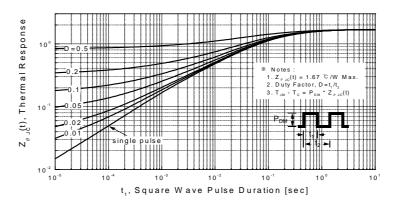
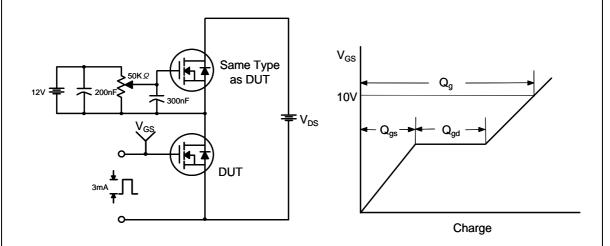


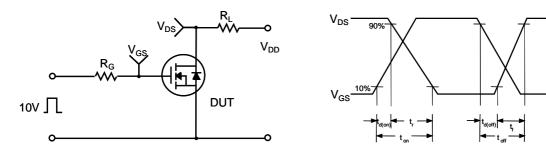
Figure 11. Transient Thermal Response Curve

©2000 Fairchild Semiconductor International Rev. A, April 2000

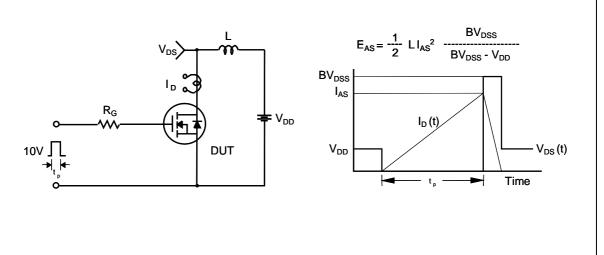
# **Gate Charge Test Circuit & Waveform**



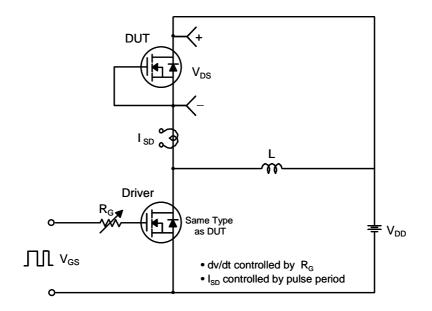
# Resistive Switching Test Circuit & Waveforms

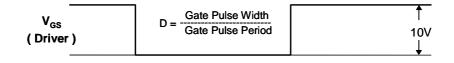


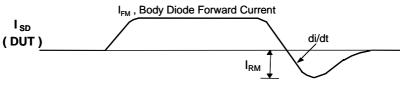
# **Unclamped Inductive Switching Test Circuit & Waveforms**



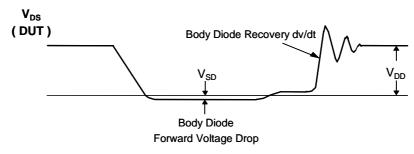
# Peak Diode Recovery dv/dt Test Circuit & Waveforms



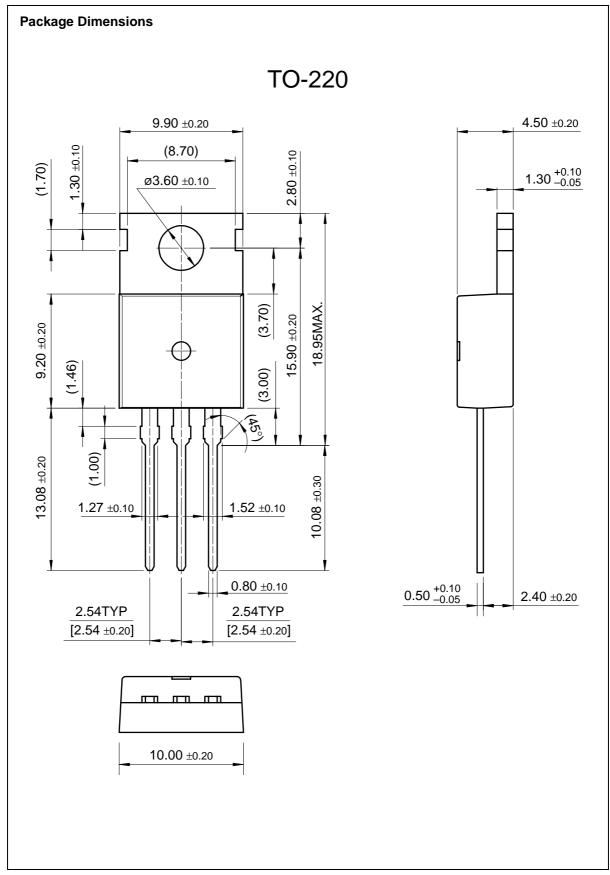




Body Diode Reverse Current



©2000 Fairchild Semiconductor International



### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $\begin{array}{ccc} \mathsf{FACT^{\mathsf{TM}}} & \mathsf{QFET^{\mathsf{TM}}} \\ \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{QS^{\mathsf{TM}}} \end{array}$ 

FAST® Quiet Series $^{TM}$  FAST $^{TM}$  SuperSOT $^{TM}$ -3 SuperSOT $^{TM}$ -6

#### **DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR INTERNATIONAL.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to

result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## **PRODUCT STATUS DEFINITIONS**

## **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

©2000 Fairchild Semiconductor International Rev. A, January 2000