May 2014



FQA13N50CF

N-Channel QFET® FRFET® MOSFET

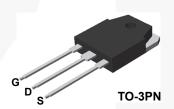
500 V, 15 A, 480 mΩ

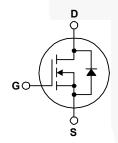
Features

- 15 A, 500 V, $R_{DS(on)}$ = 480 m Ω (Max.) @ V_{GS} = 10 V, I_D = 7.5 A
- Low Gate Charge (Typ. 43 nC)
- Low C_{rss} (Typ. 20 pF)
- 100% Avalanche Tested
- Fast Recovery Body Diode (Typ. 100 ns)

Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





Absolute Maximum Ratings TC = 25°C unless otherwise noted.

Symbol	Parameter		FQA13N50CF	Unit
V _{DSS}	Drain-Source Voltage		500	V
I _D	Drain Current - Continuous (T _C = 25°C)		15	Α
	- Continuous (T _C = 100°C)		9.5	Α
I _{DM}	Drain Current - Pulsed	(Note 1)	60	Α
V _{GSS}	Gate-Source Voltage		± 30	V
E _{AS}	Single Pulsed Avalanche Energy		860	mJ
I _{AR}	Avalanche Current	(Note 1)	15	Α
E _{AR}	Repetitive Avalanche Energy	(Note 1)	21.8	mJ
dv/dt	Peak Diode Recovery dv/dt		4.5	V/ns
P _D	Power Dissipation (T _C = 25°C)		218	W
	- Derate above 25°C		1.56	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
T _L	Maximum lead temperature for soldering purposes, 1/8""from case for 5 seconds	300	°C	

Thermal Characteristics

Symbol	Parameter	FQA13N50CF	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.58	°C/W	
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W	

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA13N50CF	FQA13N50CF	TO-3PN	Tube	N/A	N/A	30 units

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions		Тур	Max	Unit
Off Charac	teristics					ı
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	500			V
$\Delta BV_{DSS}/$ ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C		0.5		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500 V, V _{GS} = 0 V			1	μА
		V _{DS} = 400 V, T _C = 125°C			10	μА
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Charact	eristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 7.5A		0.43	0.48	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 7.5 A	\	15		S
Dynamic Cl	haracteristics					
C _{iss}	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	\	1580	2055	pF
C _{oss}	Output Capacitance	f = 1.0 MHz	\	180	235	pF
C _{rss}	Reverse Transfer Capacitance			20	25	pF
Switching C	Characteristics				1	
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 250 \text{ V}, I_D = 15\text{A},$		25	60	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		100	210	ns
t _{d(off)}	Turn-Off Delay Time			130	270	ns
t _f	Turn-Off Fall Time	(Note 4)		100	210	ns
Qg	Total Gate Charge	V _{DS} = 400 V, I _D = 15A,	/	43	56	nC
Q _{gs}	Gate-Source Charge	V _{GS} = 10 V	/	7.5		nC
Q _{qd}	Gate-Drain Charge	(Note 4)	7	18.5		nC
	Legion Community Diode Characteristics and Maximum Ratings				-	
I _S	Maximum Continuous Drain-Source Diode Fo				15	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				60	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 15 A			1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_S = 15 \text{ A},$		100		ns
Q _{rr}	Reverse Recovery Charge	$dI_F / dt = 100 \text{ A/}\mu\text{s}$		0.4		
NOTES:	The state of the s			.		μС

NOTES

^{1.} Repetitive rating: pulse-width limited by maximum junction temperature.

^{2.} L = 5.6 mH, I_{AS} = 15 A, V_{DD} = 50 V, R_G = 25 Ω , starting T_J = 25°C.

 $^{3.}I_{SD} \leq 15$ A, di/dt ≤ 200 A/µs, $V_{DD} \leq BV_{DSS}$, starting T_J = 25°C.

 $^{{\}bf 4.} \ {\bf Essentially \ independent \ of \ operating \ temperature \ typical \ characteristics.}$

Typical Performance Characteristics

Figure 1. On-Region Characteristics

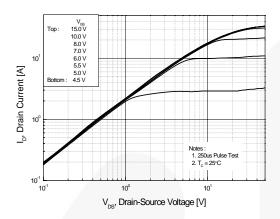


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

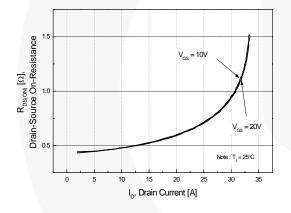


Figure 5. Capacitance Characteristics

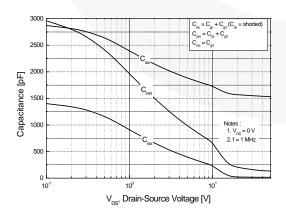


Figure 2. Transfer Characteristics

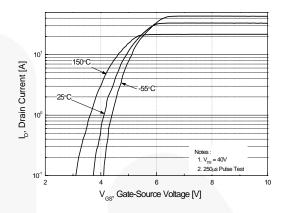


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

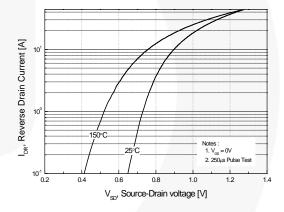
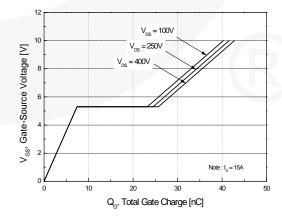


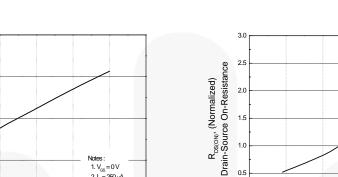
Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

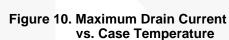
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Figure 9. Maximum Safe Operating Area

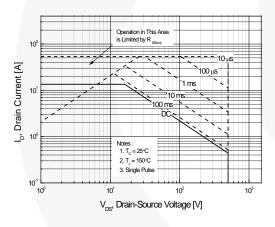


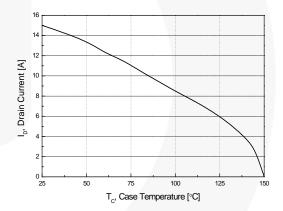
-50

-100

Figure 8. On-Resistance Variation

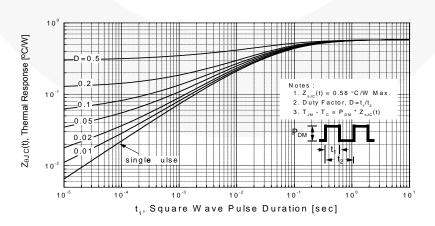
vs. Temperature





T_,, Junction Temperature [°C]

Figure 11. Transient Thermal Response Curve



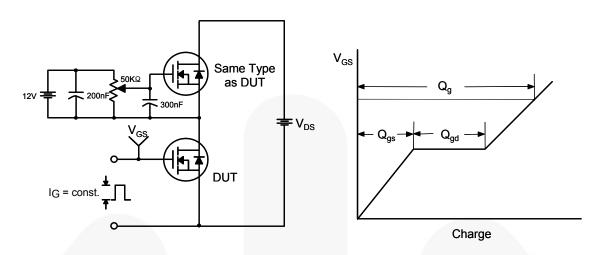


Figure 12. Gate Charge Test Circuit & Waveform

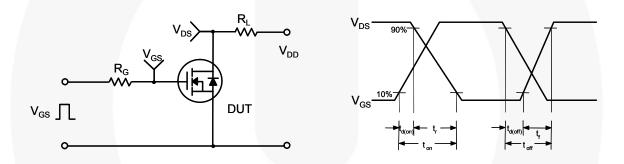


Figure 13. Resistive Switching Test Circuit & Waveforms

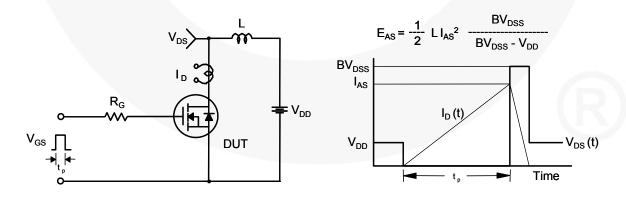


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

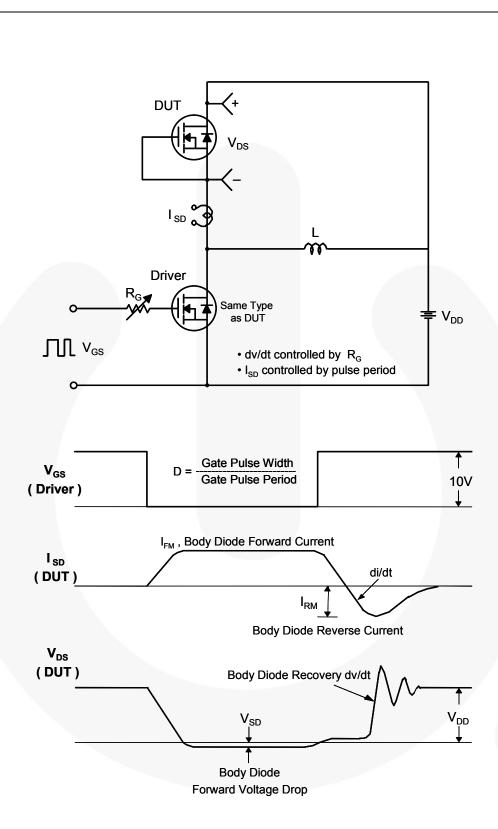
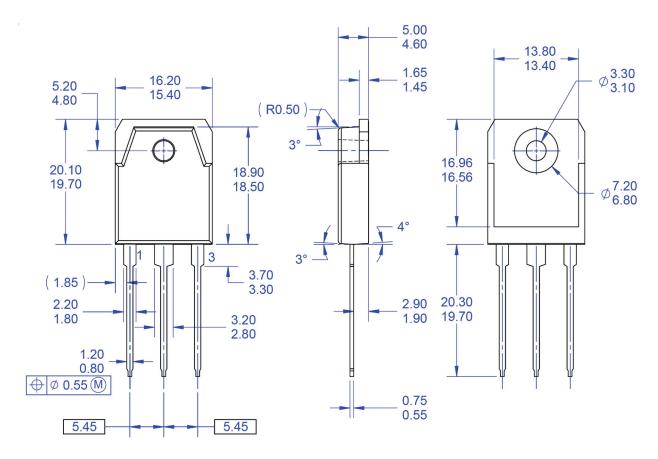
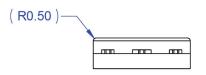


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

Mechanical Dimensions





- NOTES: UNLESS OTHERWISE SPECIFIED
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- D) DIMENSIONS ARE EXCLUSSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSSIONS.
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Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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