1 Characteristics

Table 1. Absolute maximum ratings (T_{amb} = 25 °C)

Symbol	Parameter	Value	Unit
V_{PP}	Peak pulse voltage: IEC 61000-4-2 contact discharge IEC 61000-4-2 air discharge	 ±16 ±30	kV
P_{PP}	Peak pulse power (8/20 µs)(1)	20	W
I_{PP}	Peak pulse current (8/20 µs)(1)	2.2	A
T_j	Operating junction temperature range	- 55 to +150	°C
T_{stg}	Storage temperature range	- 65 to +150	°C
T_L	Maximum lead temperature for soldering during 10 s	260	°C

1. According to IEC61000-4-5, for a surge greater than the maximum values, the diode will fail in short-circuit.

Figure 2. Electrical characteristics (definitions)

Symbol		Parameter
V_{BR}	=	Breakdown voltage
V_{CL}	=	Clamping voltage
I_{RM}	=	Leakage current @ V_{RM}
V_{RM}	=	Stand-off voltage
I_F	=	Forward current
I_{PP}	=	Peak pulse current
I_R	=	Breakdown current
V_F	=	Forward voltage drop
R_d	=	Dynamic impedance
αT	=	Voltage temperature

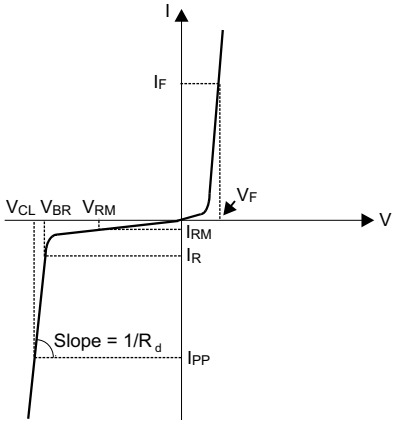


Table 2. Electrical characteristics (values, T_{amb} = 25 °C)

Symbol	Parameter	Test Condition		Min.	Typ.	Max.	Unit
V_{BR}	Breakdown voltage	I_R = 1 mA		5	6.6		V
V_{RM}	Reverse working voltage					3.6	V
I_{RM}	Leakage current	V_{RM} = 3.6 V			4	100	nA
C_{line}	Line capacitance	F = (200 MHz- 3000 MHz), V_{LINE} = 0 V			0.55	0.7	pF
V_{CL}	Reverse clamping voltage	I_{PP} = 1 A, 8/20 µs			7		V
		I_{PP} = 2.2 A, 8/20 µs			8		
		IEC 61000-4-2, 8 kV contact measured at 30 ns			10.4		
		TLP measurement (pulse duration 100 ns), I_{PP} = 16 A(1)			13.7		
R_d	Dynamic resistance(1)	Pulse duration 100 ns(1)	Direct		0.39		Ω
			Forward		0.52		
V_{FCL}	Forward clamping voltage	I_{PP} = 1 A, 8/20 µs			2.5		V
		I_{PP} = 2.2 A, 8/20 µs			4.0		
		TLP measurement (pulse duration 100 ns), I_{PP} = 16 A(1)			10.4		
F_C	Cut-off frequency	-3 dB			11.4		GHz

1. More information is available in ST application note: AN4022

Figure 3. Leakage current versus junction temperature (typical values)

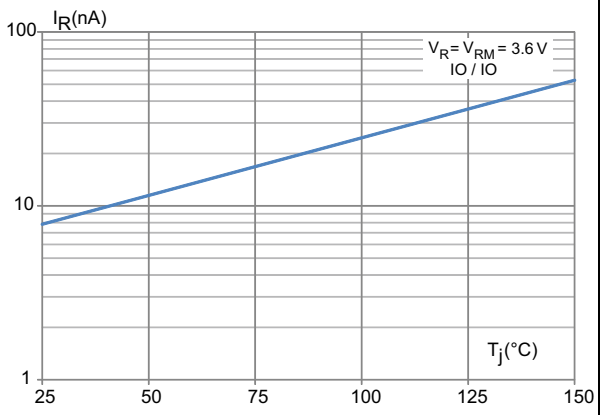


Figure 4. Junction capacitance versus frequency (typical values)

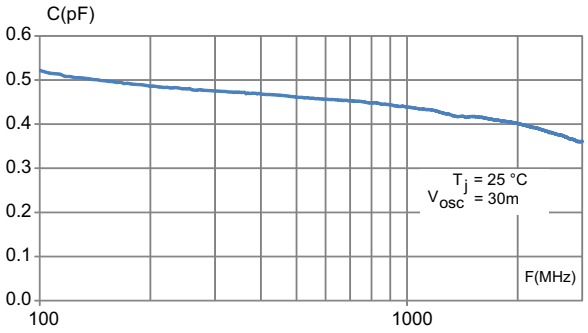


Figure 5. ESD response to IEC 61000-4-2 (+8 kV contact discharge)

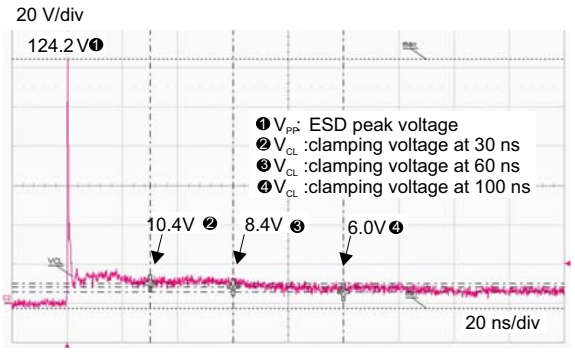


Figure 6. ESD response to IEC 61000-4-2 (-8 kV contact discharge)

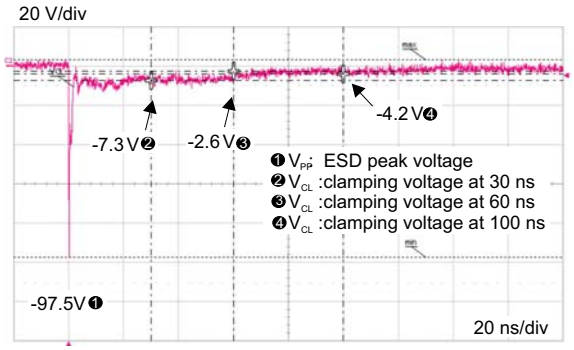


Figure 7. S21 attenuation measurement results

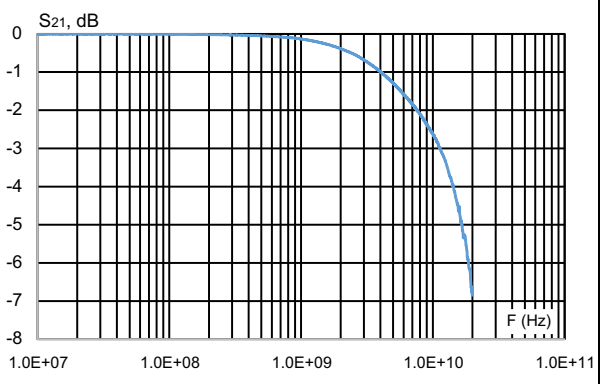
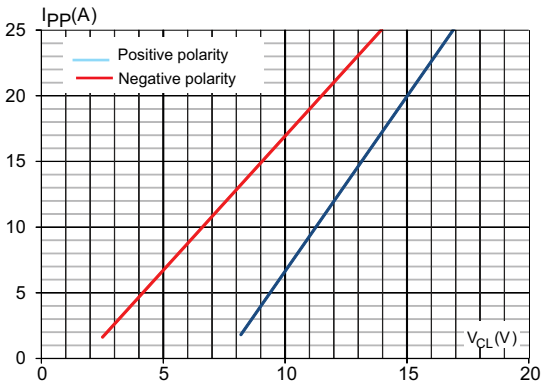


Figure 8. TLP measurements



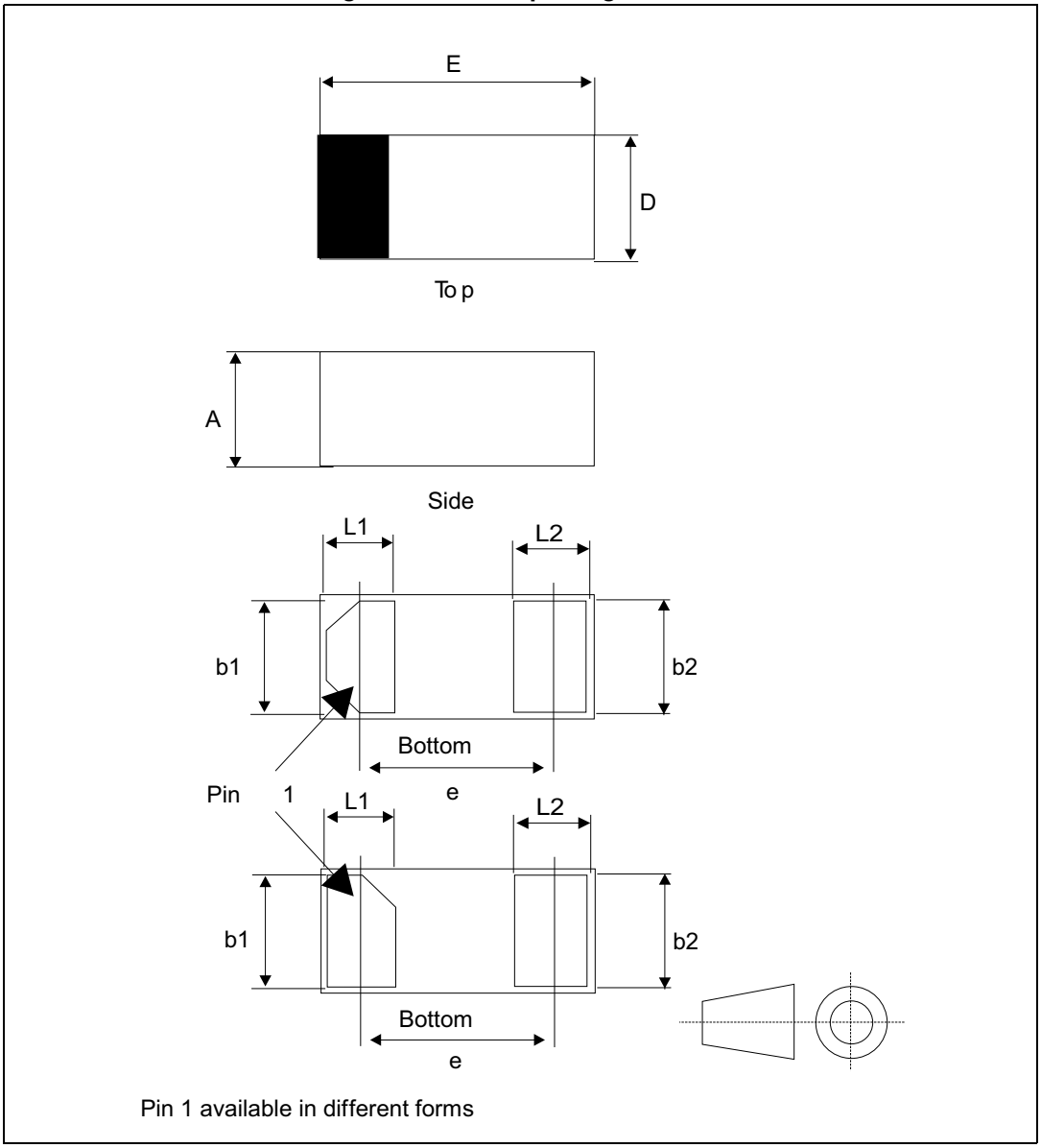
2 Package information

- Epoxy meets UL94, V0
- Bar indicates pin 1

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: *www.st.com.* ECOPACK® is an ST trademark.

2.1 ST0201 package information

Figure 9. ST0201 package outline



Pin 1 available in different forms

Table 3. 0201 package mechanical data

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	0.23	0.28	0.33	0.0091	0.0110	0.0130
b1	0.20	0.25	0.30	0.0079	0.0098	0.0118
b2	0.20	0.25	0.30	0.0079	0.0098	0.0118
D	0.25	0.30	0.35	0.0099	0.0118	0.0138
E	0.55	0.60	0.65	0.0217	0.0236	0.0256
e		0.35			0.0138	
L1	0.13	0.18	0.23	0.0052	0.0071	0.0091
L2	0.14	0.19	0.24	0.0055	0.0075	0.0095

Figure 10. Footprint, dimensions in mm (inches)

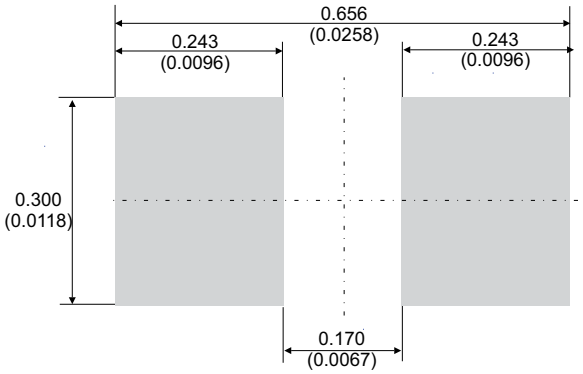
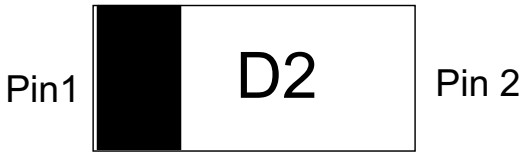
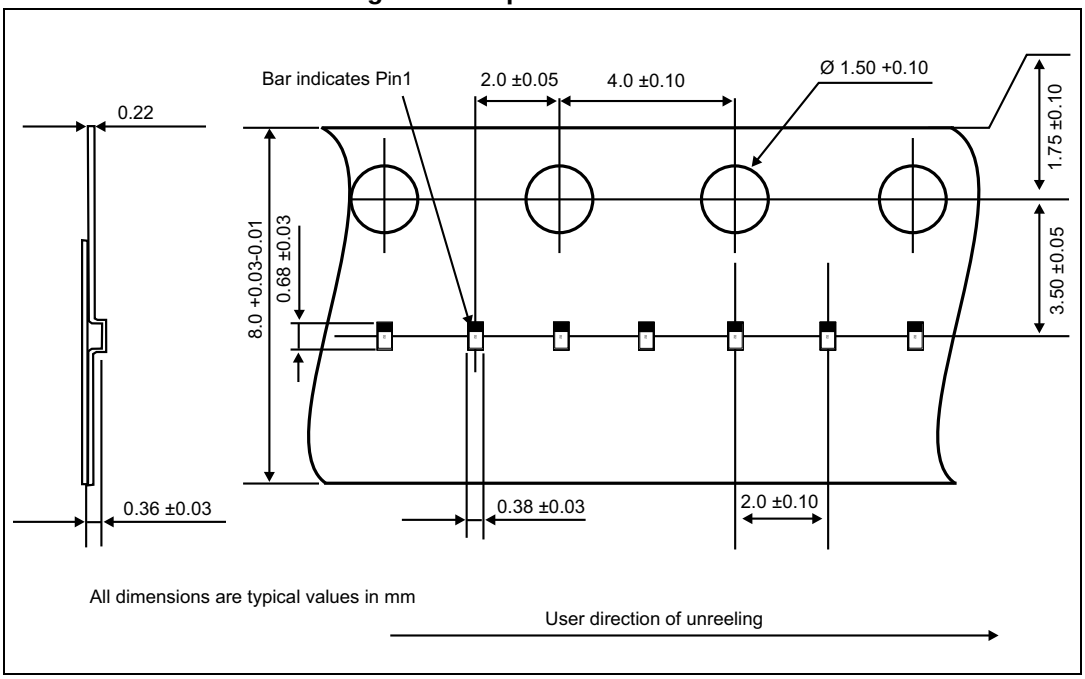


Figure 11. Marking



Note: Product marking may be rotated by 180° for assembly plant differentiation. In no case should this product marking be used to orient the component for its placement on a PCB. Only pin 1 mark is to be used for this purpose.

Figure 12. Tape and reel outline

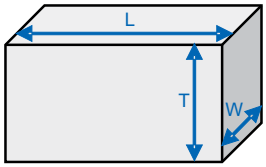


3 Recommendation on PCB assembly

3.1 Stencil opening design

1. General recommendations on stencil opening design
   a) Stencil opening dimensions: L (Length), W (Width), T (Thickness).

Figure 13. Stencil opening dimensions



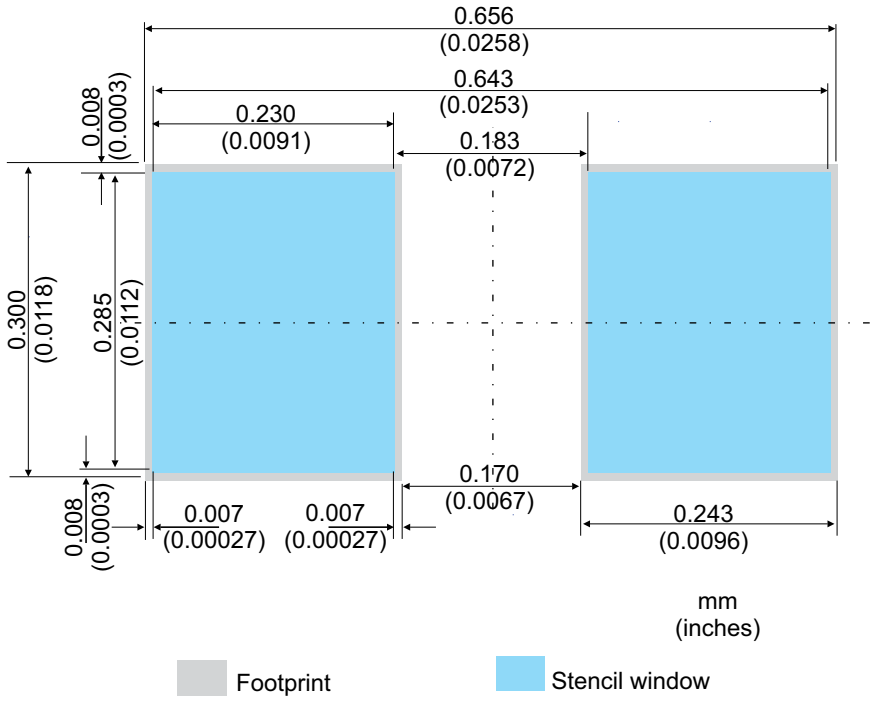
   b) General design rule
   Stencil thickness (T) = 75 ~ 125 µm

$$\text{Aspect Ratio} = \frac{W}{T} \geq 1.5$$

$$\text{Aspect Area} = \frac{L \times W}{2T(L + W)} \geq 0.66$$

2. Recommended stencil window
   a) Stencil opening thickness: 80 µm
   b) Other dimensions: see *Figure 14*

Figure 14. Recommended stencil window position, stencil opening thickness: 80 µm



3.2 Solder paste

1. Halide-free flux qualification ROL0 according to ANSI/J-STD-004.
2. “No clean” solder paste is recommended.
3. Offers a high tack force to resist component displacement during PCB movement.
4. Use solder paste with fine particles: Type 4 (powder particle size is 20-45 µm).

3.3 Placement

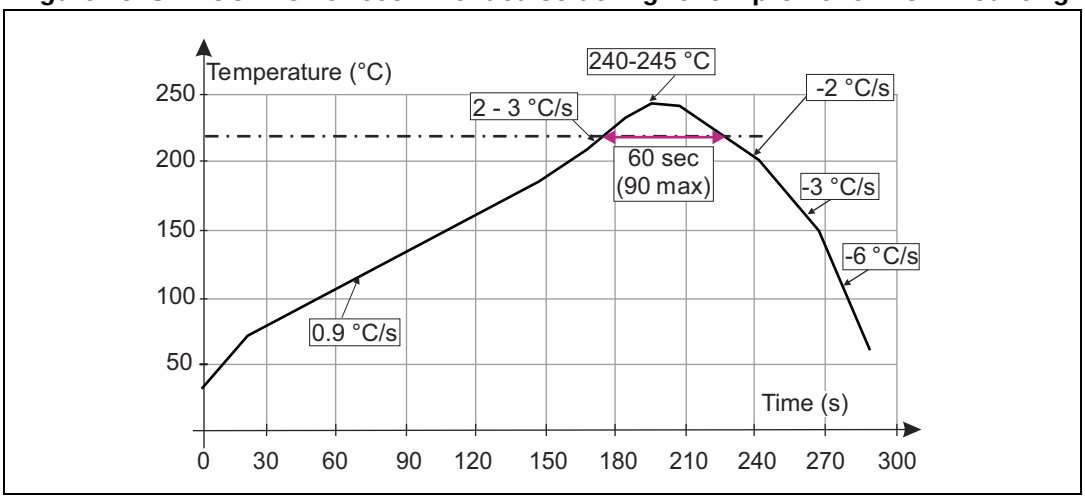
1. Manual positioning is not recommended.
2. It is recommended to use the lead recognition capabilities of the placement system, not the outline centering
3. Standard tolerance of ±0.05 mm is recommended.
4. 1.0 N placement force is recommended. Too much placement force can lead to squeezed out solder paste and cause solder joints to short. Too low placement force can lead to insufficient contact between package and solder paste that could cause open solder joints or badly centered packages.
5. To improve the package placement accuracy, a bottom side optical control should be performed with a high resolution tool.
6. For assembly, a perfect supporting of the PCB (all the more on flexible PCB) is recommended during solder paste printing, pick and place and reflow soldering by using optimized tools.

3.4 PCB design preference

1. To control the solder paste amount, the closed via is recommended instead of open vias.
2. The position of tracks and open vias in the solder area should be well balanced. A symmetrical layout is recommended, to avoid any tilt phenomena caused by asymmetrical solder paste due to solder flow away.

3.5 Reflow profile

Figure 15. ST ECOPACK® recommended soldering reflow profile for PCB mounting



Note: Minimize air convection currents in the reflow oven to avoid component movement. Maximum soldering profile corresponds to the latest IPC/JEDEC J-STD-020.

4 Ordering information

Figure 16. Ordering information scheme

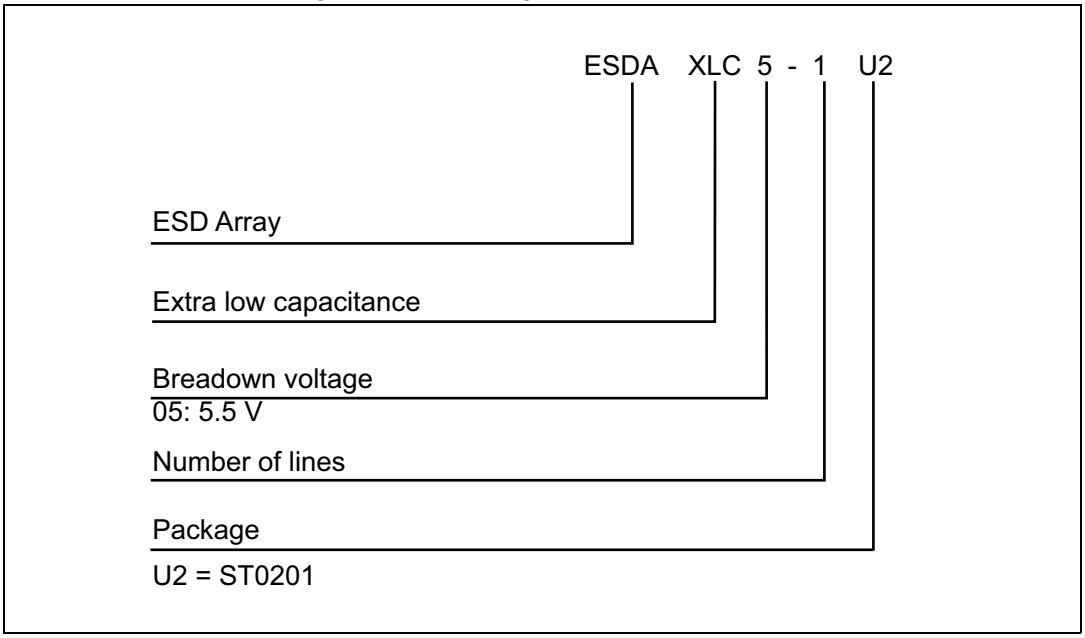


Table 4. Ordering information

Order code	Marking	Weight	Base qty	Delivery mode
ESDAXLC5-1U2	D2(1)	0.124 mg	15000	Tape and reel

1. The marking can be rotated by 180° to differentiate assembly location

5 Revision history

Table 5. Document revision history

Date	Revision	Changes
25-Jan-2016	1	Initial release.