

Absolute Maximum Ratings - NPN (Q1) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	60	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Collector Current	Ic	200	mA

Absolute Maximum Ratings - PNP (Q2) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	-40	V
Collector-Emitter Voltage	V_{CEO}	-40	V
Emitter-Base Voltage	V_{EBO}	-5.0	V
Collector Current	I _C	-200	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ hetaJA}$	417	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	°C

Note: 5. Device mounted on FR-4 PCB with minimum recommended pad layout.

ESD Rating (Note 6)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	200	V	В

Note: 6. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Thermal Characteristics and Derating Information

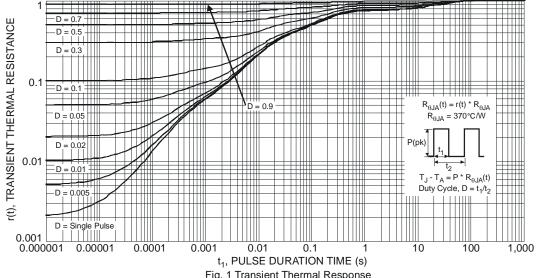
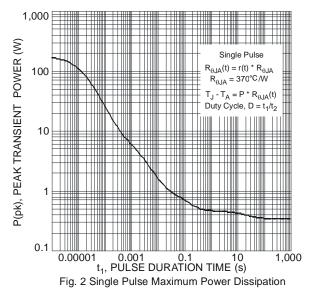


Fig. 1 Transient Thermal Response



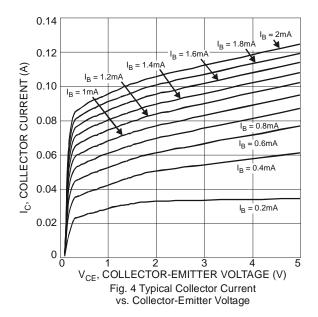
0.4 P_D, POWER DISSIPATION (W) 1.0 0.0 1.0 60 80 100 120 140 160 T_A, AMBIENT TEMPERATURE (°C)

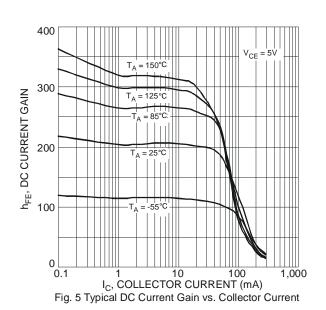
Fig. 3 Power Dissipation vs. Ambient Temperature



Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)					
Collector-Base Breakdown Voltage	BV _{CBO}	60	_	V	$I_C = 10\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 7)	BV_{CEO}	40	_	V	$I_C = 1.0 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	6.0	_	V	$I_E = 10\mu A, I_C = 0$
Collector Cutoff Current	I _{CEX}		50	nA	$V_{CE} = 30V$, $V_{EB(OFF)} = 3.0V$
Base Cutoff Current	I_{BL}		50	nA	$V_{CE} = 30V, V_{EB(OFF)} = 3.0V$
ON CHARACTERISTICS (Note 7)					
		40	_		$I_C = 100\mu A, V_{CE} = 1.0V$
		70	_		$I_C = 1.0 \text{mA}, V_{CE} = 1.0 \text{V}$
DC Current Gain	h_{FE}	100	300		$I_C = 10 \text{mA}, V_{CE} = 1.0 \text{V}$
		60	_		$I_C = 50 \text{mA}, V_{CE} = 1.0 \text{V}$
		30			$I_C = 100 \text{mA}, V_{CE} = 1.0 \text{V}$
Collector-Emitter Saturation Voltage	V05(04T)		0.20	V	$I_C = 10mA, I_B = 1.0mA$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}		0.30	V	$I_C = 50 \text{mA}, I_B = 5.0 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(SAT)}	0.65	0.85	V	$I_C = 10mA$, $I_B = 1.0mA$
9		_	0.95	V	$I_C = 50 \text{mA}, I_B = 5.0 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{OBO}	_	4.0	pF	$V_{CB} = 5.0V$, $f = 1.0MHz$, $I_E = 0$
Input Capacitance	C _{IBO}	_	8.5	pF	$V_{EB} = 0.5V$, $f = 1.0MHz$, $I_{C} = 0$
Input Impedance	h _{IE}	1.0	10	kΩ	
Voltage Feedback Ratio	h _{RE}	0.5	8.0	x 10 ⁻⁴	V _{CE} = 10V, I _C = 1.0mA,
Small Signal Current Gain	h _{FE}	100	400	_	f = 1.0kHz
Output Admittance	hoE	1.0	40	μs	
Current Gain-Bandwidth Product	f _T	300	_	MHz	$V_{CE} = 20V, I_{C} = 10mA,$ f = 100MHz
SWITCHING CHARACTERISTICS					
Delay Time	t_D	_	35	ns	$V_{CC} = 3.0V, I_{C} = 10mA,$
Rise Time	t _R	_	35	ns	$V_{BE(OFF)} = -0.5V, I_{B1} = 1.0mA$
Storage Time	ts	_	200	ns	$V_{CC} = 3.0V, I_{C} = 10mA,$
Fall Time	t _F	_	50	ns	$I_{B1} = -I_{B2} = 1.0 \text{mA}$

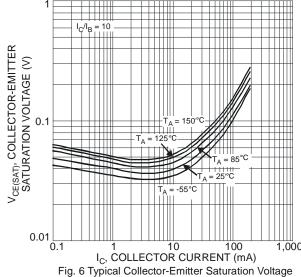
Note: 7. Short duration pulse test used to minimize self-heating effect.



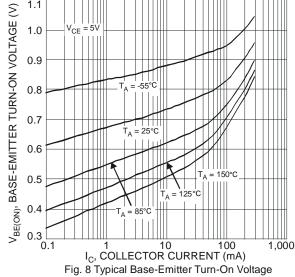












vs. Collector Current

10 T_A = 25°C Single, Non-Repetitive Pulse I_C, COLLECTOR CURRENT (A) 1 0.1 0.001 V_{CE}, COLLECTOR EMITTER CURRENT (V) 100 Fig. 10 Safe Operation Area (NPN)

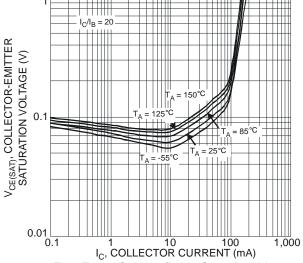


Fig. 7 Typical Collector-Emitter Saturation Voltage vs. Collector Current

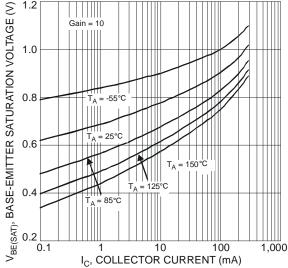


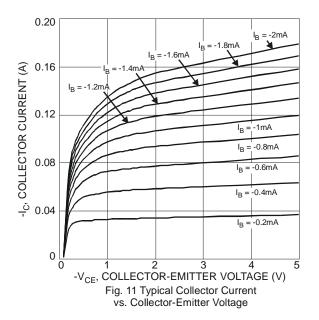
Fig. 9 Typical Base-Emitter Saturation Voltage vs. Collector Current

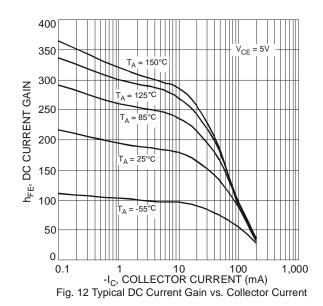


Electrical Characteristics - PNP (Q2) (@T_A = +25°C, unless otherwise specified.)

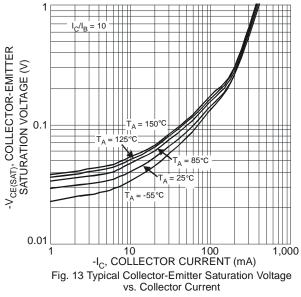
Characteristic	Symbol	Min	Max	Unit	Test Condition	
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV_{CBO}	-40	_	V	$I_C = -10\mu A, I_E = 0$	
Collector-Emitter Breakdown Voltage (Note 8)	BV_{CEO}	-40	_	V	$I_C = -1.0 \text{mA}, I_B = 0$	
Emitter-Base Breakdown Voltage	BV_{EBO}	-5.0	_	V	$I_E = -10\mu A, I_C = 0$	
Collector Cutoff Current	I _{CEX}		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -3.0V$	
Collector Cuton Current	I _{CBO}		-50	nA	$V_{CE} = -30V, I_{E} = 0$	
Base Cutoff Current	I_{BL}		-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -3.0V$	
ON CHARACTERISTICS (Note 8)						
		60	_		$I_C = -100\mu A, V_{CE} = -1.0V$	
		80			$I_C = -1.0 \text{mA}, V_{CE} = -1.0 \text{V}$	
DC Current Gain	hFE	100	300	_	$I_C = -10 \text{mA}, V_{CE} = -1.0 \text{V}$	
		60	_		$I_C = -50 \text{mA}, V_{CE} = -1.0 \text{V}$	
		30	_		$I_C = -100 \text{mA}, V_{CE} = -1.0 \text{V}$	
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	-0.25		$I_C = -10mA$, $I_B = -1.0mA$	
Concotor Enniter Cutaration Voltage	V CE(SAT)		-0.40	•	$I_C = -50 \text{mA}, I_B = -5.0 \text{mA}$	
Base-Emitter Saturation Voltage	V _{BE(SAT)}	-0.65	-0.85	V	$I_C = -10 \text{mA}, I_B = -1.0 \text{mA}$	
· ·	VBE(SAT)	-0.95	V	$I_C = -50 \text{mA}, I_B = -5.0 \text{mA}$		
SMALL SIGNAL CHARACTERISTICS				•		
Output Capacitance	C _{OBO}	_	4.5	pF	$V_{CB} = -5.0V$, $f = 1.0MHz$, $I_E = 0$	
Input Capacitance	C _{IBO}	_	10	pF	$V_{EB} = -0.5V$, $f = 1.0MHz$, $I_{C} = 0$	
Input Impedance	h _{IE}	2.0	12	kΩ		
Voltage Feedback Ratio	h_{RE}	0.1	10	x 10 ⁻⁴	$V_{CE} = -10V, I_{C} = -1.0mA,$	
Small Signal Current Gain	hFE	100	400	_	f = 1.0kHz	
Output Admittance	h _{OE}	3.0	60	μS	1	
Current Gain-Bandwidth Product	f _T	300	_	MHz	$V_{CE} = -20V, I_{C} = -10mA,$ f = 100MHz	
SWITCHING CHARACTERISTICS						
Delay Time	t_D		35	ns	$V_{CC} = -3.0V, I_{C} = -10mA,$	
Rise Time	t _R		35	ns	$V_{BE(OFF)} = 0.5V, I_{B1} = -1.0mA$	
Storage Time	ts	_	225	ns	$V_{CC} = -3.0V, I_{C} = -10mA,$	
Fall Time	t _F	_	75	ns	$I_{B1} = -I_{B2} = -1.0 \text{mA}$	

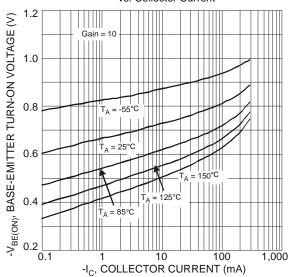
Note: 8. Short duration pulse test used to minimize self-heating effect.

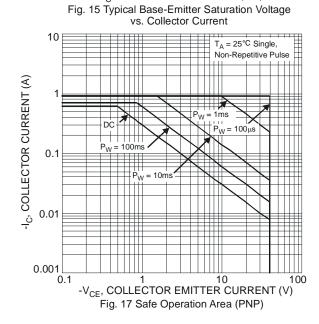


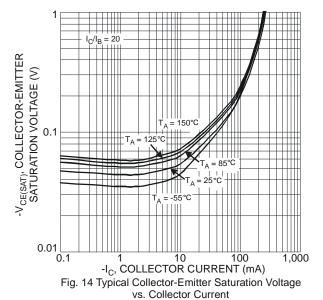


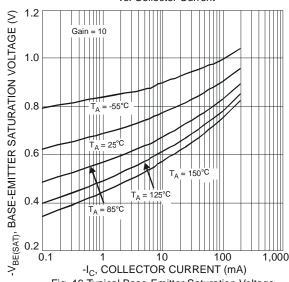










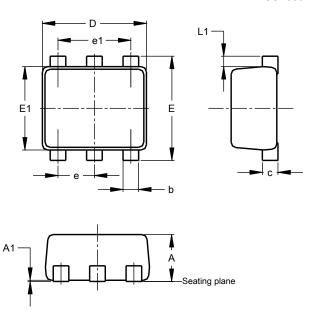




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT963

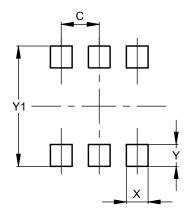


	SOT963					
Dim	Min	Max	Тур			
Α	0.40	0.50	0.45			
A1	0.00	0.05				
b	0.10	0.20	0.15			
C	0.120	0.180	0.150			
D	0.95	1.05	1.00			
Е	0.95	1.05	1.00			
E1	0.75	0.85	0.80			
е			0.35			
e1			0.70			
L1	0.05	0.15	0.10			
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT963



Dimensions	Value			
Dilliensions	(in mm)			
С	0.350			
Х	0.200			
Υ	0.200			
Y1	1.100			



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