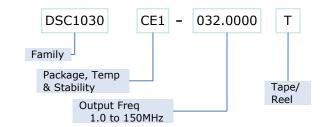


Absolute Maximum Ratings¹

Item	Min.	Max	Unit	Condition		
Input Voltage	-0.3	VDD+0.3	V			
Junction Temp	-	+150	°C			
Storage Temp	-55	+150	°C			
Soldering Temp	-	+260	°C	40 sec max.		
ESD	-		V			
НВМ		2000				
ММ		200				
CDM		500				



^{*} See Ordering Information for details

Ordering Code

Recommended Operating Conditions

Parameter	Symbol	Range
Supply Voltage	V_{DD}	2.7 to 3.3V
Output Load	Z_L	R>10KΩ, C≤15pF
Operating Temperature Option 1 Option 2 Option 3	Т	-40 - +85 °C -20 - +70 °C 0 - +70 °C

Specifications

Parameter	Symbol	Condition		Min.	Typ.	Max.	Unit
Frequency	f_0	Single Frequency		1		150	MHz
Frequency Tolerance Option 1 Option 2 Option 3	Δf	-40°C to +85°C -20°C to +70°C 0°C to +70°C				±25,±50 ±25,±50 ±25,±50	ppm
Supply Current, no load	${ m I}_{ m DD}$	$C_L=0p$ $R_L=\infty$ $T=25^{\circ}$ C	1 to 40MHz 40 to 80MHz 80 to 125MHz 125 to 150MHz		3 4 5 6	10	mA
Supply Current, standby	${ m I}_{ m DD}$	T=25°C				1.0	uA
Output Logic Levels Output logic high Output logic low	V _{OH} V _{OL}	C _L =15pF		0.8*V _{DD}		- 0.2*V _{DD}	Volts
Output Transition time Rise Time Fall Time	t _R t _F	C _L =15pF; T=25°C 20%/80%*V _{DD}			1.3 1.3	2 2	ns
Output Startup Time ²	t _{su}	T=25°C			1.5	3	ms
Output Disable Time	t _{DA}				20	100	ns
Output Duty Cycle	SYM			45		55	%
Input Logic Levels Input logic high Input logic low	V _{IH} V _{IL}			0.75*V _{DD}		- 0.25* V _{DD}	Volts
Jitter, Cycle to Cycle	J _{CC}	F	= 100MHz ³		95		ps

Notes:

- 1. Absolute maximum ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated beyond these limits.
- 2. t_{SU} is time to stable output frequency after V_{DD} is applied. t_{SU} and t_{EN} (after EN is asserted) are identical values.
- 3. See typical cycle to cycle jitter graph for frequency dependence.

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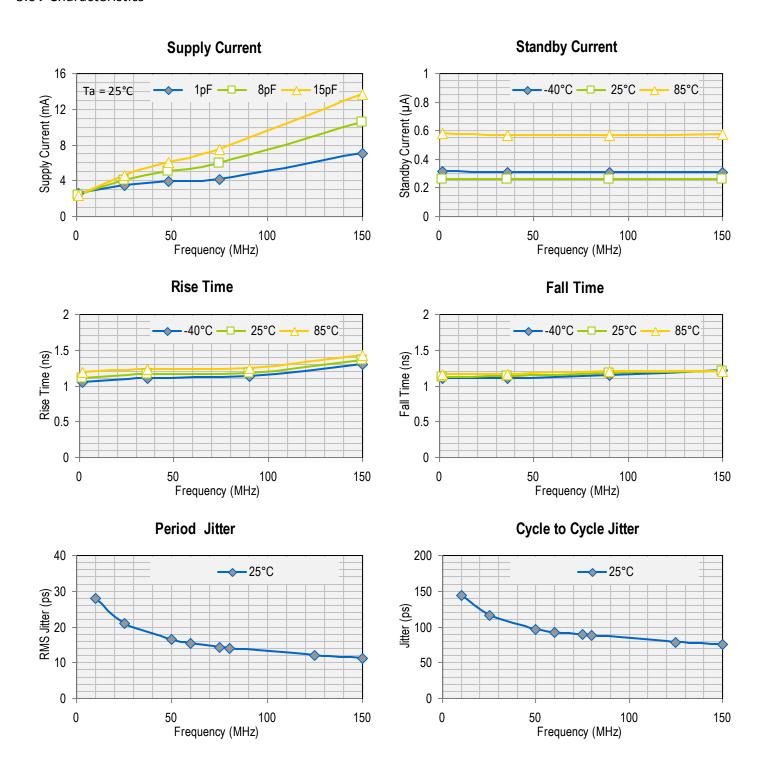
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Nominal Performance Characteristics

3.0V Characteristics

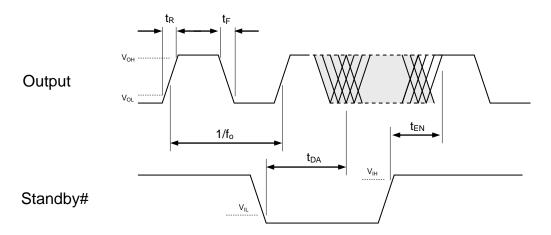


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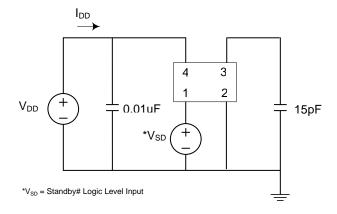
Output Waveform



Standby Function

Standby# (pin 1)	Output (pin 3)		
Hi Level	Output ON		
Open (no connect)	Output ON		
Low Level	High Impedance		

Test Circuit



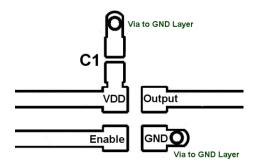
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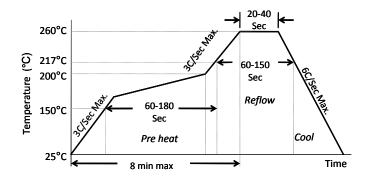
Downloaded from Arrow.com.

Board Layout (recommended)



3.0V

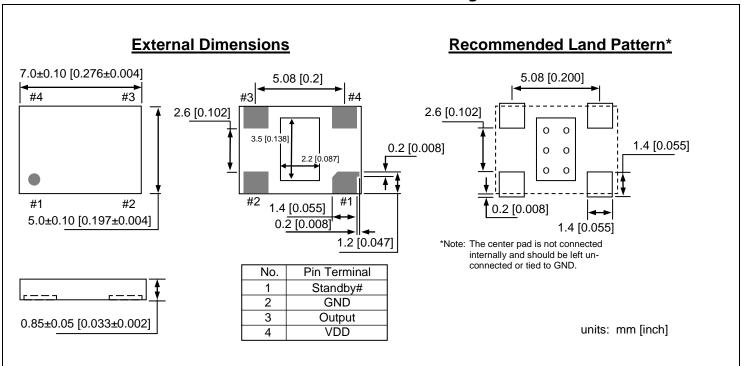
Solder Reflow Profile



MSL 1 @ 260°C refer to JSTD-020C				
Ramp-Up Rate (200°C to Peak Temp)	3°C/Sec Max.			
Preheat Time 150°C to 200°C	60-180 Sec			
Time maintained above 217°C	60-150 Sec			
Peak Temperature	255-260°C			
Time within 5°C of actual Peak	20-40 Sec			
Ramp-Down Rate	6°C/Sec Max.			
Time 25°C to Peak Temperature	8 min Max.			

Package Dimensions

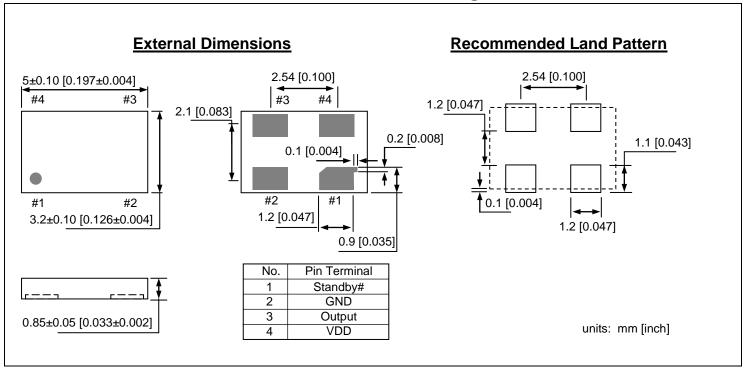
7.0 x 5.0 mm Plastic Package



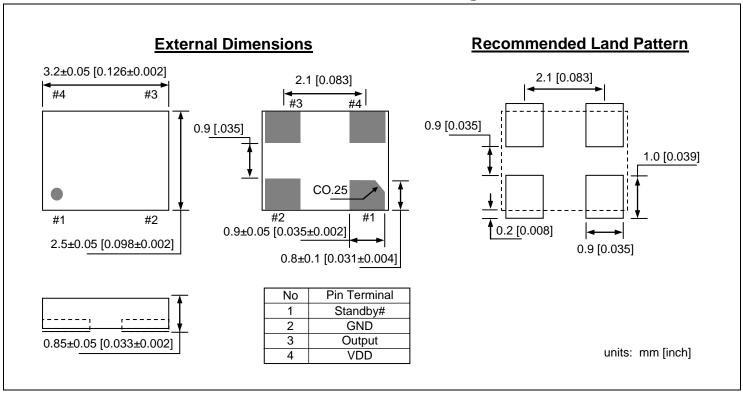
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5.0 x 3.2 mm Plastic Package



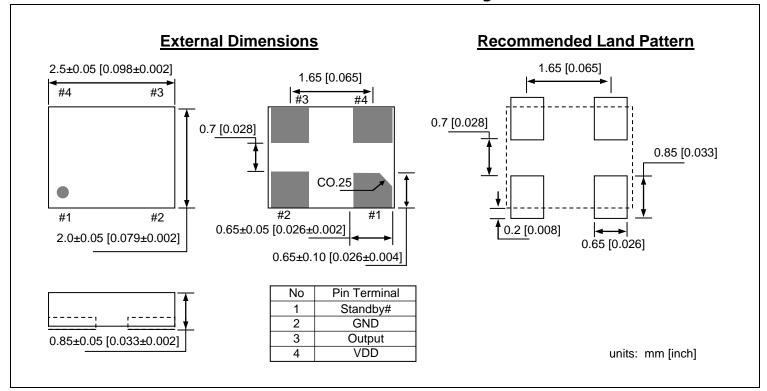
3.2 x 2.5 mm Plastic Package



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2.5 x 2.0 mm Plastic Package



Ordering Information

DSC1030

DSC1030 PTS - xxx.xxxx T

PART NUMBERING GUIDE				
Package (Plastic QFN)	Temperature	Stability	Frequency	Packing Option
P=A: 7.0x5.0mm P=B: 5.0x3.2mm P=C: 3.2x2.5mm P=D: 2.5x2.0mm	T=C: $0^{\circ} \sim +70^{\circ} \text{ C}$ T=E: $-20^{\circ} \sim +70^{\circ} \text{ C}$ T=I: $-40^{\circ} \sim +85^{\circ} \text{ C}$	S=1: ±50ppm S=2: ±25ppm	XXX.XXXX (4 decimal places)	Blank: Tubes T: Tape & Reel

Example: DSC1030CE1-123.0000T

The example part number above is a 123.0000MHz oscillator in Plastic 3.2x2.5mm package, with $\pm 50ppm$ stability over an operating temperature of -20 to +70°C, shipped in Tape and Reel. The reel size (7" or 13" diameter) will be determined by the factory based on quantity.

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