Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	20	V
RMS Reverse Voltage	$V_{R(RMS)}$	14	V
Average Forward Current	$I_{F(AV)}$	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I_{FSM}	50	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_D	1.67	W
Power Dissipation (Note 7)	P_D	556	mW
Thermal Resistance Junction to Ambient (Note 6)	$R_{\theta JA}$	60	°C/W
Thermal Resistance Junction to Ambient (Note 7)	$R_{\theta JA}$	180	°C/W
Thermal Resistance Junction to Soldering (Note 8)	$R_{\theta JS}$	10	°C/W
Operating Temperature Range	T_J	-55 to +125	°C
Storage Temperature Range	T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 10)	$V_{(BR)R}$	20	—	—	V	I_R = 1.0mA
Forward Voltage	V_F	— — —	0.20 0.30 0.32	— — 0.36	V	I_F = 0.1A I_F = 0.7A I_F = 1.0A
Leakage Current (Note 10)	I_R	— —	0.26 —	— 1.0	mA	V_R = 5V, T_A = +25°C V_R = 20V, T_A = +25°C
Total Capacitance	C_T	—	75	—	pF	V_R = 10V, f = 1.0MHz

Notes:
6. Part mounted on 50.8mm X 50.8mm GETEK board with 25.4mm X 25.4mm copper pad, 25% anode, 75% cathode. T_A = +25°C.
7. Part mounted on FR-4 board with 1.8mm X 2.5mm cathode and 1.8mm X 1.2mm anode, 1 oz. copper pads. T_A = +25°C.
8. Theoretical $R_{\theta JS}$ calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
9. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see *EU Directive 2002/95/EC Annex Notes.*
10. Short duration pulse test used to minimize self-heating effect.

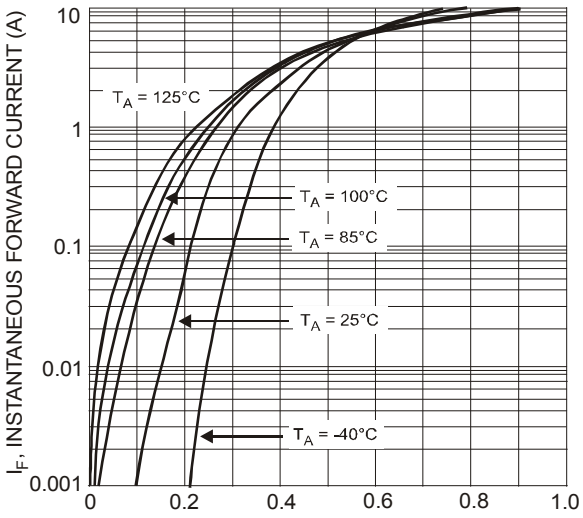


Fig. 1 Typical Forward Characteristics

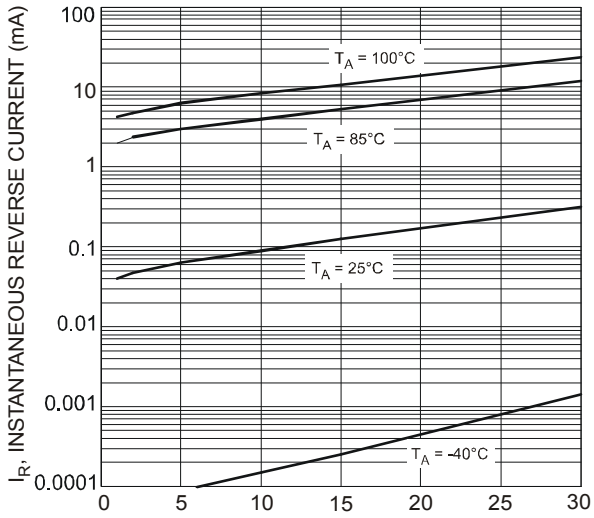


Fig. 2 Typical Reverse Characteristics

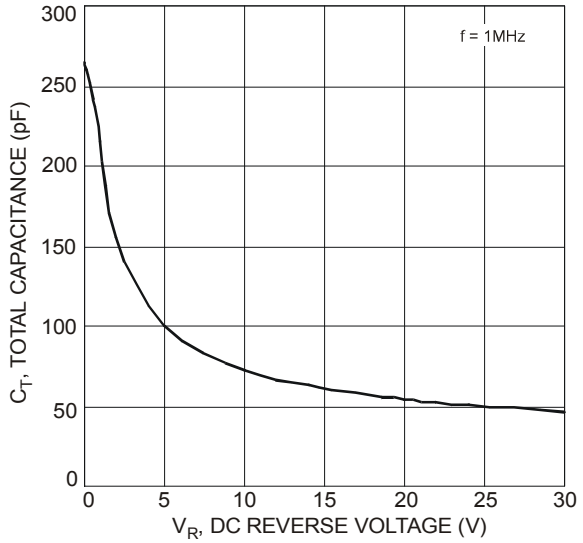
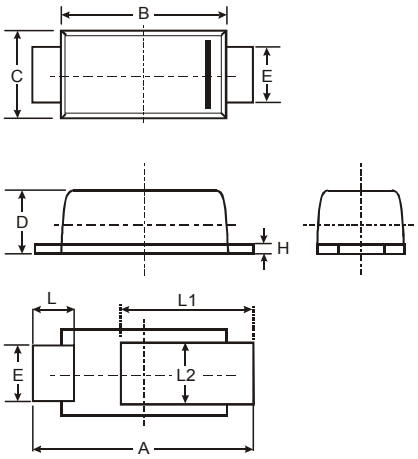


Fig. 3 Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

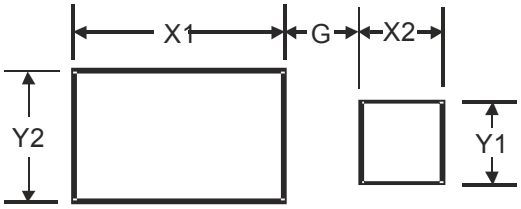
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



PowerDI®123			
Dim	**Min**	**Max**	**Typ**
A	3.50	3.90	3.70
B	2.60	3.00	2.80
C	1.63	1.93	1.78
D	0.93	1.00	0.98
E	0.85	1.25	1.00
H	0.15	0.25	0.20
L	0.55	0.75	0.65
L1	1.80	2.20	2.00
L2	0.95	1.25	1.10
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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