BTA20 Series

Tables 4: Electrical Characteristics ($T_j = 25^{\circ}C$, unless otherwise specified)

Symbol	Test Conditions	Quadrant		BTA20		Unit
	rest conditions	Quadrant		BW	CW	Unit
I _{GT} (1)		ALL	MIN.	2	1	mΛ
'GT (')	$V_D = 12 V$ $R_L = 33 \Omega$	ALL	MAX.	50	35	mA
V _{GT}		ALL	MAX.	1.5		V
V_{GD}	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $T_j = 125^{\circ}\text{C}$	ALL	MIN.	0.2		V
I _H (2)	I _T = 500 mA gate open		MAX.	75	50	mA
		I - III	TYP.	50	-	
IL	$I_{G} = 1.2 I_{GT}$	II	111.	90	-	mA
		I - II - III	MAX.	-	80	
dV/dt (2) $V_D = 67 \% V_{DRM}$ gate open	T _i = 125°C	TYP.	750	500	V/µs	
	AD = 6, % ADRW date oben	1] = 120 0	MIN.	500	250	ν/μ3
(dV/dt)c (2)	(dl/dt)c = 20 A/ms	T _j = 125°C	TYP.	36	22	V/µs
			MIN.	18	11	

Table 5: Static Characteristics

Symbol	Test Conditions			Value	Unit
V _{TM} (2)	$I_{TM} = 28 \text{ A}$ $t_p = 380 \mu\text{s}$	$T_j = 25^{\circ}C$	MAX.	1.70	V
I _{DRM}	V _{DRM} = V _{RRM}	T _j = 25°C	MAX.	10	μΑ
I _{RRM}	VDRM — VRRM	T _j = 125°C	IVIAA.	3	mA

Note 1: minimum I_{GT} is guaranted at 5% of I_{GT} max. Note 2: for both polarities of A2 referenced to A1.

Table 6: Thermal resistance

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Junction to case (AC)	2.1	°C/W
R _{th(j-c)}	Junction to case (DC)	2.8	C/VV
R _{th(j-a)}	Junction to ambient	60	°C/W

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Figure 1: Maximum power dissipation versus RMS on-state current (full cycle)

P(W)

25

20 $\alpha = 120^{\circ}$ $\alpha = 30^{\circ}$ $\alpha = 30^{\circ}$ $\alpha = 30^{\circ}$ $\alpha = 100^{\circ}$ $\alpha = 100^{\circ}$

Figure 3: RMS on-state current versus case temperature (full cycle)

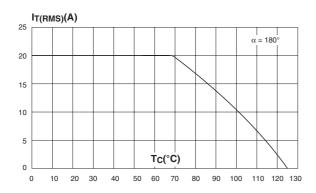


Figure 5: On-state characteristics (maximum values)

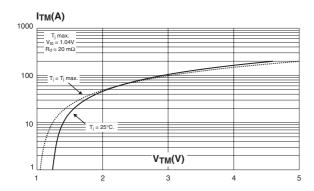


Figure 2: Correlation between maximum RMS power dissipation and maximum allowable temperatures (Tamb and Tcase) for different thermal resistances heatsink + contact

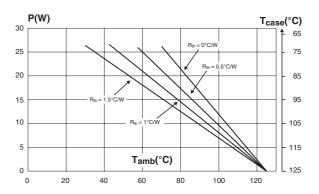


Figure 4: Relative variation of thermal impedance versus pulse duration

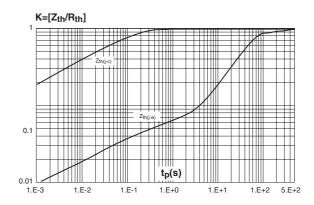
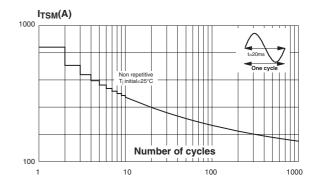


Figure 6: Non repetitive surge peak on-state current versus number of cycles



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Figure 7: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_{\rm p}$ < 10 ms and corresponding value of l^2t

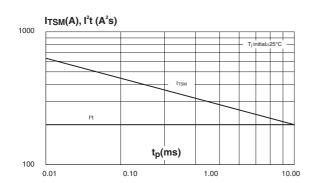


Figure 8: Relative variation of gate trigger current and holding current versus junction temperature

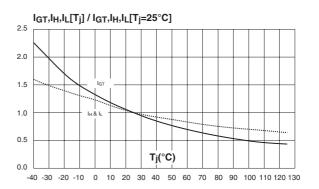


Figure 9: Ordering Information Scheme

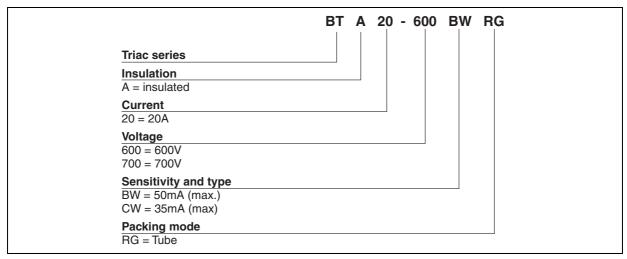


Table 7: Product Selector

Part Numbers	Voltage (xxx)		Sensitivity	Туре	Package	
Tart Nambers	600 V	700 V	Constitution	Турс	i ackage	
BTA20-xxxBWRG	Х	Χ	50 mA	Snubberless	TO-220AB Ins.	
BTA20-xxxCWRG	Х	Х	35 mA	O I I I D D E I I E S S	10 22000 1113.	

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Inches

Тур.

0.147

Max.

0.625

0.551

0.409

0.034

0.051

0.181

0.027

0.107

0.106

0.259 0.151

0.116

0.066

0.066

0.102

Figure 10: TO-220AB Insulated Package Mechanical Data

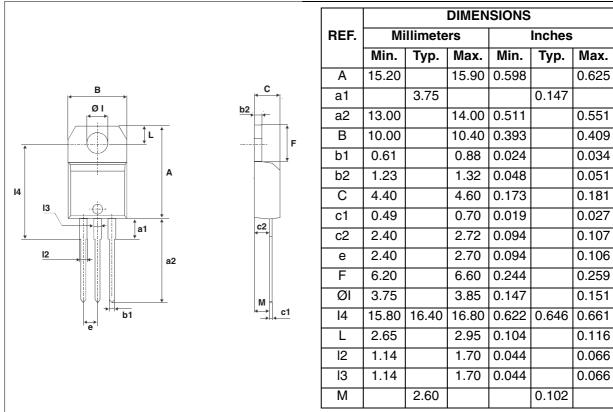


Table 8: Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
BTA20-600BWRG	BTA20-600BW				
BTA20-600CWRG	BTA20-600CW	TO-220AB Ins.	2.3 g	50	Tube
BTA20-700BWRG	BTA20-700BW	10-22000 1113.	2.5 g	30	Tube
BTA20-700CWRG	BTA20-700CW				

Table 9: Revision History

Date	Revision	Description of Changes
Sep-2001	1A	First issue.
08-Feb-2006	2	TO-220AB Ins. delivery mode changed from bulk to tube.

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