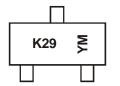


Marking Information

SOT23



K29 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016)M = Month (ex: 9 = September)

K28

SC59

K28 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016)M = Month (ex: 9 = September)

Date Code Key

Year	201	3	2014		2015	20)16	2017		2018		2019
Code	Α		В		С		D	Е		F		G
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	600	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	50 40	mA
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	70 55	mA
Continuous Drain Current (Note 5) V _{GS} = 5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	45 35	mA
Continuous Drain Current (Note 6) V _{GS} = 5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	65 50	mA
Pulsed Drain Current @ T _{SP} = +25°C (Note 7)	I _{DM}	0.16	А		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation, @T _A = +25°C (Note 5)	P _D	0.61	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 5)	$R_{ heta JA}$	204	°C/W
Power Dissipation, @T _A = +25°C (Note 6)	P_{D}	1.25	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 6)	$R_{\theta JA}$	100	°C/W
Operating and Storage Temperature Range	T_{J}, T_{STG}	-55 to +150	°C

Notes:

- 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- 6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2 oz. Copper, single sided.
 7. Repetitive rating, pulse width limited by junction temperature, 10µs pulse, duty cycle = 1%.



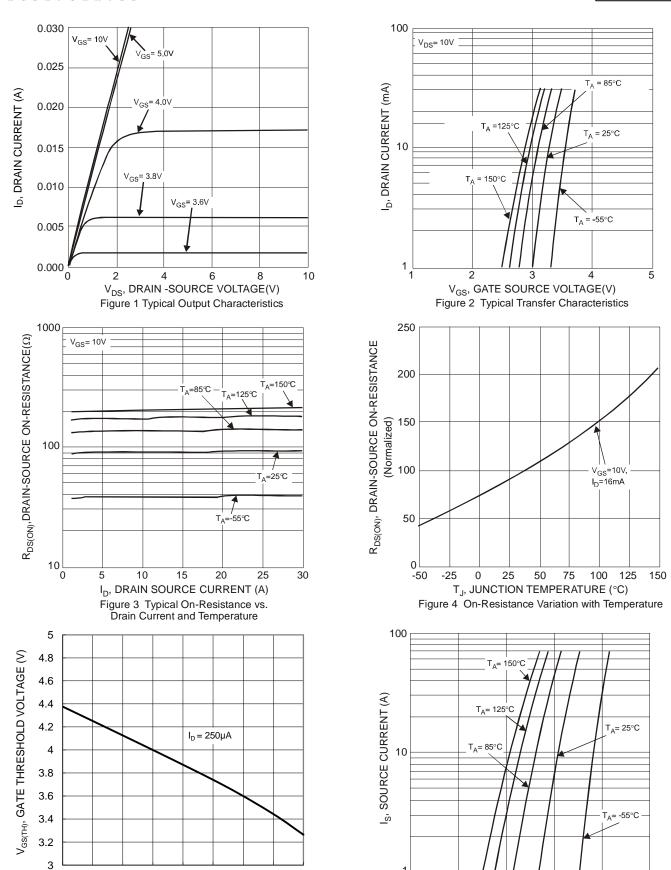
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	600	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	0.1	μΑ	$V_{DS} = 600V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	3	_	4.5	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	D	_	80	160	Ω	$V_{GS} = 10V, I_D = 16mA$
Static Diani-Source Off-Resistance	R _{DS(ON)}	_	95	190	12	$V_{GS} = 5.0V, I_D = 16mA$
Forward Transfer Admittance	Y _{fs}	_	76	_	mS	$V_{DS} = 10V, I_D = 16mA$
Diode Forward Voltage	V _{SD}	_	_	1.5	V	$V_{GS} = 0V$, $I_S = 16mA$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	21.8	_		
Output Capacitance	Coss	_	2.2	_	pF	$V_{DS} = 25V$, $V_{GS} = 0V$, $f = 1.0MHz$
Reverse Transfer Capacitance	C _{rss}	_	0.3	_		
Total Gate Charge	Qg	_	1.08	_	nC	$V_{GS} = 10V, V_{DD} = 300V,$ $I_{D} = 0.01A$
Gate-Source Charge	Qgs	_	0.08	_		
Gate-Drain Charge	Q_{gd}	_	0.50	_		
Turn-On Delay Time	t _{D(ON)}	_	5.0	_	ns	
Turn-On Rise Time	t _R	_	7.2	_	ns ns	$V_{DD} = 300V, V_{GS} = 10V,$ $R_{GEN} = 6\Omega,$
Turn-Off Delay Time	t _{D(OFF)}	_	28.7	_		
Turn-Off Fall Time	t _F	_	168	_	ns	$I_D = 10$ mA
Reverse Recovery Time	t _{RR}	_	131	_	ns	V _R =300V, I _F =0.016A,
Reverse Recovery Charge	Q _{RR}	_	32	_	nC	di/dt = 100A/µs

Notes:

- 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to production testing.





-25

-50

25

50

 T_A , AMBIENT TEMPERATURE (°C)

Figure 5. Gate Threshold Variation vs. Ambient Temperature

75

100

125

150

0.1

1.1

 V_{SD} , SOURCE-DRAIN VOLTAGE (V)

Figure 6 Diode Forward Voltage vs. Current



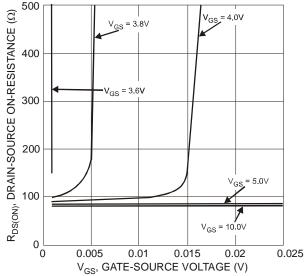


Figure 7 Typical On-Resistance vs. Drain Current and Gate Voltage

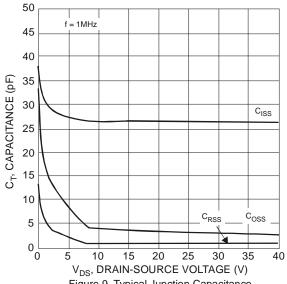


Figure 9 Typical Junction Capacitance

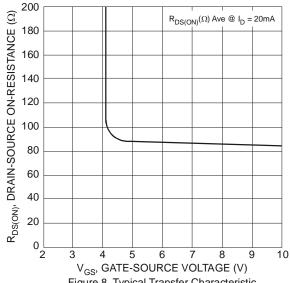
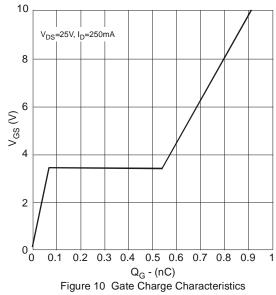


Figure 8 Typical Transfer Characteristic



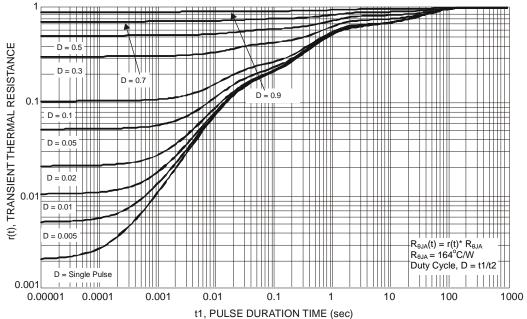


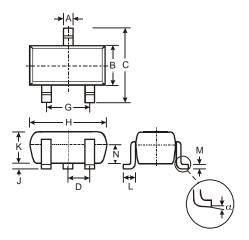
Figure 11 Transient Thermal Resistance



Package Outline Dimensions

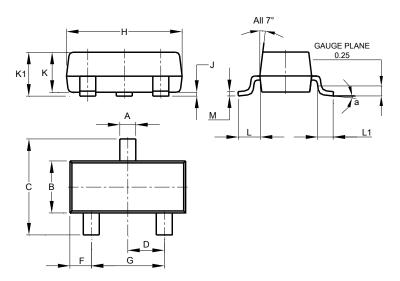
Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59



SC59					
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
C	2.70	3.00	2.80		
D	-	-	0.95		
G	-	-	1.90		
H	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
Ν	0.70	0.80	0.75		
α	0°	8°	-		
All Dimensions in mm					

SOT23

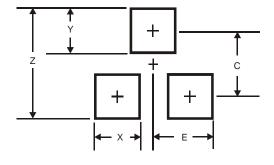


SOT23						
Dim	Min	Max	Тур			
Α	0.37	0.51	0.40			
В	1.20	1.40	1.30			
С	2.30	2.50	2.40			
D	0.89	1.03	0.915			
F	0.45	0.60	0.535			
G	1.78	2.05	1.83			
Ι	2.80	3.00	2.90			
7	0.013	0.10	0.05			
K	0.890	1.00	0.975			
K 1	0.903	1.10	1.025			
٦	0.45	0.61	0.55			
L1	0.25	0.55	0.40			
М	0.085	0.150	0.110			
а	0°	8°				
All	All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SC59



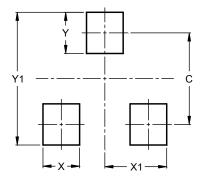
Dimensions	Value (in mm)
Z	3.4
Х	0.8
Y	1.0
С	2.4
F	1 35



Suggested Pad Layout (cont.)

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)			
С	2.0			
Х	0.8			
X1	1.35			
Y	0.9			
Y1	2.9			

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