

## BSR18A PNP General-Purpose Amplifier

## Description

This device is designed as a general-purpose amplifier for switching applications at collector currents of 10  $\mu$ A to 100 mA. Sourced from process 66.



## **Ordering Information**

Part Number	Marking	Package	Packing Method
BSR18A	T92	SOT-23 3L	Tape and Reel

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-40	V
V <sub>CBO</sub>	Collector-Base Voltage	-40	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
Ι <sub>C</sub>	Collector Current - Continuous	-200	mA
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature Range	-55 to +150	°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

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## Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
PD	Total Device Dissipation	350	mW
	Derate Above T <sub>A</sub> = 25°C	2.8	mW/°C
R <sub>θJA</sub>	Thermal Resistance, Junction to Ambient	357	°C/W

Note:

3. Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

## **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	$I_{\rm C} = -10 \ \mu \text{A}, I_{\rm B} = 0$	-40	maxi	V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = -1.0 \text{ mA}, I_{\rm F} = 0$	-40		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm F} = -10 \ \mu \text{A}, I_{\rm C} = 0$	-5.0		V
I <sub>CBO</sub>	Collector Cut-Off Current	$V_{CB} = -30 \text{ V}, I_{F} = 0$	0.0	-50	nA
	Emitter Cut-Off Current	$V_{EB} = -3.0 \text{ V}, I_C = 0$		-50	nA
I <sub>EBO</sub>		$I_{\rm C} = -0.1 \text{ mA}, V_{\rm CE} = -1.0 \text{ V}$	60		
		$I_{\rm C} = -1.0 \text{ mA}, V_{\rm CE} = -1.0 \text{ V}$	80		-
h <sub>FE</sub>	DC Current Gain <sup>(4)</sup>	$I_{\rm C} = -10$ mA, $V_{\rm CE} = -1.0$ V	100	300	-
ΨFE	De Current Gainte	$I_{\rm C} = -50$ mA, $V_{\rm CE} = -1.0$ V	60		-
		$I_{\rm C} = -100 \text{ mA}, V_{\rm CE} = -1.0 \text{ V}$	30		-
V <sub>CE</sub> (sat)	Collector-Emitter Saturation	$I_{\rm C} = -10$ mA, $I_{\rm B} = -1.0$ mA		-0.25	
		$I_{\rm C} = -50$ mA, $I_{\rm B} = -5.0$ mA		-0.40	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	$I_{\rm C} = -10$ mA, $I_{\rm B} = -1.0$ mA	-0.65	-0.85	- V
		$I_{\rm C} = -50$ mA, $I_{\rm B} = -5.0$ mA	0.00	-0.95	
f <sub>T</sub>	Transition Frequency	$I_{C} = -10 \text{ mA}, V_{CE} = -20 \text{ V},$ f = 100 MHz	250		MHz
C <sub>cb</sub>	Collector-Base Capacitance	V <sub>CB</sub> = -5.0 V, I <sub>E</sub> = 0, f = 100 kHz		4.5	pF
C <sub>eb</sub>	Emitter-Base Capacitance	$V_{EB} = -0.5 \text{ V}, \text{ I}_{C} = 0,$ f = 100 kHz		10	pF
h <sub>ie</sub>	Input Impedance	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -1.0 \text{ mA},$ f = 1.0 kHz	2	12	kΩ
h <sub>ie</sub>	Small-Signal Current Gain	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -1.0 \text{ mA},$ f = 1.0 kHz	100	400	D
h <sub>ie</sub>	Output Admittance	$V_{CE} = -10 \text{ V}, \text{ I}_{C} = -1.0 \text{ mA},$ f = 1.0 kHz	3	60	μS
t <sub>d</sub>	Delay Time	I <sub>C</sub> = -10 mA, I <sub>B1</sub> = -1.0 mA,		35	ns
t <sub>r</sub>	Rise Time	V <sub>EB</sub> = -0.5 V		35	ns
t <sub>s</sub>	Storage Time	I <sub>C</sub> = -10 mA,		275	ns
t <sub>f</sub>	Fall Time	$I_{Bon} = I_{Boff} = -1.0 \text{ mA}$		75	ns

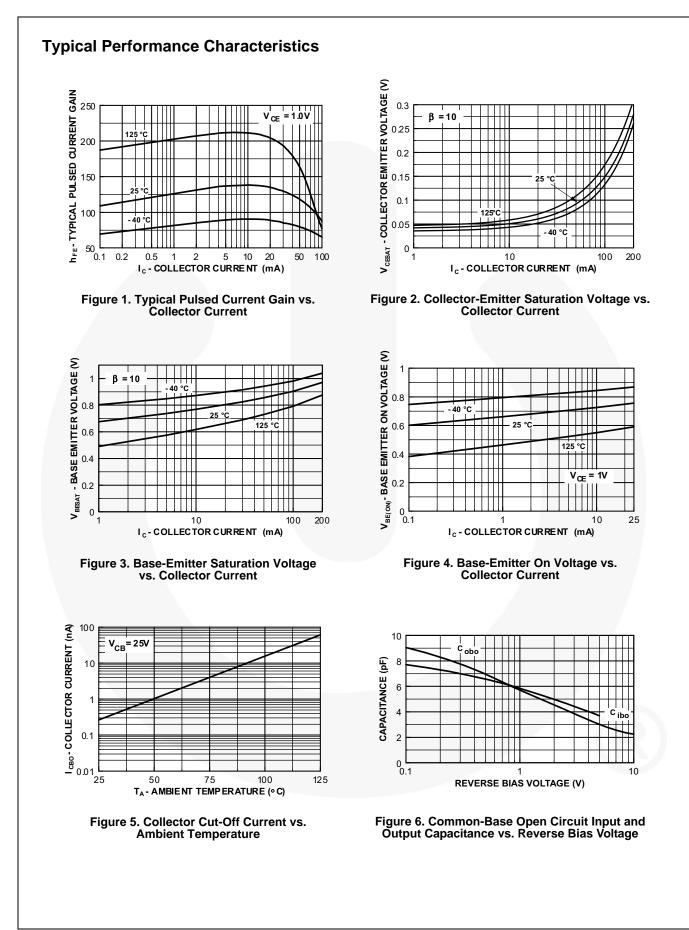
#### Note:

4. Pulse test: pulse width  $300 \ \mu s$ , duty cycle 0.01%.

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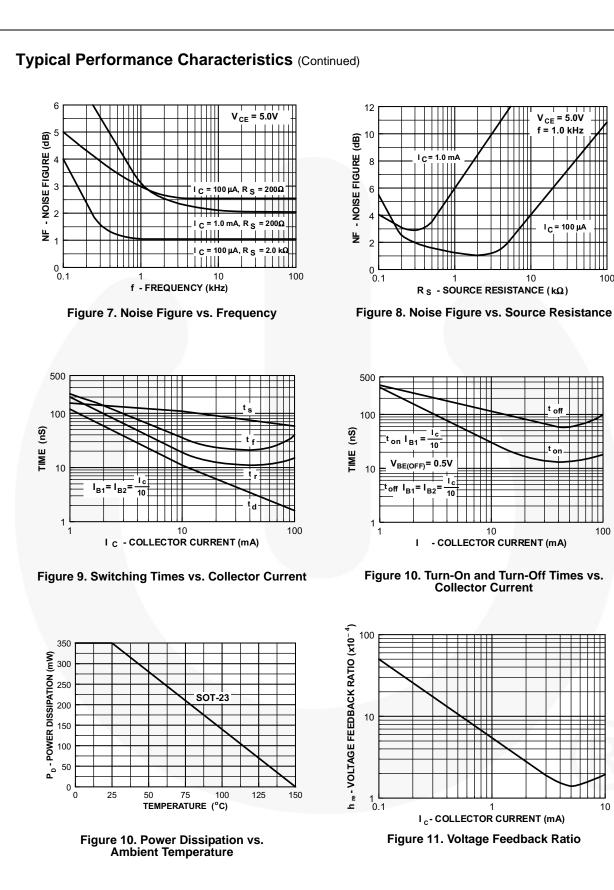
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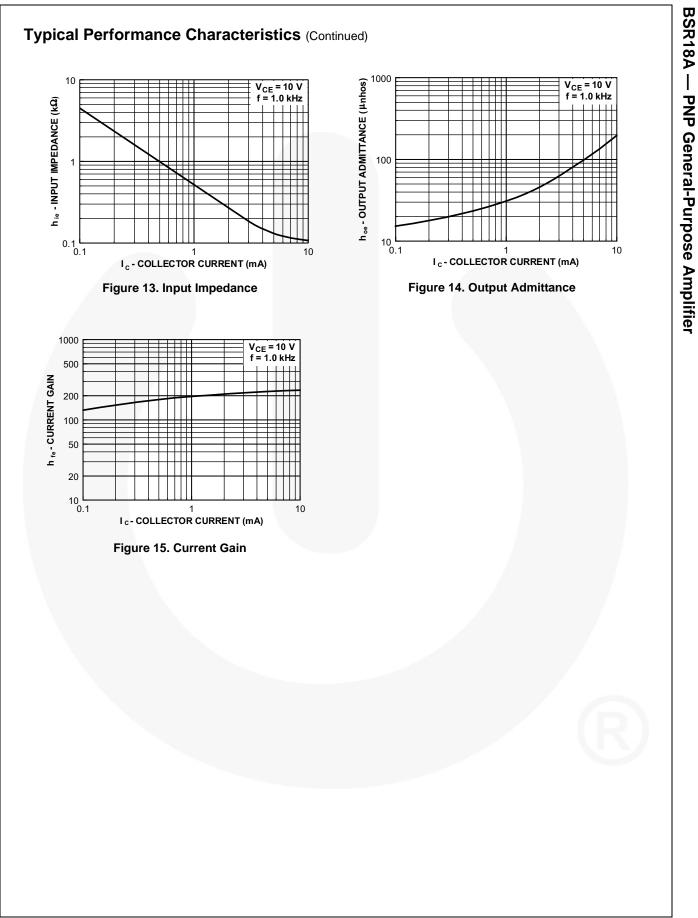
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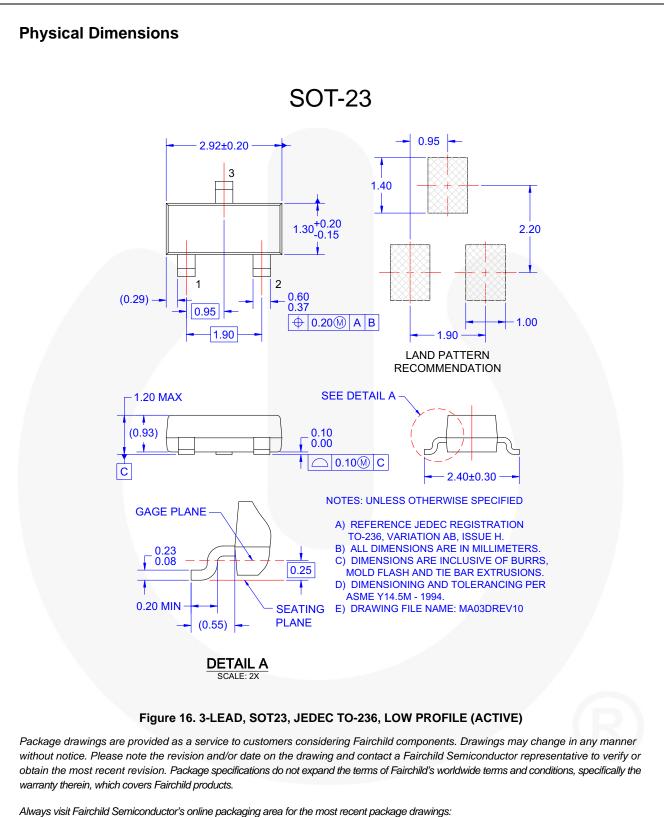
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http://www.fairchildsemi.com/dwg/MA/MA03D.pdf.

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