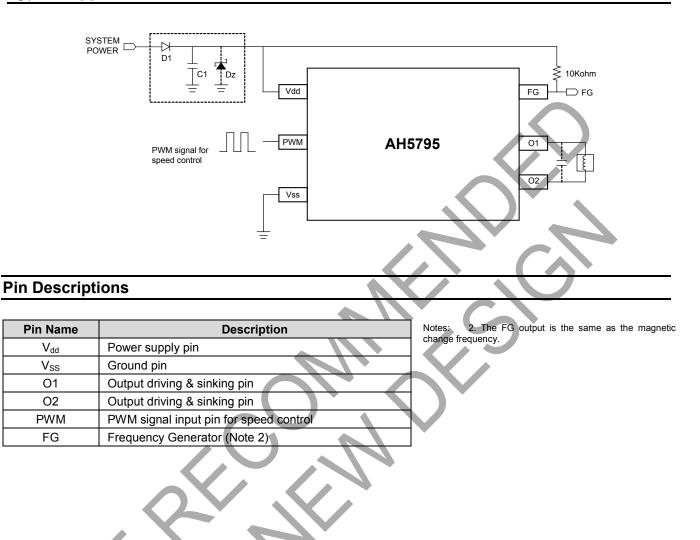




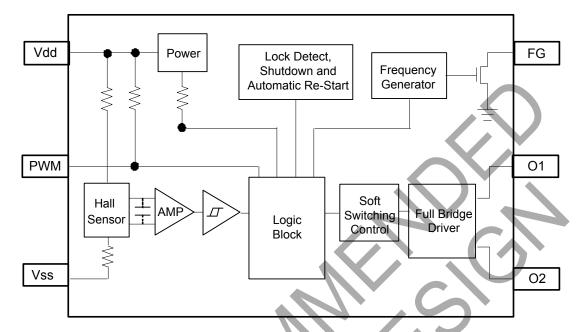
Typical Application Circuit







Functional Block Diagram (Note 3)



Notes: 3. The AH5795 has an open-drain tachometer FG output that follows the magnetic change frequency. Typically a pull-up resistor of 10KΩ is recommended from FG pin to the supply voltage.





Absolute Maximum Ratings (T_A = 25°C, unless otherwise noted, Note 4)

Symbol		Characteristics	Values	Unit
Vdd	Supply voltage		7	V
I _{O(PEAK)}	Maximum Output Current	(Peak)	1000mA	mA
Р	Dower Discinction	TSOT23-6	650	mW
P _D Power Dissipation	Power Dissipation	DFN2020C-6	780 (Note 5)	TIVV
T _{ST}	Storage Temperature Rar	nge	-65 ~ 150	°C
ESD HBM	Human Body Model (HBM	1) ESD Protection	4	kV

Notes: 4. Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at these or any other conditions exceeding those indicated in this specification is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time

 DFN2020C-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a two-layer 2oz. copper FR4 PCB (1.6mm thickness) with no thermal vias in exposed PADs or any copper flood connecting to the landing pattern of the exposed pad.

Recommended Operating Conditions (T_A = 25°C)

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{DD}	Supply Voltage	Operating	1.8	6.0	V
T _A	Operating Ambient Temperature Range	Operating	-40	105	°C

Electrical Characteristics (T_A = 25°C, V_{DD} = 5V)

• • •				-		
Symbol	Characteristics	Conditions	Min	Тур.	Max	Unit
IDD	Supply Current	No Load	-	2.2	-	mA
V	Output Voltage Lligh	I _{OUT} = 300mA	4.70	4.88	-	V
V _{он}	Output Voltage High	I _{OUT} = 500mA	4.5	4.8	-	V
N/		I _{OUT} = 300mA	-	0.12	0.3	V
V _{OL}	Output Voltage Low	l _{ouτ} = 500mA	-	0.2	0.5	V
Voh+Vol	Output voltage of N- and PMOS	I _{OUT} = 300mA		0.3	0.6	V
von+voi	combined	l _{оит} = 500mA		0.5		V
T _{SW}	Output Switching Slope Duration	17Ω load on out1/out2	-	200	-	μs
I _{LEAK}	FG Output Leakage Current		-	-	5	μA
V _{FGOL}	FG Output Voltage Low	I _{FG} = 5mA	-	-	0.4	V
T _{ON}	On Time		350	500	650	ms
R _{DR}	Duty Ratio	T _{OFF} / T _{ON}	-	10	-	
V _{PWMH}	PWM Input H Level	-	0.5 Vdd	-	Vdd	V
V _{PWML}	PWM Input L Level	-	0	-	0.14 Vdd	V
I _{PWMH}	PWM Input current H Level	PWM=Vdd		0		uA
I PWMH	PWM Input current L Level	PWM=GND		-10		uA
F _{PWM}	PWM Input Frequency	-	0.02	-	50	KHz
D _{PWM MIN}	Output minimum duty ratio	Motor rotating;	10%		100	%
	IC junction temperature thermal			175		°C
Tj_sdn_th	shutdown threshold			175		U
ті	IC junction temperature thermal			25		°C
Tj_sdn_hyst	shutdown hysteresis			20		U



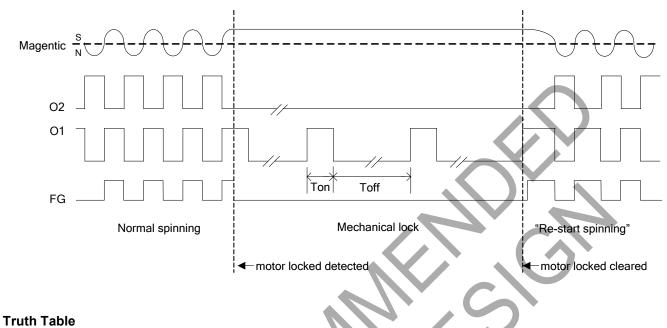
Magnetic Characteristics (T_A = 25°C, Vdd = 1.8V~6V, Note 6)

				(1n	าT = 10 G)
Symbol	Parameter	Min	Тур.	Мах	Unit
Bop	Operate Point	10	25	50	
Brp	Release Point	-50	-25	-10	Gauss
B _{hy}	Hysteresis	-	50		
	gnetic characteristics may vary with supply voltage, operating te	mperature and	after soldering.	\mathbf{N}	
Operating	Characteristics		Δ		
Output Voltage in Volts	O1 RP OFF OP OP Vol Brp 0 Bop Magnetic Flux Density in Gauss	RP Vol	O2 O2 Brp 0 tic Flux Density	OP ON Bop r in Gauss	— V _{он}
4	(DFN2020C-6)		↓↓↓↓ S (TSOT23-6)		





Operating Characteristics (cont. Note 7, 8, 9 and 10)



01	02	PWM	FG
L	Н	Н	
Н	L	H	Н
L	L	L	X (Note 10)

Notes:

- 7. In "Normal spinning, the FG changes its state at each edge of O1.
 8. When the motor locks with South pole at the Hall element, O2 is kept on "L" and O1 is a clock with Ton/Toff ratio. When motor locks with North pole at the Hall element, O1 is kept on "L", O2 is a clock with Ton/Toff ratio.
 9. When "Re-start spinning" occurs, the motor speed ramps up to the "Normal Spinning" speed from zero. Speed ramp-up profile depends on motor or provide the related in the related in
- characteristics.10. X: H or L depends on magnetic pole north or South



Application Note

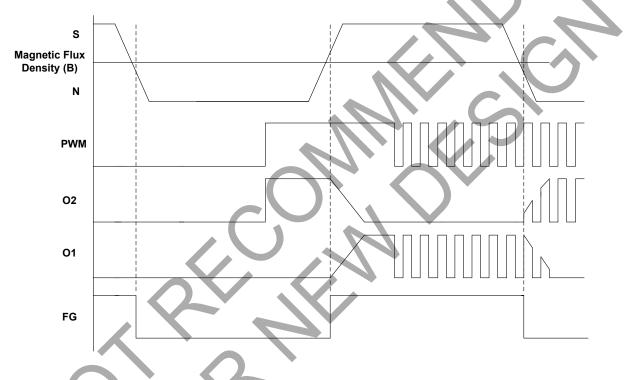
DC Supply voltage speed control

Motor speed can be controlled by varying the Vdd supply voltage while PWM pin is tied to Vdd pin.

For example, with 5V nominal motor, changing supply voltage between 5V to 1.8V, speed can be reduced from 100% to 36% typically.

PWM speed control

Motor speed can also be adjusted by applying a PWM speed control signal into the PWM pin while keeping the Vdd pin at nominal motor voltage. The motor speed is proportional to the PWM signal duty. For example, with 5V nominal motor, Vdd pin is maintained at 5V typical while varying the PWM control signal duty to adjust the motor speed linearly. Figure below shows the output O1 and O2 in relation to PWM speed control signal at PWM pin.



Frequency of PWM speed control signal can be between 15Hz to 50kHz. Recommended typical PWM signal frequency is 25kHz to keep switching frequency away from audible band. If PWM signal level at PWM pin stays low for longer than 65ms typical, the outputs are disabled.

Depending on the motor design and its inertia, minimum start-up PWM duty required can be typically between 30% - 40% while minimum running PWM duty can be down to 20%-25% typical. If voltage at Vdd is lower than the nominal motor voltage, both start-up PWM duty and minimum running PWM duty required will be higher.

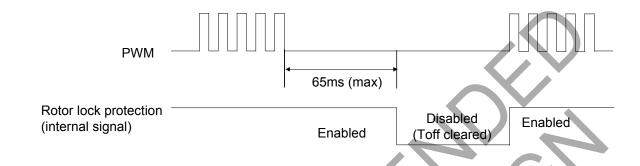


Lead-free Green AH5795 SINGLE PHASE HALL EFFECT LATCH FAN MOTOR DRIVER

Application Note (cont.)

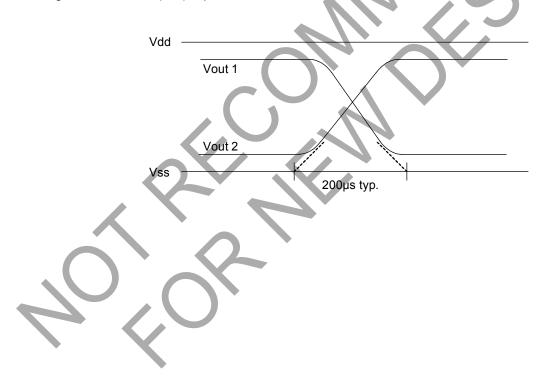
Rotor lock Toff and PWM signal

When PWM signal input at PWM pin is low for longer than 65ms, internal rotor lock protection Toff is cleared. This allows the device to enter motor start Ton time on the next PWM high signal



Soft Switching

AH5795 uses soft switching of the motor coil current during commutation for to minimize audible switching noise and electromagnetic interference (EMI) to provide a low noise solution.



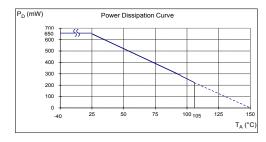




Thermal Performance Characteristics

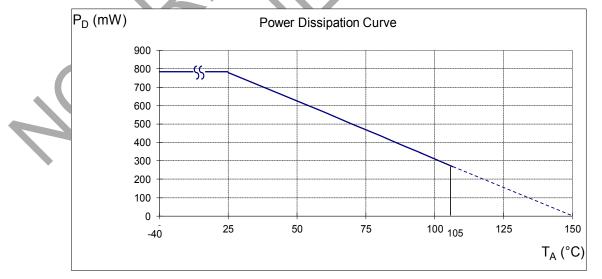
(1) TSOT23-6

T _A (°C)	25	50	60	70	75	80	85	90	95	100
P _D (mW)	651	521	469	417	391	365	339	313	286	260
T _A (°C)	105	110	115	120	125	130	135	140	145	150
P _D (mW)	234	208	182	156	130	104	78	52	26	0



(2) DFN2020C-6 (Note 11)

			-							
T _A (°C)	25	50	60	70	75	80	85	90	95	100
P _D (mW)	781	625	563	500	469	438	406	375	344	313
T _A (°C)	105	110	115	120	125	130	135	140	145	150
P _D (mW)	281	250	219	188	156	125	94	63	31	0

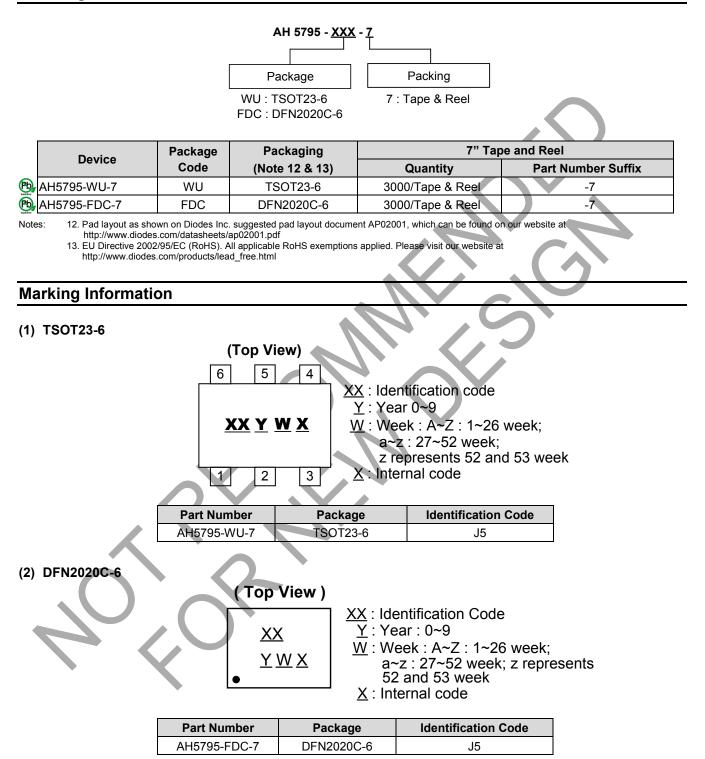


Notes: 11. DFN2020C-6 exposed pad soldered to minimum recommended landing pads (see Package Outline Dimension section) on a two-layer 2oz. copper FR4 PCB (1.6mm thickness) with no thermal vias in exposed PADs or any copper flood connecting to the landing pattern of the exposed pad.





Ordering information

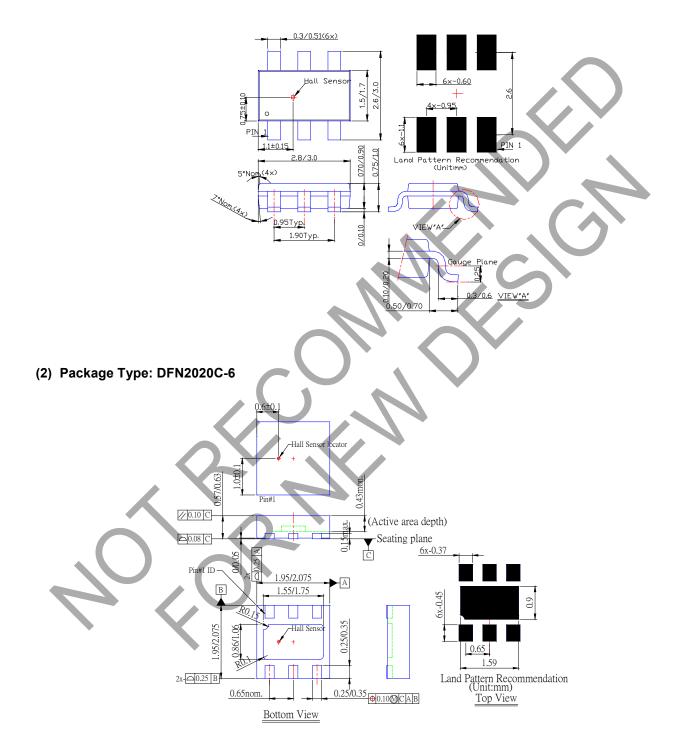






Package Outline Dimensions (All Dimensions in mm)

(1) Package type: TSOT23-6

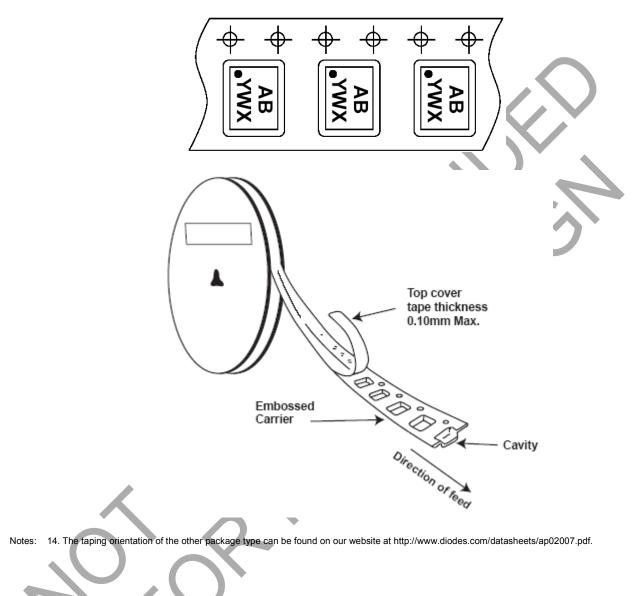






Taping Orientation

For DFN2020C-6







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