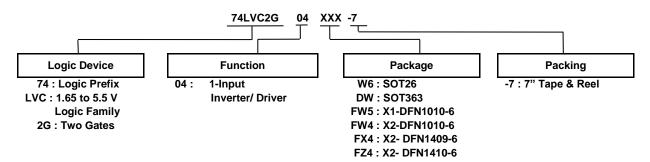


Ordering Information



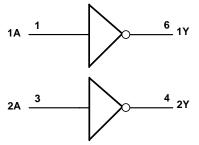
Device	Package	Package	Package	7" Tape and	Reel (Note 5)
Device	Code (Note 4) Size		Size	Quantity	Part Number Suffix
74LVC2G04W6-7	W6	SOT26	2.8mm X 2.2 mm X 1.1mm 0.95 mm lead pitch	3,000/Tape & Reel	-7
74LVC2G04DW-7	DW	SOT363	2.0mm X 2.0mm X 1.1mm 0.65 mm lead pitch	3,000/Tape & Reel	-7
74LVC2G04FW5-7	FW5	X1-DFN1010-6	1.0mm X 1.0mm X 0.5mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G04FW4-7	FW4	X2-DFN1010-6	1.0mm X 1.0mm X 0.4mm 0.35 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G04FX4-7	FX4	X2-DFN1409-6 Chip Scale Alternative	1.4mm X 0.9mm X 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7
74LVC2G04FZ4-7	FZ4	X2-DFN1410-6	1.4mm X 1.0mm X 0.4mm 0.5 mm pad pitch	5,000/Tape & Reel	-7

Notes:

Pin Descriptions

Pin Name	Pin.	Function	
1A	1	Data Input	
GND	2	Ground	
2A	3	Data Input	
2Y	4	Data Output	
Vcc	5	Supply Voltage	
1Y	6	Data Output	

Logic Diagram



Function Table

Inputs	Output
Α	Y
Н	L
L	Н

^{4.} Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

^{5.} The taping orientation is located on our website at http://www.diodes.com/datasheets/ap02007.pdf.



Absolute Maximum Ratings (Notes 6 & 7) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +6.5	V
VI	Input Voltage Range	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to +6.5	V
Vo	Voltage Applied to Output in High or Low State	-0.3 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
lok	Output Clamp Current V _O < 0	-50	mA
lo	Continuous Output Current	-50	mA
	Continuous Current Through V _{DD} or GND		mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Note

Recommended Operating Conditions (Note 8) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter		Max	Unit
V	Operating Voltage	Operating	1.65	5.5	V
V _{CC}	Operating voltage	Data Retention Only	1.5	_	V
		$V_{CC} = 1.65V \text{ to } 1.95V$		_	
\ /	High-Level Input Voltage	$V_{CC} = 2.3V$ to $2.7V$	1.7	_	V
V _{IH}	Ingri-Level input voltage	V_{CC} = 3V to 3.6V	2	_	V
		V_{CC} = 4.5V to 5.5 V	0.7 X V _{CC}	_	
		V _{CC} = 1.65V to 1.95V	_	0.35 X V _{CC}	
\/	Low Lovel Input Voltage	$V_{CC} = 2.3V$ to $2.7V$	_	0.7	٧
V_{IL}	Low-Level Input Voltage	$V_{CC} = 3V$ to 3.6V	_	0.8	V
		$V_{CC} = 4.5V$ to $5.5V$	_	0.3 X V _{CC}	
Vı	Input Voltage		0	5.5	V
Vo	Output Voltage		0	Vcc	V
		V _{CC} = 1.65V	_	-4	mA
		$V_{CC} = 2.3V$	_	-8	
I _{OH}	High-Level Output Current	urrent	_	-16	
		$V_{CC} = 3V$		-24	
		V _{CC} = 4.5V	_	-32	
		V _{CC} = 1.65V	_	4	
		V _{CC} = 2.3V	_	8	
I_{OL}	Low-Level Output Current	., 2,,	_	16	mA
		Vcc = 3V	_	24	
		$V_{CC} = 4.5V$	_	32	
		$V_{CC} = 1.8V \pm 0.15 \text{ V}, 2.5V \pm 0.2V$	_	20	
$\Delta t/\Delta V$	Input Transition Rise or Rall Rate	$V_{CC} = 3.3V \pm 0.3V$	_	10	ns/V
		$V_{CC} = 5V \pm 0.5V$	_	5	
T _A	Operating Free-Air Temperature		-40	125	°C

Note: 8. Unused inputs should be held at V_{CC} or Ground.

^{6.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{7.} Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.



Electrical Characteristics

Cumbal	Doromotor	Test Conditions	V	-40°C to	+85°C	-40°C to	+125°C	
Symbol	Parameter	rest Conditions	V _{CC}	Min	Max	Min	Max	Unit
		$I_{OH} = -100 \mu A$	1.65V to 5.5V	V _{CC} - 0.1	_	V _{CC} - 0.1	_	
		I _{OH} = -4mA	1.65V	1.2	_	0.95	_	
.,	High-Level Output	$I_{OH} = -8mA$	2.3V	1.9	_	1.7	_	V
Voн	Voltage	I _{OH} = -16mA	- 3V	2.4	_	1.9	_	V
		I _{OH} = -24mA	3 V	2.3	_	2.0	_	
		I _{OH} = -32mA	4.5V	3.8	_	3.4	_	
		I _{OL} = 100μA	1.65V to 5.5V	_	0.1	_	0.1	
		I _{OL} = 4mA	1.65V	_	0.45	_	0.70	
.,	Low-Level Output	I _{OL} = 8mA	2.3V	_	0.3	_	0.45	V
V _{OL}	Voltage	I _{OL} = 16mA	- 3V	_	0.4	_	0.60	v
		$I_{OL} = 24mA$	3 V	_	0.55	_	0.80	
		I _{OL} = 32mA	4.5V	_	0.55	_	0.80	
II	Input Current	V _I = 5.5V or GND	0 to 5.5V	_	± 5	_	± 20	μΑ
I _{OFF}	Power Down Leakage Current	V_I or $V_O = 5.5V$	0	_	± 10	_	± 20	μA
Icc	Supply Current	$V_I = 5.5V$ or GND $I_O = 0$	1.65V to 5.5V	_	10	_	40	μA
Δl _{CC}	Additional Supply Current	Input at V _{CC} -0.6V	3V to 5.5V	_	500	_	5000	μΑ

Package Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C.)

Symbol	Parameter	Package	Conditions	Min	Тур	Max	Unit
Cı	Input Capacitance	Typical of all packages	Vcc = 3.3V $V_1 = V_{CC}$ or GND	_	3.5	_	pF
		SOT26		_	204		
		SOT363		_	371	_	
	Thermal Resistance	X2-DFN1410-6	(Nata 0)	_	430	_	0000
θ_{JA}	Junction-to-Ambient	X2-DFN1409-6	(Note 9)	_	450	_	°C/W
		X1-DFN1010-6		_	495	_	
		X2-DFN1010-6		_	510	_	
		SOT26		_	52	_	
		SOT363		_	143	_	
	Thermal Resistance	X2-DFN1410-6	(Nata 0)	_	190	_	0000
θ_{JC}	Junction-to-Case	X2-DFN1409-6	(Note 9)	_	225	_	°C/W
		X1-DFN1010-6		_	245	_	
		X2-DFN1010-6		_	250	_	

Note: 9. Test condition for all packages: Device mounted on FR-4 substrate PC board, 2oz copper with minimum recommended pad layout.

Switching Characteristics

 $T_A = -40$ °C to +85°C, $C_L = 30$ or 50pF (See Figure 1)

Parameter	From (Input)	TO (OUTPUT)	V _{CC} = ± 0.	: 1.8V .15V	V _{CC} = ± 0	: 2.5V .2V		: 3.3V).3V	V _{CC} ± 0	= 5V .5V	Unit
	(mpat)	(0011 01)	Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	Α	Υ	0.5	8.0	1.0	4.4	0.5	4.1	0.5	3.2	ns

 $T_A = -40$ °C to +125°C, $C_L = 30$ or 50pF (See Figure 1)

Parameter	From (Input)	TO (OUTPUT)		= 1.8V .15V	V _{CC} = ± 0	: 2.5V).2V	V _{CC} = ± 0	: 3.3V :3V		= 5V).5V	Unit
	(IIIput)	(001701)	Min	Max	Min	Max	Min	Max	Min	Max	
t _{pd}	А	Υ	0.5	9.5	0.5	5.4	0.5	5.5	0.5	3.8	ns

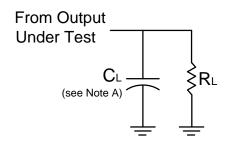


Operating Characteristics

 $T_A = +25$ °C

	Parameter	Test Conditions	V _{CC} = 1.8V Typ	V _{CC} = 2.5V Typ	V _{CC} = 3.3V Typ	V _{CC} = 5V Typ	Unit
C _{pd}	Power Dissipation Capacitance	f = 10MHz	17	19	20	21	pF

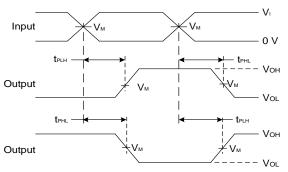
Parameter Measurement Information



V	Inp	uts	V		9	
V _{CC}	VI	t _r /t _f	V _M	C _L	R _L	
1.8V±0.15V	Vcc	≤2ns	V _{CC} /2	30 pF	1 kΩ	
2.5V±0.2V	V _{CC}	≤2ns	V _{CC} /2	30 pF	500 Ω	
3.3V±0.3V	3V	≤2.5ns	1.5 V	50 pF	500 Ω	
5V±0.5V	V _{CC}	≤2.5ns	V _{CC} /2	50 pF	500 Ω	







Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1 Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 10 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD} .



Marking Information

(1) SOT26, SOT363

6 5 4

XX Y W X

XX: Identification code

Y: Year 0~9

<u>W</u>: Week: A~Z: 1~26 week;

a~z: 27~52 week; z represents

52 and 53 week

X: A~Z: Internal Code

Part Number	Package	Identification Code
74LVC2G04W6-7	SOT26	Z2
74LVC2G04DW-7	SOT363	Z2

(2) X1-DFN1010-6, X2-DFN1010-6, X2-DFN1409-6, X2-DFN1410-6

(Top View)

XX: Identification Code

Ÿ : Year : 0~9

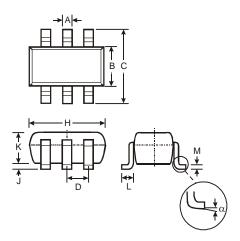
W: Week: A~Z: 1~26 week; a~z: 27~52 week; z represents

52 and 53 week X: A~Z: Internal code

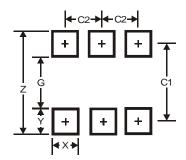
Part Number	Package	Identification Code
74LVC2G04FW4-7	X2-DFN1010-6	Z2
74LVC2G04FW5-7	X1-DFN1010-6	W2
74LVC2G04FX4-7	X2-DFN1409-6	X2
74LVC2G04FZ4-7	X2-DFN1410-6	Z2



SOT26 Package Outline Dimensions and Suggested Pad Layout



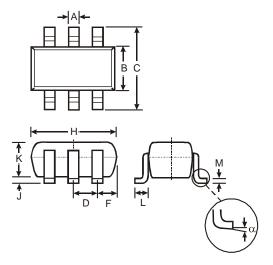
	SOT26				
Dim	Min	Max	Тур		
Α	0.35	0.50	0.38		
В	1.50	1.70	1.60		
С	2.70	3.00	2.80		
D	_	_	0.95		
Н	2.90	3.10	3.00		
J	0.013	0.10	0.05		
K	1.00	1.30	1.10		
L	0.35	0.55	0.40		
M	0.10	0.20	0.15		
α	0°	8°	_		
All Dimensions in mm					



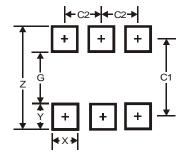
Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Υ	0.80
C1	2.40
C2	0.95



SOT363 Package Outline Dimensions and Suggested Pad Layout



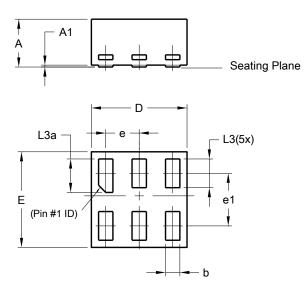
	SOT363				
Dim	Min	Max	Тур		
Α	0.10	0.30	0.25		
В	1.15	1.35	1.30		
С	2.00	2.20	2.10		
D		0.65 Ty	р		
F	0.40	0.45	0.425		
Н	1.80	2.20	2.15		
J	0	0.10	0.05		
K	0.90	1.00	1.00		
L	0.25	0.40	0.30		
М	0.10	0.22	0.11		
α	0°	8°	-		
All Dimensions in mm					



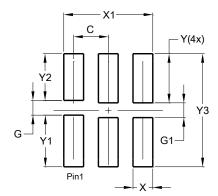
Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Y	0.6
C1	1.9
C2	0.65



X1-DFN1010-6 (Type B) Package Outline Dimensions and Suggested Pad Layout



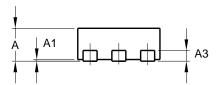
	X1-DFN1010-6 (Type B)				
Dim	Min	Min Max Typ			
Α	-	0.50	0.39		
A1	-	0.04	-		
b	0.12	0.20	0.15		
D	0.95	1.050	1.00		
Е	0.95	1.050	1.00		
e 0.35 BSC					
e1	0.55 BSC				
L3	0.27	0.30	0.30		
L3a	0.32	0.40	0.35		
All Dimensions in mm					

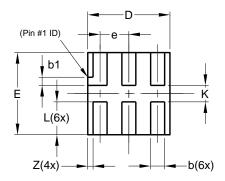


Dimensions	Value (in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Υ	0.500
Y1	0.525
Y2	0.475
Y3	1.150

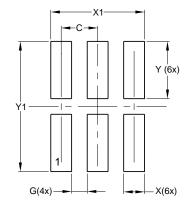


X2-DFN1010-6 Package Outline Dimensions and Suggested Pad Layout





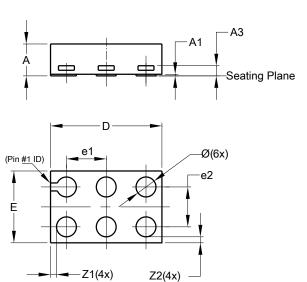
	X2-DFN1010-6				
Dim	Min Max Typ				
Α		0.40	0.39		
A1	0.00	0.05	0.02		
А3	_		0.13		
b	0.14	0.20	0.17		
b1	0.05	0.15	0.10		
D	0.95	1.05	1.00		
Е	0.95	1.05	1.00		
е	_	_	0.35		
L	0.35	0.45	0.40		
K	0.15	_	_		
Z			0.065		
All Dimensions in mm					



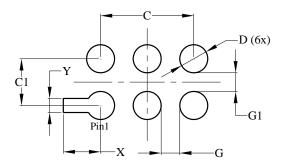
Dimensions	Value (in mm)	
С	0.350	
G	0.150	
Х	0.200	
X1	0.900	
Υ	0.550	
Y1	1.250	



X2-DFN1409-6 Package Outline Dimensions and Suggested Pad Layout



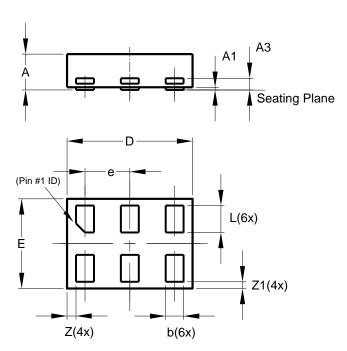
)	X2-DFN1409-6			
Dim	Min	Max	Тур	
Α		0.40	0.39	
A1	0	0.05	0.02	
А3	_		0.13	
Ø	0.20	0.30	0.25	
D	1.35	1.45	1.40	
Е	0.85	0.95	0.90	
e1		_	0.50	
e2			0.50	
Z 1	_		0.075	
Z2	_		0.075	
All Dimensions in mm				



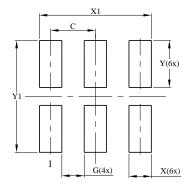
Dimensions	Value	
Dilliensions	(in mm)	
С	1.000	
C1	0.500	
D	0.300	
G	0.200	
G1	0.200	
Х	0.400	
Y	0.150	



X2-DFN1410-6 Package Outline Dimensions and Suggested Pad Layout



X2-DFN1410-6			
Dim	Min	Max	Тур
Α	_	0.40	0.39
A1	0.00	0.05	0.02
A3	_	_	0.13
b	0.15	0.25	0.20
D	1.35	1.45	1.40
Е	0.95	1.05	1.00
е	_	-	0.50
L	0.25	0.35	0.30
Z	_		0.10
Z 1	0.045	0.105	0.075
All Dimensions in mm			



Dimensions	Value (in mm)
С	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250



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