43CTQ100PbF

Vishay High Power Products Schottky Rectifier, 2 x 20 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS		
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _{.1} = 25 °C	0.81	V	
		40 A	1j=25 C	0.98		
		20 A	T 105 00	0.67		
		40 A	T _J = 125 °C	0.81		
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V Dated V	1	mA	
See fig. 2	IRM ('')	T _J = 125 °C	V _R = Rated V _R	11		
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.71	V	
Forward slope resistance	r _t			0.43	mΩ	
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1480	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/μs		

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and stora temperature range	ge	T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance junction to case per leg) ,	D	DC eneration	2.0	°C/W	
Maximum thermal resistance junction to case per package	•	R_{thJC}	DC operation	1.0		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque -	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf \cdot in)	
Marking daving			Coop at do TO 200AP	43CT	43CTQ080	
Marking device			Case style TO-220AB	43CT	43CTQ100	

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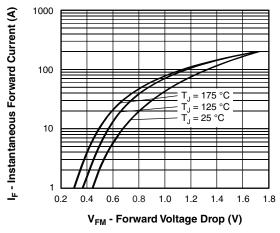


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

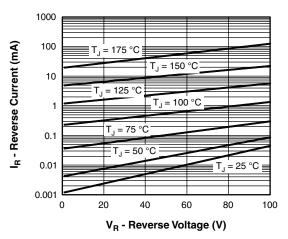


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

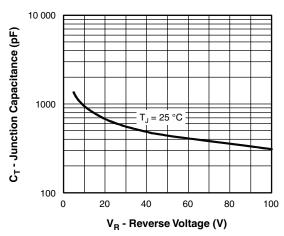


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

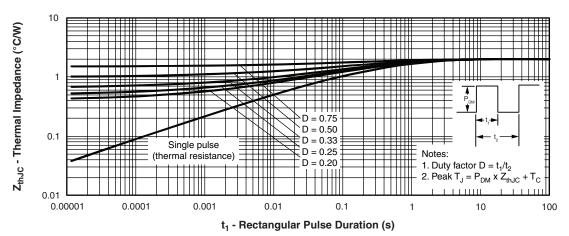


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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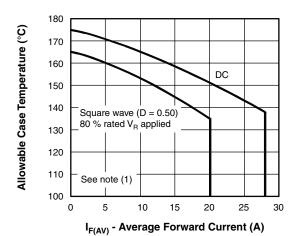


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

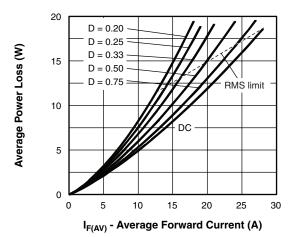


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

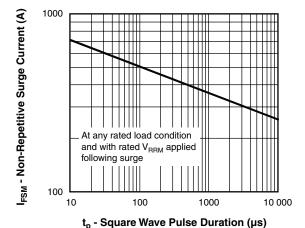


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

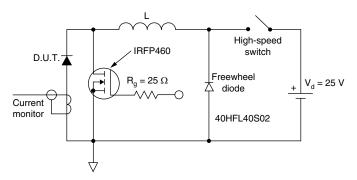


Fig. 8 - Unclamped Inductive Test Circuit

Note

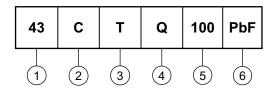
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ORDERING INFORMATION TABLE

Device code



- 1 Current rating (40 A)
- 2 Circuit configuration:

C = Common cathode

3 - Package:

T = TO-220

- 4 Schottky "Q" series
- 5 Voltage rating (100 = 100 V)
- 6 None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95222			
Part marking information	www.vishay.com/doc?95225			
SPICE model	www.vishay.com/doc?95065			

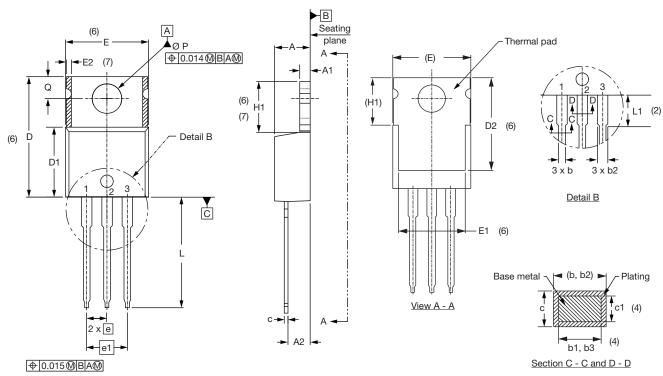
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Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches



Lead assignments

<u>Diodes</u>

- 1. Anode/open
- 2. Cathode
- 3. Anode

Conforms to JEDEC outline TO-220AB

SYMBOL	MILLIMETERS		INCHES		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° to 93°		90° to 93°		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, except A2 (maximum) and D2 (minimum) where dimensions are derived from the actual package outline

Lead tip

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