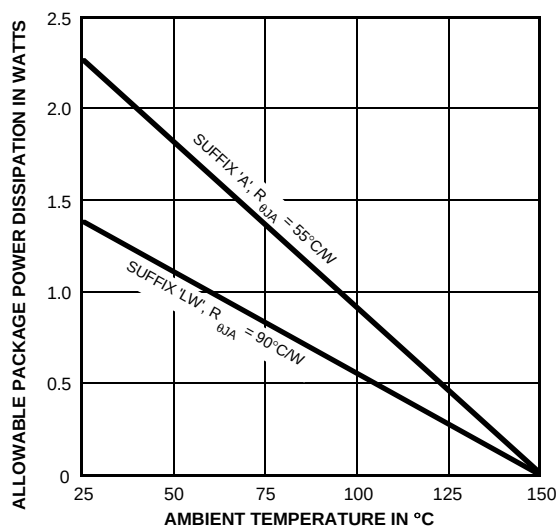
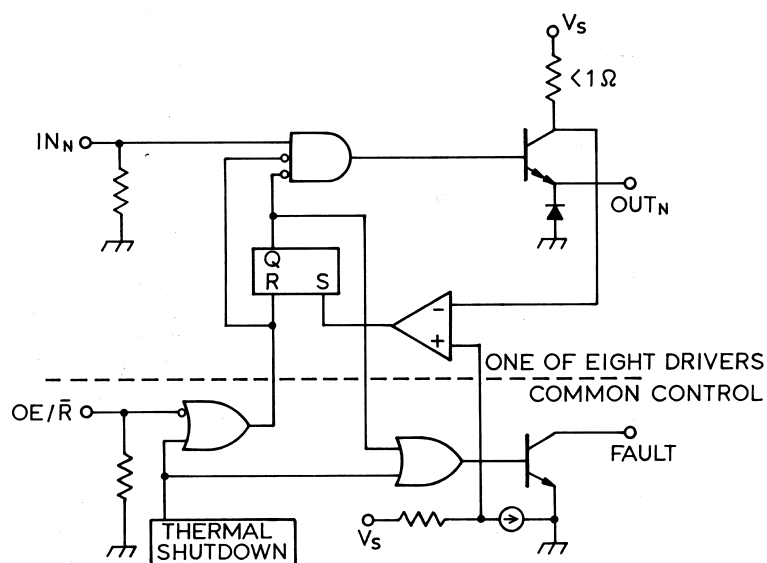


2987 8-CHANNEL SOURCE DRIVER



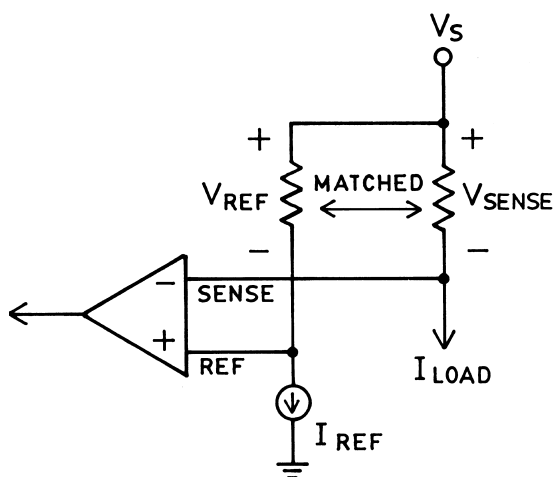
Dwg. GS-004B

FUNCTIONAL BLOCK DIAGRAM



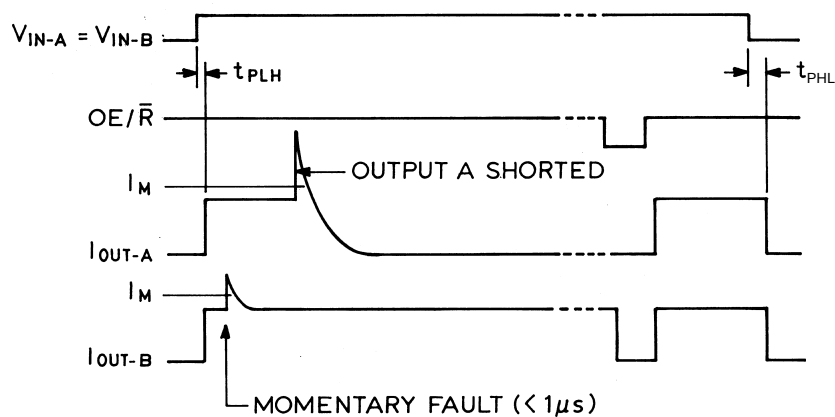
Dwg. No. A-13,286

OVER-CURRENT FAULT SENSE



Dwg. No. A-13,292

OUTPUT CURRENT WAVESHAPES



Dwg. No. A-13,293

2987

8-CHANNEL

SOURCE DRIVER

ELECTRICAL CHARACTERISTICS at $T_A = 25^\circ\text{C}$, $V_{OE} = 2.4\text{ V}$, $V_S = 35\text{ V}$ (unless otherwise noted).

Characteristic	Symbol	Test Conditions	Limits			
			Min.	Typ.	Max.	Units
Functional Supply Range	V_S		7.0	—	35	V
Output Leakage Current	I_{CEX}	$V_{IN} = 0.4\text{ V}^*$	—	<-5.0	-200	μA
Output Sustaining Voltage	$V_{OUT(SUS)}$	$I_{OUT} = -350\text{ mA}$, $L = 2.0\text{ mH}$	35	—	—	V
Output Saturation Voltage	$V_{OUT(SAT)}$	$V_{IN} = 2.4\text{ V}$, $I_{OUT} = -100\text{ mA}$	—	1.6	1.8	V
		$V_{IN} = 2.4\text{ V}$, $I_{OUT} = -225\text{ mA}$	—	1.7	1.9	V
		$V_{IN} = 2.4\text{ V}$, $I_{OUT} = -350\text{ mA}$	—	1.8	2.0	V
Channel Shutdown Threshold	I_M	$V_{IN} = 2.4\text{ V}$	-370	-500	—	mA
FAULT Leakage Current	I_{CEX}	$V_{CC} = 35\text{ V}$	—	<1.0	100	μA
FAULT Saturation Voltage	$V_{CE(SAT)}$	$I_C = 30\text{ mA}$	—	0.3	0.8	V
Input Voltage	$V_{IN(ON)}$		2.4	—	—	V
	$V_{IN(OFF)}$		—	—	0.4	V
Input Current	$I_{IN(ON)}$	$V_{IN} = 2.4\text{ V}$	—	125	170	μA
		$V_{IN} = 5.0\text{ V}$	—	840	1020	μA
		$V_{IN} = 12\text{ V}$	—	1500	1800	μA
	$I_{IN(OFF)}$	$V_{IN} = 0.4\text{ V}$	—	—	15	μA
Clamp Diode Leakage Current	I_R	$V_R = 35\text{ V}$, $T_A = 70^\circ\text{C}$	—	—	50	μA
Clamp Diode Forward Voltage	V_F	$I_F = 350\text{ mA}$	—	1.5	1.8	V
Supply Current	$I_{S(ON)}$	$V_{IN} = 2.4\text{ V}^*$, Outputs Open	—	13	18	mA
	$I_{S(OFF)}$	$V_{IN} = 0.4\text{ V}^*$	—	8.0	12	mA
Thermal Shutdown	T_J		—	165	—	$^\circ\text{C}$
Thermal Hysteresis	ΔT_J		—	15	—	$^\circ\text{C}$
Propagation Delay Time	t_{PLH}	$R_L = 100\Omega$	—	0.3	0.6	μs
	t_{PHL}	$R_L = 100\Omega$	—	2.0	4.0	μs
Dead Time	t_d		—	1.0	—	μs

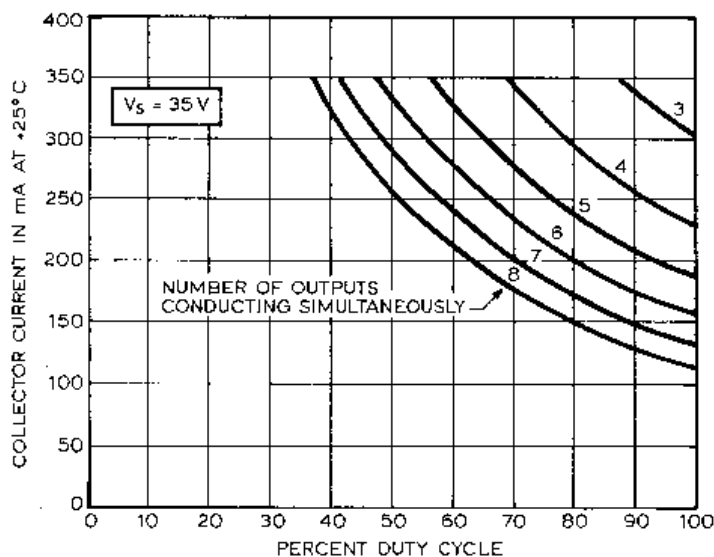
*All inputs simultaneously.

2987 8-CHANNEL SOURCE DRIVER

ALLOWABLE OUTPUT CURRENT AS A FUNCTION OF DUTY CYCLE

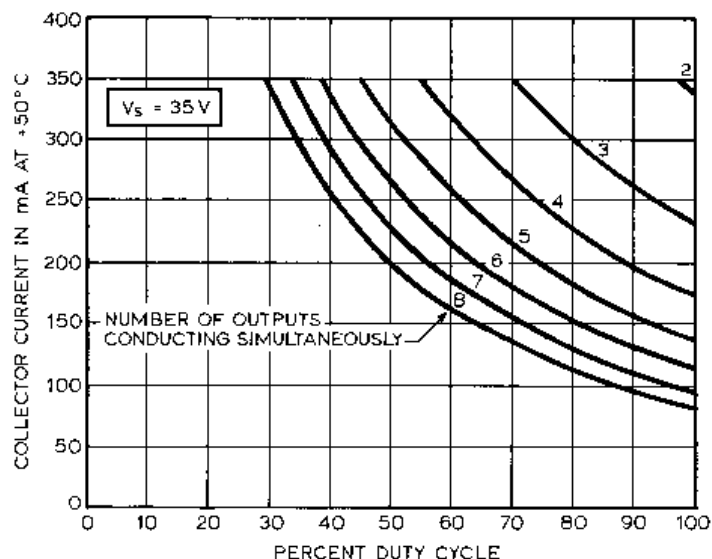
(UDN2987A shown, multiply by 78% for UDN2987LW)

At +25°C



Dwg. No. A-13,288

At +50°C



Dwg. No. A-13,289

APPLICATIONS INFORMATION AND CIRCUIT DESCRIPTION

As with all power integrated circuits, the UDN2987A and UDN2987LW have a maximum allowable output current rating. The 500 mA rating does not imply that operation at that value is permitted or even obtainable. The channel output current trip point is specified as -370 mA, minimum; therefore, attempted operation at current levels greater than -370 mA may cause a fault indication and channel shutdown. The device is tested at a maximum of -350 mA and that is the recommended maximum output current per driver. It provides protection for current overloads or shorted loads up to 35 V.

All outputs are enabled by pulling the OE/R input high. When OE/R is low or allowed to float (internal pull-down), all outputs are inhibited and the latches are reset. Note that the RESET pulse duration (OE/R low) should be at least 1 μ s. This will ensure safe operation under attempted RESET conditions with a shorted load. The latches are also reset during power up, regardless of the state of the OE/R input.

The load current causes a small voltage drop across the internal low-value sense resistor. This voltage is compared to the voltage drop across a reference resistor with a constant current. The two resistors are matched to eliminate errors due to manufacturing tolerances or temperature effects. Each channel includes a comparator and its own latch. An over-current fault ($V_{SENSE} > V_{REF}$) will set the affected latch and shut down only that channel. All other channels will continue to operate normally. The latch includes a 1 μ s delay (t_d) to prevent unwanted triggering due to crossover currents generated when switching inductive loads. For an abrupt short circuit, the delay and output switching times will allow a brief, permissible current in excess of the trip current before the output driver is turned OFF.

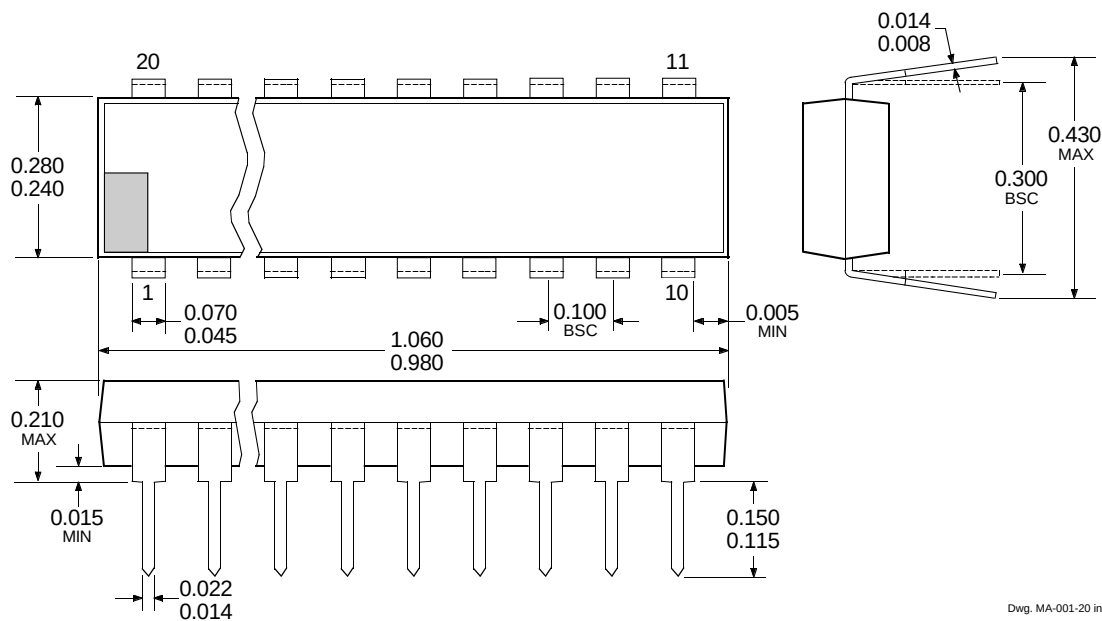
A common thermal shutdown disables all outputs if the chip temperature exceeds +165°C. At thermal shutdown, all latches are reset. The outputs are disabled until the chip cools down to about +150°C (thermal hysteresis).

A common open-collector FAULT output is used to indicate any channel over-current condition or chip thermal shutdown.

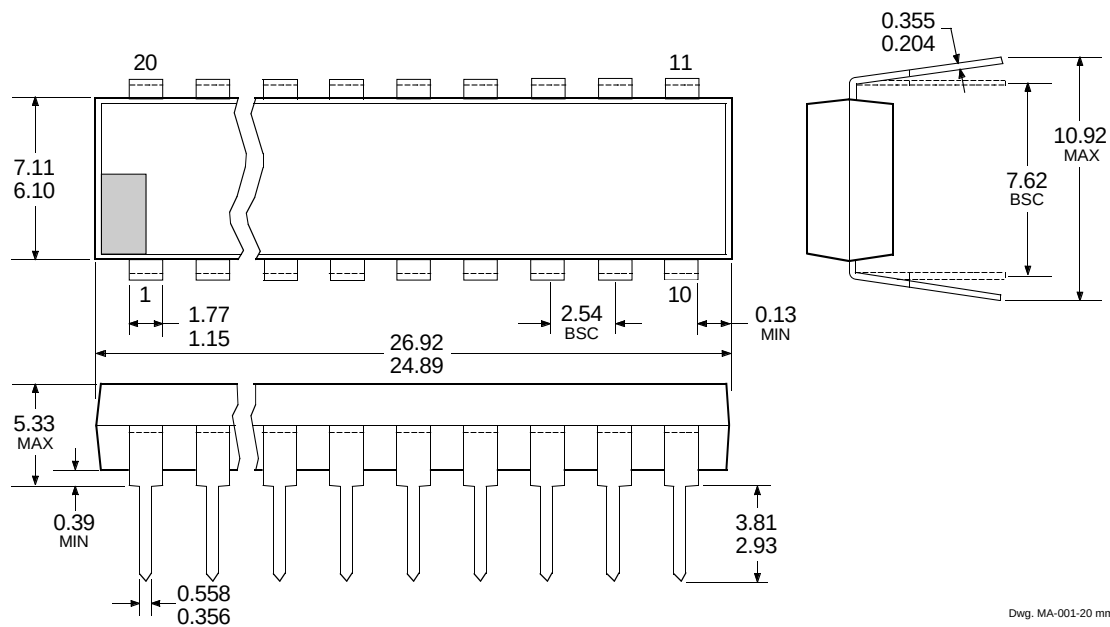
2987 8-CHANNEL SOURCE DRIVER

UDN2987A

Dimensions in Inches
(controlling dimensions)



Dimensions in Millimeters
(for reference only)



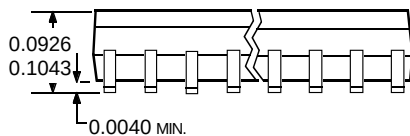
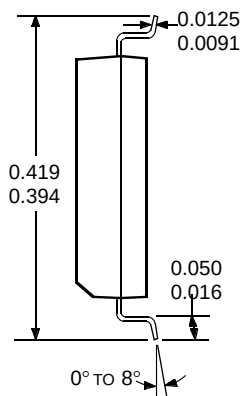
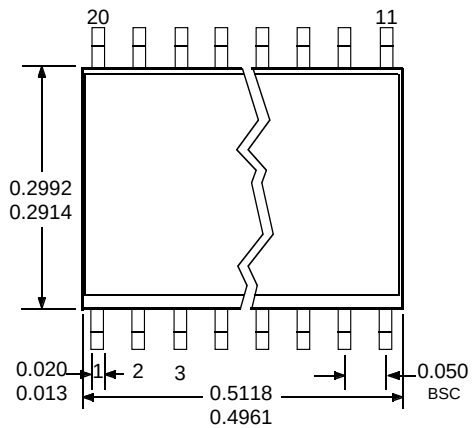
- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Lead spacing tolerance is non-cumulative.
3. Lead thickness is measured at seating plane or below.

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8-CHANNEL SOURCE DRIVER

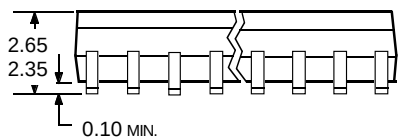
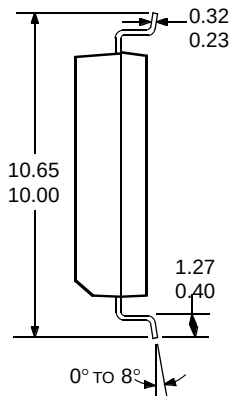
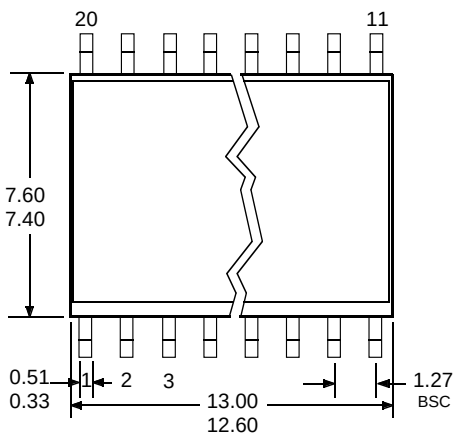
UDN2987LW

Dimensions in Inches
(for reference only)



Dwg. MA-008-20 in

Dimensions in Millimeters
(controlling dimensions)



Dwg. MA-008-20 mm

- NOTES: 1. Exact body and lead configuration at vendor's option within limits shown.
2. Lead spacing tolerance is non-cumulative.

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2987 8-CHANNEL SOURCE DRIVER

POWER SOURCE DRIVERS

IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

Output Ratings *			Features					Part Number [†]
mA	V	#	Serial Input	Latched Drivers	Diode Clamp	Saturated Outputs	Internal Protection	
-25	60	8	—	X	—	—	—	5815
	60	10	X	X	active pull-down	—	—	5810-F and 6810
	60	12	X	X	active pull-down	—	—	5811
	60	20	X	X	active pull-down	—	—	5812-F and 6812
	60	32	X	X	active pull-down	—	—	5818-F and 6818
	85	8	—	—	—	—	—	6118
-120	-25	8	—	—	X	X	—	2585
	30	8	—	—	X	X	—	2985
	50	8	X	X	X	X	—	5895
-350	35	8	—	—	X	—	X	2987
	50	8	—	—	X	—	—	2981 and 2982
	50	8	X	X	X	—	—	5891
	-50	8	—	—	X	—	—	2580
	80	8	—	—	X	—	—	2983
	80	8	X	X	X	—	—	5890
	-80	8	—	—	X	—	—	2588
-500	6	1	—	—	—	MOSFET	X	2525 and 2535
	6	2	—	—	—	MOSFET	X	2535 and 2536
-4000	60	4	—	—	X	—	—	2944

* Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

† Complete part number includes additional characters to indicate operating temperature range and package style.

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