

Figure 1. Block Diagram

Table 1. PIN DESCRIPTION

Pin#	Pin Name	Туре	Description
1	CLKIN	Input	External Reference Input frequency.
2	VSS	Power	Ground to entire chip.
3	ModOUT	Output	Modulated Frequency Output.
4	SS1%	Input	Spread Deviation Selection Pin -1. Refer to <i>Spread Deviation Selection Table</i> for details. Has an Internal pull-up resistor.
5	SS2%	Input	Spread Deviation Selection Pin -2. Refer to <i>Spread Deviation Selection Table</i> for details. Has an Internal pull-up resistor.
6	VDD	Power	Power to entire chip.

Table 2. SPREAD DEVIATION SELECTION TABLE

SS2%	SS1%	Frequency Deviation
L	L	±1.50%
L	Н	±1.25%
Н	L	±0.75%
Н	Н	±1.00%

Table 3. MODRATE TABLE

ModRate	20 MHz – 40 MHz	40 MHz – 130 MHz	
	Fin / 336	Fin / 840	

Table 4. ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V_{DD}, V_{IN}	Voltage on any pin with respect to Ground	-0.5 to +4.6	V
T _{STG}	Storage temperature	-65 to +125	°C
Ts	Max. Soldering Temperature (10 sec)	260	°C
TJ	Junction Temperature	150	°C
T _{DV}	Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 5. OPERATING CONDITIONS FOR 2.5 V AND 3.3 V SUPPLY VOLTAGE

Parameter	Description	Min	Max	Unit
V _{DD(2.5)}	Supply Voltage	2.375	2.625	V
V _{DD(3.3)}		3.0	3.6	
T _A	Operating Temperature (Ambient Temperature)	-40	+85	°C
C _L	Load Capacitance		15	pF

Table 6. DC ELECTRICAL CHARACTERISTICS FOR 2.5 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3		0.7	V
V _{IH}	Input high voltage	1.7		VDD + 0.3	V
I _{IL}	Input low current			-35	μΑ
I _{IH}	Input high current			35	μΑ
V _{OL}	Output low voltage (VDD = 2.5 V, I _{OL} = 8 mA)			0.6	V
V _{OH}	Output high voltage (VDD = 2.5 V, I _{OH} = -8 mA)	1.8			V
I _{DD}	Static supply current (Note 1)			4	mA
Icc	Dynamic supply current (2.5 V and no load)		11		mA
V_{DD}	Operating voltage	2.375	2.5	2.625	V
t _{ON}	Power-up time (first locked cycle after power-up)			5	mS
C _{IN}	Input Capacitance		5		pF
Z _{OUT}	Output Impedance		40		Ω

^{1.} CLKIN pin is pulled low.

Table 7. AC ELECTRICAL CHARACTERISTICS FOR 2.5 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency	30		130	MHz
ModOUT	Output frequency	30		130	MHz
t _{LH} (Note 2)	Output rise time (measured from 0.7 V to 1.7 V)		2.2		nS
t _{HL} (Note 2)	Output fall time (measured from 1.7 V to 0.7 V)		1.2		nS
t _{JC}	Jitter (Cycle-to-cycle)		±250		pS
t _D	Output duty cycle	40	50	60	%

^{2.} t_{LH} and t_{HL} are measured into a capacitive load of 15 pF.

Table 8. DC ELECTRICAL CHARACTERISTICS FOR 3.3 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
V _{IL}	Input low voltage	VSS - 0.3		0.8	V
V _{IH}	Input high voltage	2.0		VDD + 0.3	V
I _{IL}	Input low current			-35	μΑ
I _{IH}	Input high current			35	μΑ
V _{OL}	Output low voltage (VDD = 3.3 V, I _{OL} = 8 mA)			0.4	V
V _{OH}	Output high voltage (VDD = 3.3 V, I _{OH} = -8 mA)	2.5			V
I _{DD}	Static supply current (Note 3)			4.5	mA
Icc	Dynamic supply current (3.3 V and no load)		14		mA
V_{DD}	Operating voltage	3.0	3.3	3.6	V
t _{ON}	Power-up time (first locked cycle after power-up)			5	mS
C _{IN}	Input Capacitance		5		pF
Z _{OUT}	Output Impedance		40		Ω

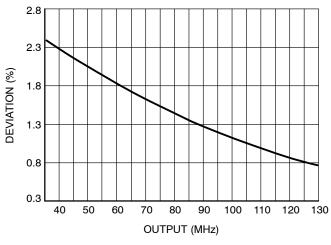
^{3.} CLKIN pin is pulled low.

Table 9. AC ELECTRICAL CHARACTERISTICS FOR 3.3 V SUPPLY

Symbol	Parameter	Min	Тур	Max	Unit
CLKIN	Input frequency	20		130	MHz
ModOUT	Output frequency	20		130	MHz
t _{LH} (Note 4)	Output rise time (measured from 0.8 V to 2.0 V)		1.5		nS
t _{HL} (Note 4)	Output fall time (measured at 2.0 V to 0.8 V)		1.1		nS
t _{JC}	Jitter (Cycle-to-cycle)		±225		pS
t _D	Output duty cycle	45	50	55	%

^{4.} t_{LH} and t_{HL} are measured into a capacitive load of 15 pF.

DEVIATION CHARTS



2.5 2.0 **DEVIATION** (%) 1.5 1.0 0.5 20 21 22 23 24 25 26 27 29 OUTPUT (MHz)

Figure 2. Deviation vs. Output Frequency (35 MHz to 130 MHz) at 25° C for VDD = 2.5 V

Figure 3. Deviation vs. Output Frequency (20 MHz to 29 MHz) at 25°C for VDD = 3.3 V

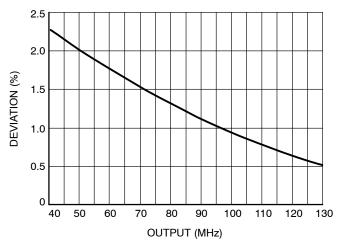
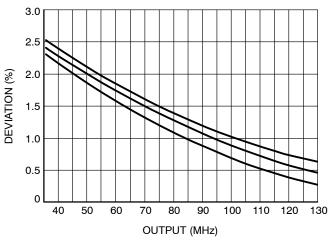


Figure 4. Deviation vs. Output Frequency (40 MHz to 130 MHz) at 25°C for VDD = 3.3 V

NOTE: Transition band is 30 MHz to 34 MHz for VDD = 2.5 V at 25° C. Deviation in this band is $2.64\% \pm 3\%$. Transition band is 30 MHz to 39 MHz for VDD = 3.3 V at 25° C. Deviation in this band is $1.67\% \pm 45\%$.

DEVIATION CHARTS



2.5 2.7 V (-40°C) 2.5 V (25°C) 2.0 DEVIATION (%) 2.37 V (85°C) 1.5 1.0 0.5 n 25 20 21 22 23 24 26 27 28 29 OUTPUT (MHz)

Figure 5. Deviation vs. Output Frequency (35 MHz to 130 MHz) across Temperature for VDD = $2.5 \text{ V} \pm 5\%$

Figure 6. Deviation vs. Output Frequency (20 MHz to 29 MHz) across Temperature for $VDD = 3.3 V \pm 0.3 V$

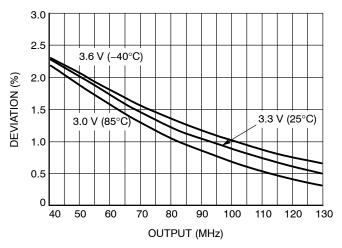
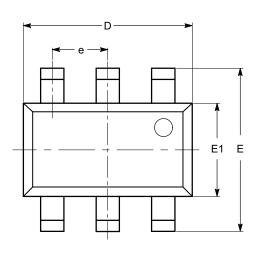


Figure 7. Deviation vs. Output Frequency (40 MHz to 130 MHz) across Temperature for VDD = 3.3 V \pm 0.3 V

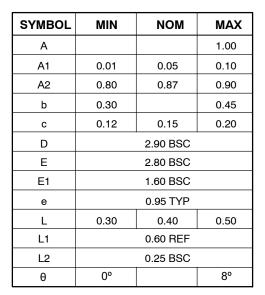
NOTE: Transition band is 30 MHz to 34 MHz for VDD = $2.5 \text{ V} \pm 5\%$, across -40°C to $+85^{\circ}\text{C}$. Deviation in this band is $1.8\% \pm 53\%$. Transition band is 30 MHz to 39 MHz for VDD = $3.3 \text{ V} \pm 0.3 \text{ V}$, across -40°C to $+85^{\circ}\text{C}$. Deviation in this band is $1.67\% \pm 60\%$.

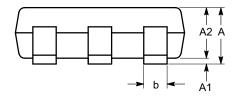
PACKAGE DIMENSIONS

TSOT-23, 6 LEAD CASE 419AF-01 ISSUE O

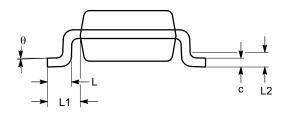


ТОР	VIEW





SIDE VIEW



END VIEW

Notes:

- (1) All dimensions are in millimeters. Angles in degrees.(2) Complies with JEDEC MO-193.

Table 10. ORDERING INFORMATION

Part Number	Marking	Package Type	Temperature
PCS3I6100AG-06JR	AB2	6L-TSOT 23 (6L-TSOT 26), TAPE & REEL, Green	-40°C to +85°C

NOTE: A "microdot" placed at the end of last row of marking or just below the last row toward the center of package indicates Pb-free.

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